



## MIC69301/2/3

Single Supply  $V_{IN}$ , Low  $V_{IN}$ , Low  $V_{OUT}$ , 3A  
LDO

### General Description

The MIC69301/2/3 is the 3A output current member of the MIC69xxx family of high current, low voltage regulators, which support currents of 1A, 1.5A, 3A, and 5A. This family operates from a single low voltage supply, yet offers high precision and ultra low dropout of 500mV under worst case conditions.

The MIC69301/2/3 operates from an input voltage of 1.65V to 5.5V. It is designed to drive digital circuits requiring low voltage at high currents (i.e., PLDs, DSP, microcontroller, etc.). These regulators are available in adjustable and fixed output voltages. The adjustable version can support output voltages down to 0.5V.

The  $\mu$ Cap design of the MIC69301/2/3 is optimized for stability with low value low-ESR ceramic output capacitors. Features of the MIC69301/2/3 include thermal shutdown and current limit protection. Logic enable and error flag pins are also available.

The MIC69301/2/3 is offered in TO-263, S-PAK and the EPAD SOIC-8 packages. All packages have an operating temperature range of  $-40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ .

Datasheets and support documentation can be found on Micrel's web site at: [www.micrel.com](http://www.micrel.com).

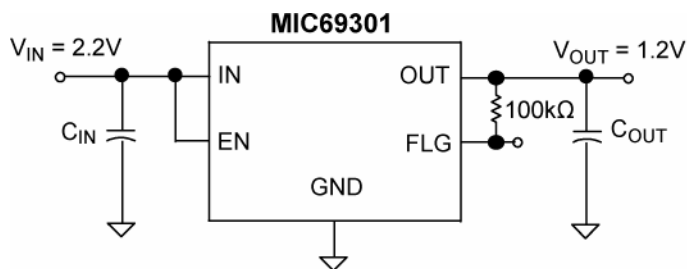
### Features

- Input voltage range:  $V_{IN}$ : 1.65V to 5.5V
- Maximum dropout ( $V_{IN} - V_{OUT}$ ) of 500mV over temperature
- Adjustable output voltage down to 0.5V
- Stable with 10 $\mu\text{F}$  ceramic output capacitor
- Excellent line and load regulation
- Logic controlled shutdown
- Thermal shutdown and current-limit protection
- 5-Pin TO-263
- 5-Pin S-Pak package
- EPAD SOIC-8 package
- $-40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$  junction temperature range

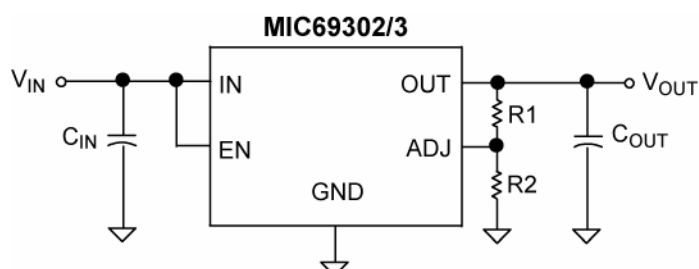
### Applications

- Point-of-load applications
- Industrial power
- Sensitive RF applications

### Typical Application



Fixed 1.2V Regulator w/Error Flag Output

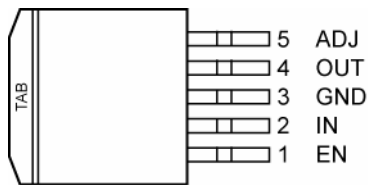


Adjustable Regulator

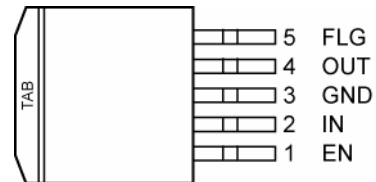
## Ordering Information

Part Number	Nominal Output Voltage	Junction Temperature Range	Package	Lead Finish
MIC69301-1.2YME	1.2V	-40° to +125°C	8-Pin e-Pad SOIC	Pb Free
MIC69301-1.2WR	1.2V	-40° to +125°C	5-Pin S-PAK	RoHS Compliant
MIC69301-1.2WU	1.2V	-40° to +125°C	5-Pin TO-263	RoHS Compliant
MIC69302WR	Adj.	-40° to +125°C	5-Pin S-PAK	RoHS Compliant
MIC69302WU	Adj.	-40° to +125°C	5-Pin TO-263	RoHS Compliant
MIC69303YME	Adj.	-40° to +125°C	8-Pin e-Pad SOIC	Pb Free

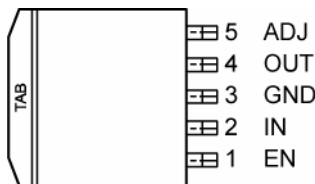
## Pin Configuration



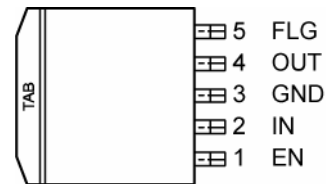
**5-Pin TO-263 (U)**  
**(Adjustable Voltage)**



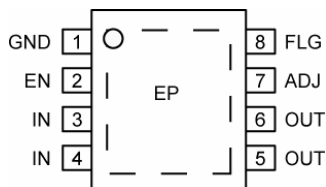
**5-Pin TO-263 (U)**  
**(Fixed Voltage with Flag)**



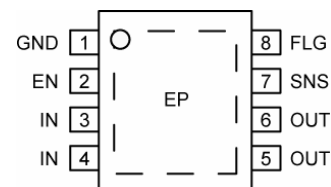
**5-Pin S-PAK (R)**  
**(Adjustable Voltage)**



**5-Pin S-PAK (R)**  
**(Fixed Voltage with Flag)**



**8-Pin e-Pad SOIC (ME)**  
**(Adjustable Voltage)**



**8-Pin e-Pad SOIC (ME)**  
**(Fixed Voltage with Flag)**

## Pin Description

Pin Number (Fixed) SPAK-5 TO-263-5	Pin Number (Adjustable) SPAK-5 TO-263-5	Pin Number (Fixed) e-Pad SOIC-8	Pin Number (Adjustable) e-Pad SOIC-8	Pin Name	Pin Function
1	1	2	2	EN	Enable (Input): CMOS compatible input. Logic high = enable, logic low = shutdown. Do not leave pin floating.
2	2	3,4	3,4	IN	Input voltage which supplies current to the output power device.
3, TAB	3, TAB	1	1	GND	Ground (TAB is connected to ground on S-Pak and TO-263).
4	4	5,6	5,6	OUT	Regulator Output.
—	—	7	—	SNS	Output voltage sense. Connect to output voltage.
—	5	—	7	ADJ	Adjustable regulator feedback input. Connect to resistor voltage divider.
5	—	8	8	FLG	Error Flag (Output): Open collector output. Active low indicates an output fault condition.
—	—	EP	EP	EP	Exposed pad. Connect to GND.

**Absolute Maximum Ratings<sup>(1)</sup>**

Supply Input Voltage ( $V_{IN}$ to GND)	–0.3V to +6V
Logic Input Voltage ( $V_{EN}$ to GND)	–0.3V to ( $V_{IN} + 0.3V$ )
Power Dissipation ( $P_D$ )	Internally Limited <sup>(3)</sup>
Flag	6V
Storage Temperature ( $T_S$ )	–65°C to +125°C
ESD <sup>(4)</sup>	2kV

**Operating Ratings<sup>(2)</sup>**

Supply Voltage ( $V_{IN}$ )	1.65V to 5.5V
Enable Input Voltage ( $V_{EN}$ )	0V to $V_{IN}$
Junction Temperature ( $T_J$ )	–40°C $\leq T_J \leq$ +125°C
Package Thermal Resistance	
S-PAK-5 ( $\theta_{JC}$ )	2°C/W
S-PAK-5 ( $\theta_{JA}$ )	38°C/W
TO-263-5 ( $\theta_{JC}$ )	2°C/W
EPAD SOIC-8 ( $\theta_{JA}$ )	41°C/W

**Electrical Characteristics<sup>(4)</sup>**

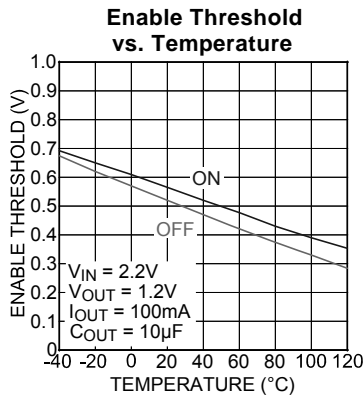
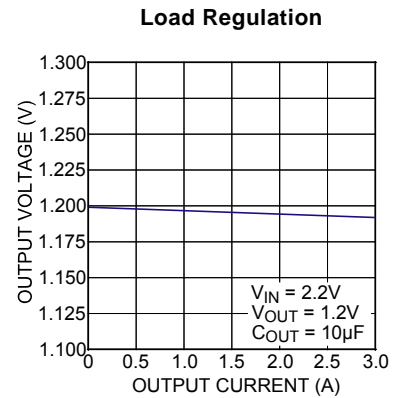
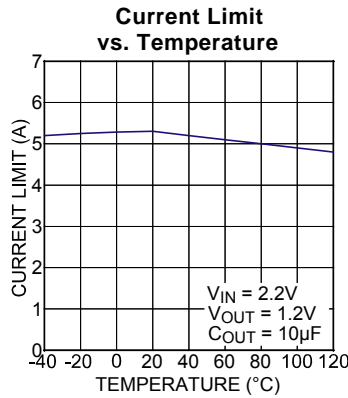
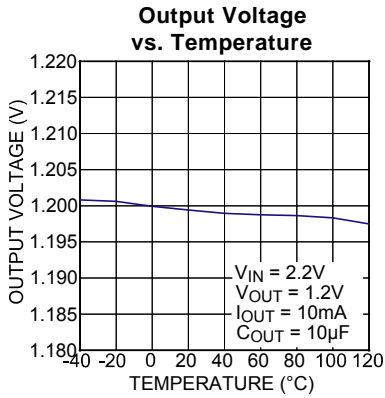
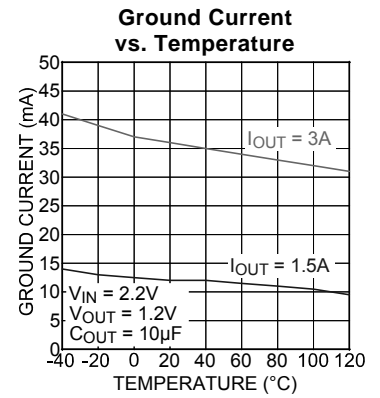
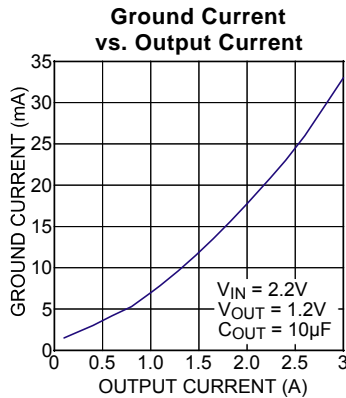
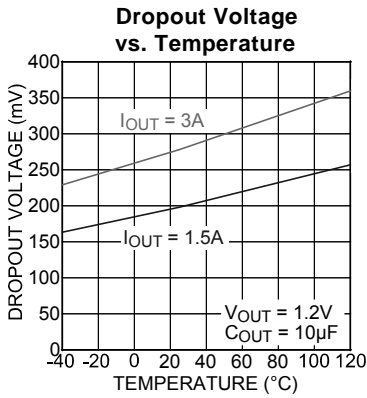
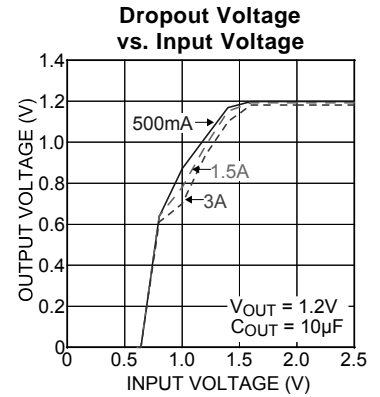
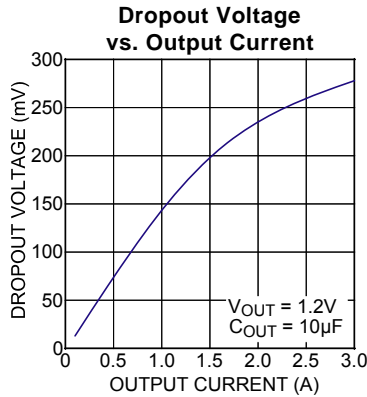
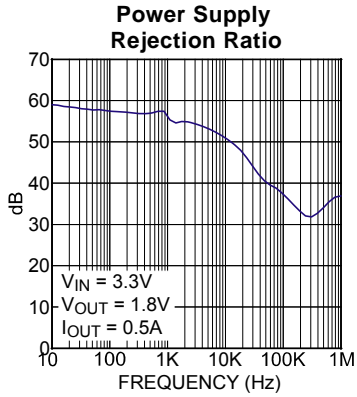
$T_A = 25^\circ\text{C}$  with  $V_{IN} = V_{OUT} + 1V$ ; **bold** values indicate  $-40^\circ\text{C} \leq T_J \leq +125^\circ\text{C}$ ;  $I_{OUT} = 10\text{mA}$ ;  $C_{OUT} = 10\mu\text{F}$  ceramic, unless noted.

Parameter	Conditions	Min	Typ	Max	Units
Output Voltage Accuracy	Over temperature range	<b>–2</b>		<b>+2</b>	%
Output Voltage Line Regulation (Note 5)	$V_{IN} = V_{OUT} + 1.0V$ to 5.5V For $V_{OUT} \geq 0.65V$ , $V_{IN} = 1.65V$ to 5.5V		$\pm 0.2$	$\pm 0.3$	%/V
Output Voltage Load Regulation	$I_{OUT} = 10\text{mA}$ to 3A		$\pm 0.3$		%
$V_{IN} - V_O$ ; Dropout Voltage (Note 6)	$I_{OUT} = 1.5A$		200	<b>300</b>	mV
	$I_{OUT} = 3.0A$		275	<b>500</b>	mV
Ground Pin Current	$I_{OUT} = 10\text{mA}$		1.2	<b>5</b>	mA
	$I_{OUT} = 1.5A$		12	<b>30</b>	mA
	$I_{OUT} = 3.0A$		32	<b>75</b>	mA
Ground Pin Current in Shutdown	$V_{EN} = 0V$		1		$\mu\text{A}$
Current Limit	$V_{OUT} = 0V$	<b>3.3</b>	5.2		A
Start-up Time	$V_{EN} = V_{IN}$		10	<b>150</b>	$\mu\text{s}$
Thermal Shutdown			165		$^\circ\text{C}$
<b>Enable Input</b>					
Enable Input Threshold	Regulator enable	<b>0.8</b>	0.57		V
	Regulator shutdown			<b>0.2</b>	V
Enable Pin Input Current	$V_{IL} \leq 0.2V$ (Regulator shutdown)		0.005		$\mu\text{A}$
	$V_{IH} \geq 0.8V$ (Regulator enable)		7		$\mu\text{A}$
<b>Flag Output</b>					
$I_{FLG(LEAK)}$	Flag Output Leakage Current (Flag Off)		0.05		$\mu\text{A}$
$V_{FLG(LO)}$	Output Logic-Low Voltage (under voltage condition), $I_{OUT} = 5\text{mA}$		150		mV
$V_{FLG}$	Threshold, % of $V_{OUT}$ below nominal (falling)	7.5	10	14	%
	Hysteresis		2		%
Feedback Pin Current			0.25	1	$\mu\text{A}$

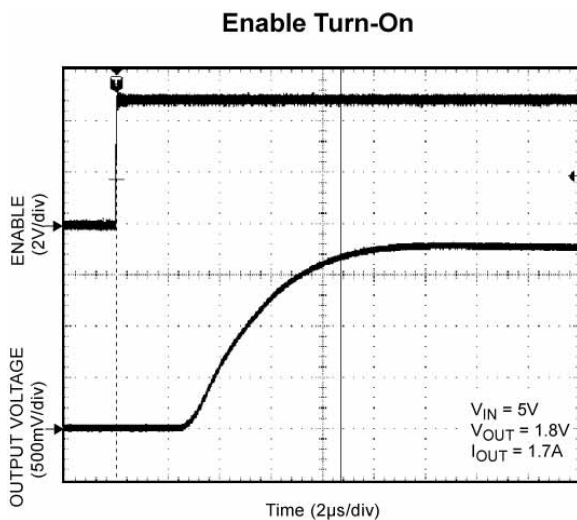
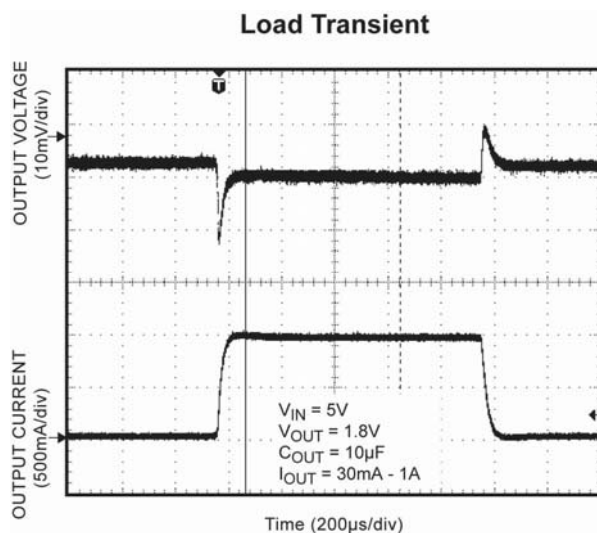
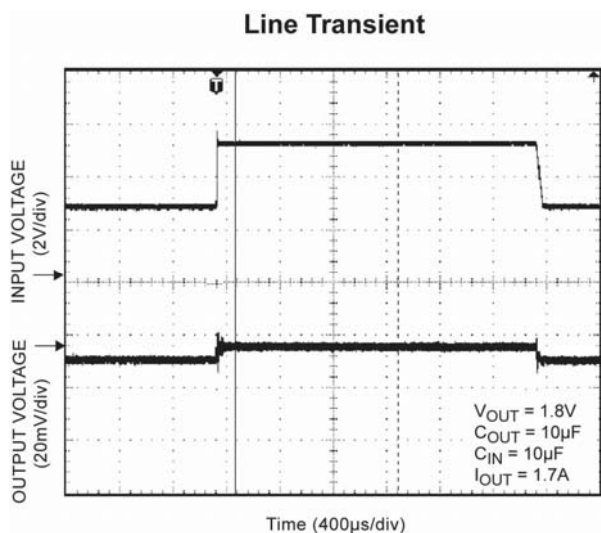
**Notes:**

- Exceeding the absolute maximum rating may damage the device.
- The device is not guaranteed to function outside its operating rating.
- The maximum allowable power dissipation of any  $T_A$  (ambient temperature) is  $(P_{D(max)} = T_{J(max)} - T_A) / \theta_{JA}$ . Exceeding the maximum allowable power dissipation will result in excessive die temperature and the regulator will go into thermal shutdown.
- Specification for packaged product only.
- Minimum input for line regulation test is set to  $V_{OUT} + 1V$  relative to the highest output voltage.
- Dropout voltage is defined as the input-to-output differential at which the output voltage drops 2% below its nominal value measured at 1V differential. For outputs below 1.65V, dropout voltage is considered the input-to-output voltage differential with the minimum input voltage of 1.65V. Minimum input operating voltage is 1.65V.

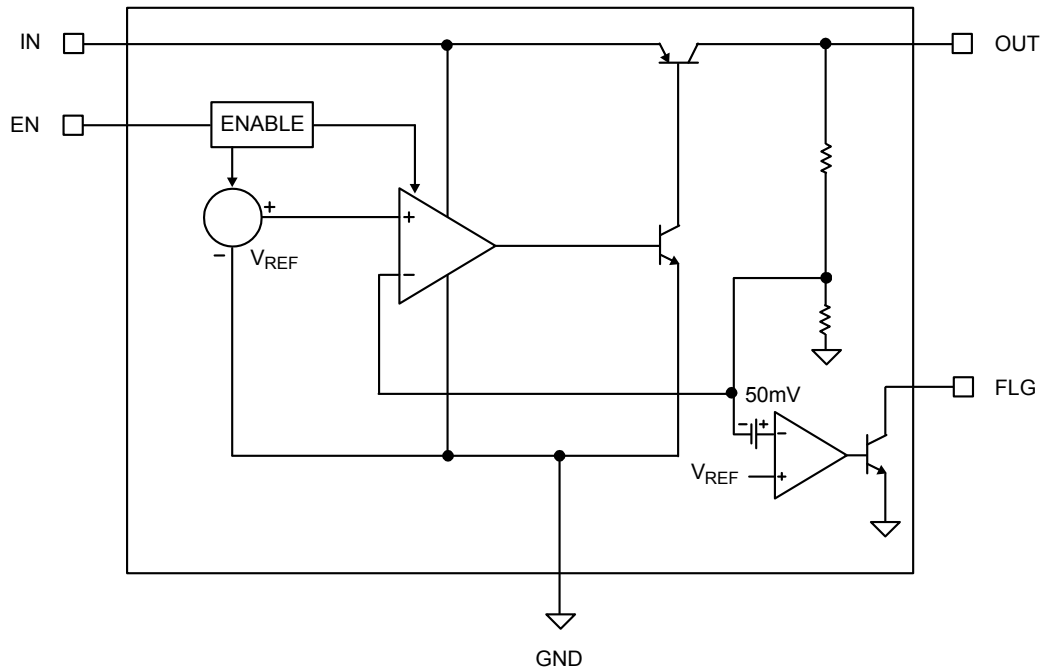
# Typical Characteristics



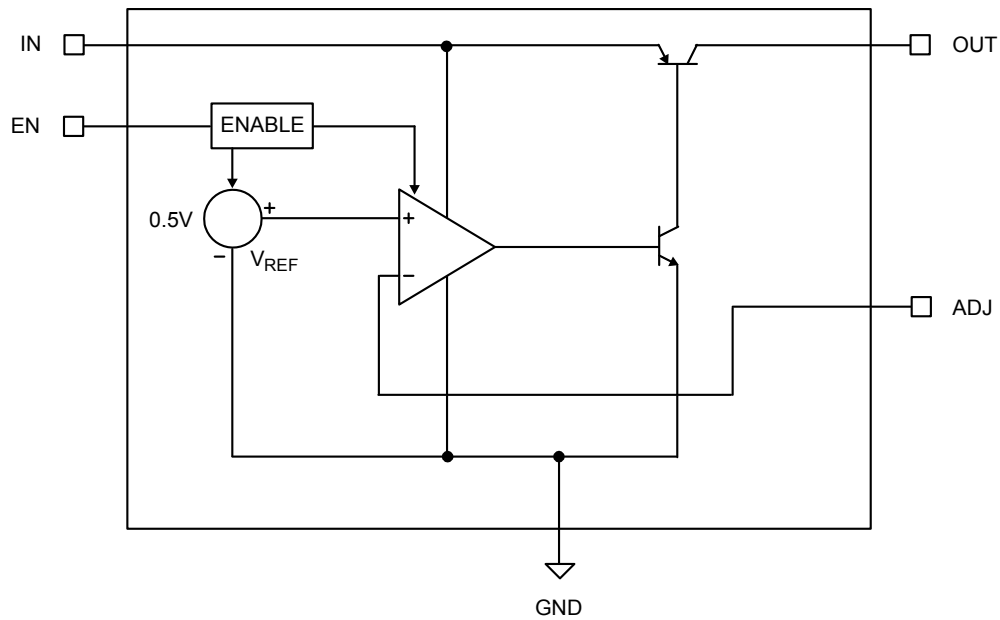
## Functional Characteristics



# Functional Diagram



**Fixed Output Regulator**



**Adjustable Output Regulator**

## Application Information

The MIC69301/2/3 is an ultra-high performance low dropout linear regulator designed for high current applications requiring a fast transient response. It utilizes a single input supply and has a very low dropout voltage perfect for low-voltage DC-to-DC conversion. The MIC69301/2/3 requires a minimum number of external components.

The MIC69301/2/3 regulator is fully protected from damage due to fault conditions offering constant current limiting and thermal shutdown.

### Input Supply Voltage

$V_{IN}$  provides a high current to the collector of the pass transistor. The minimum input voltage is 1.65V allowing conversion from low voltage supplies.

### Output Capacitor

The MIC69301/2/3 requires a minimum of output capacitance to maintain stability. However, proper capacitor selection is important to ensure desired transient response. The MIC69301/2/3 is specifically designed to be stable with low ESR ceramic chip capacitors. A 10 $\mu$ F ceramic chip capacitor should satisfy most applications. Output capacitor can be increased without bound. See typical characteristics for examples of load transient response.

X7R dielectric ceramic capacitors are recommended because of their temperature performance. X7R-type capacitors change capacitance by only 15% over their operating temperature range and are the most stable type of ceramic capacitors. Z5U and Y5V dielectric capacitors change value by as much as 50% and 60%, respectively over their operating temperature ranges. To use a ceramic chip capacitor with Y5V dielectric the value must be much higher than an X7R ceramic or a tantalum capacitor to ensure the same capacitance value over the operating temperature range. Tantalum capacitors have a very stable dielectric (10% over their operating temperature range) and can also be used with this device.

### Input Capacitor

An input capacitor of 1 $\mu$ F or greater is recommended when the device is more than 4 inches away from the bulk supply capacitance or when the supply is a battery. Small, surface mount, ceramic chip capacitors can be used for the bypassing. The capacitor should be placed within 1 inch of the device for optimal performance. Larger values will help to improve ripple rejection by bypassing the input to the regulator further improving the integrity of the output voltage.

### Minimum Load Current

The MIC69301/2/3 regulator is specified between finite loads. If the output current is too small, leakage currents dominate and the output voltage rises. A 10mA minimum load current is necessary for proper operation.

### Adjustable Regulator Design

The MIC69302 and MIC69303 adjustable version allows programming the output voltage anywhere between 0.5V and 5.5V with two resistors. The resistor value between  $V_{OUT}$  and the adjust pin should not exceed 10k $\Omega$ . Larger values can cause instability. The resistor values are calculated by:

$$V_{OUT} = 0.5 \times \left( \frac{R_1}{R_2} + 1 \right)$$

Where  $V_{OUT}$  is the desired output voltage.

### Enable

The fixed output voltage versions of the MIC69301 feature an active high enable input (EN) that allows on-off control of the regulator. Current drain reduces to near "zero" when the device is shutdown, with only microamperes of leakage current. EN may be directly tied to  $V_{IN}$  and pulled up to the maximum supply voltage.

### Thermal Design

Linear regulators are simple to use. The most complicated design parameters to consider are thermal characteristics. Thermal design requires the following application-specific parameters:

- Maximum ambient temperature ( $T_A$ )
- Output current ( $I_{OUT}$ )
- Output voltage ( $V_{OUT}$ )
- Input voltage ( $V_{IN}$ )
- Ground current ( $I_{GND}$ )

First, calculate the power dissipation of the regulator from these numbers and the device parameters from this data sheet.

$$P_D = (V_{IN} - V_{OUT}) \times I_{OUT} + V_{IN} \times I_{GND}$$

where the ground current is approximated by using numbers from the "Electrical Characteristics" or "Typical Characteristics" sections. The heat sink thermal resistance is then determined with this formula:

$$\theta_{SA} = ((T_J(\max) - T_A) / P_D) - (\theta_{JC} + \theta_{CS})$$

Where  $T_J(\max) \leq 125^\circ\text{C}$  and  $\theta_{CS}$  is between  $0^\circ\text{C}$  and  $2^\circ\text{C/W}$ .

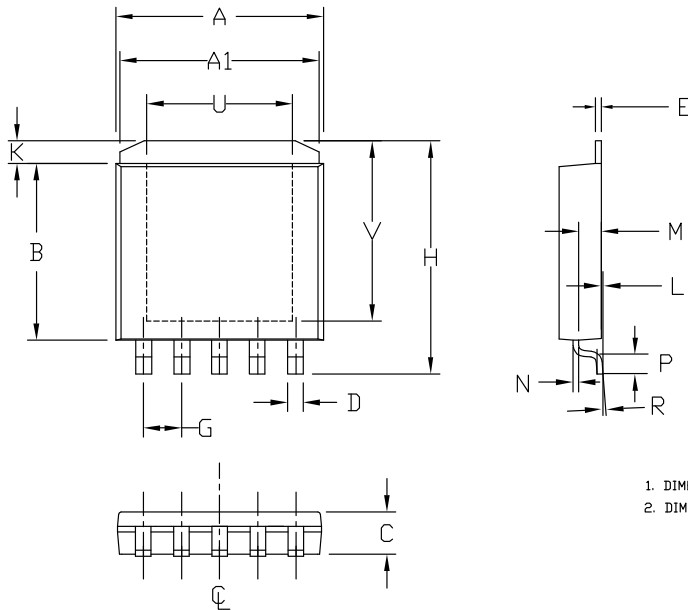


The heat sink may be significantly reduced in applications where the minimum input voltage is known and is large compared with the dropout voltage. Use a series input resistor to drop excessive voltage and distribute the heat between this resistor and the regulator. The low dropout properties of Micrel Super  $\beta$  PNP<sup>®</sup> regulators allow significant reductions in regulator power dissipation and the associated heat sink

without compromising performance. When this technique is employed, a capacitor of at least 1.0 $\mu$ F is needed directly between the input and regulator ground.

Refer to "Application Note 9" for further details and examples on thermal design and heat sink applications.

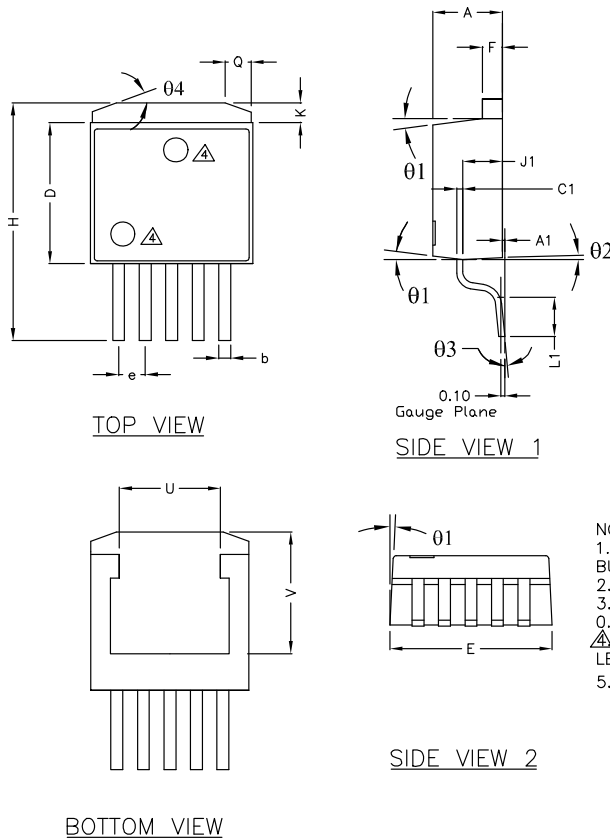
Package Information



	INCHES		MILLIMETERS	
A	0.365	0.375	9.27	9.52
A1	0.350	0.360	8.89	9.14
B	0.310	0.320	7.87	8.13
C	0.070	0.080	1.78	2.03
D	0.025	0.031	0.63	0.79
E	0.010	BSC	0.25	BSC
G	0.067	BSC	1.70	BSC
H	0.410	0.420	10.41	10.67
K	0.030	0.050	0.76	1.27
L	0.001	0.005	0.03	0.13
M	0.035	0.045	0.89	1.14
N	0.010	BSC	0.25	BSC
P	0.031	0.041	0.79	1.04
R	0°	6°	0°	6°
U	0.256	BSC	6.50	BSC
V	0.316	BSC	8.03	BSC

1. DIMENSION DOES NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
2. DIMENSION INCLUDES PLATING THICKNESS

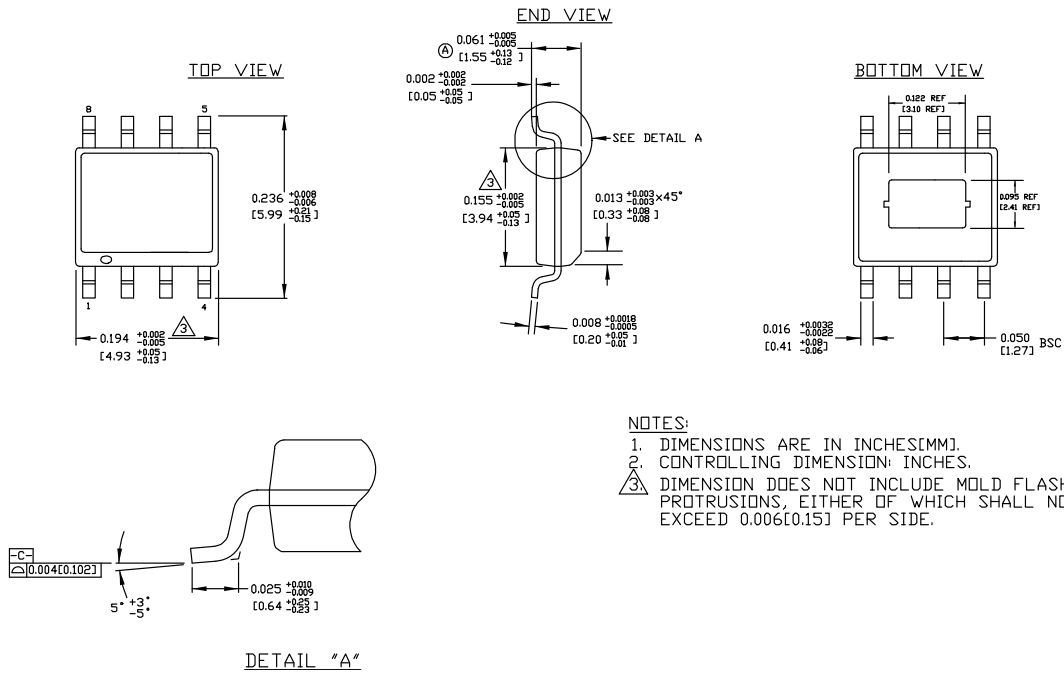
5-Pin S-PAK (R)



POS	INCH		MM	
	MIN	MAX	MIN	MAX
A	0.170	0.181	4.318	4.597
A1	0.000	0.012	0.000	0.305
b	0.026	0.036	0.660	0.914
C1	0.012	0.023	0.305	0.584
D	0.330	0.361	8.392	9.169
E	0.396	0.420	10.058	10.668
e	0.062	0.072	1.575	1.829
F	0.045	0.055	1.143	1.397
H	0.575	0.625	14.605	15.875
J1	0.080	0.120	2.032	3.048
K	0.045	0.066	1.143	1.676
L1	0.090	0.110	2.286	2.794
theta1	3°	10°	3°	10°
theta2	1°	7°	1°	7°
theta3	0°	8°	0°	8°
theta4	18°	22°	18°	22°
Q	0.055	0.075	1.397	1.905
U	0.256	Ref.	6.502	Ref.
V	0.305	Ref.	7.747	Ref.

- NOTE:
1. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & METAL BURR.
  2. PACKAGE OUTLINE INCLUSIVE OF PLATING THICKNESS.
  3. FOOT LENGTH USING GAUGE PLANE METHOD MEASUREMENT 0.010"
  4. PACKAGE TOP MARK MAY BE IN TOP CENTER OR LOWER LEFT CORNER
  5. ALL DIMENSIONS ARE IN INCHES/MILLIMETERS.

5-Pin TO-263 (U)



**NOTES:**  
 1. DIMENSIONS ARE IN INCHES[MM].  
 2. CONTROLLING DIMENSION: INCHES.  
 3. DIMENSION DOES NOT INCLUDE MOLD FLASH OR PROTRUSIONS, EITHER OF WHICH SHALL NOT EXCEED 0.006[0.15] PER SIDE.

**8-Pin EPAD SOIC (ME)**

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