TOSHIBA Transistor Silicon NPN Triple Diffused Type

2SD2352

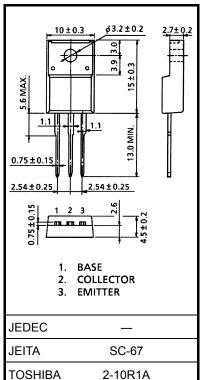
Power Amplifier Applications

Unit: mm

- High DC current gain: hFE = 800 to 3200
- Low collector saturation voltage: $V_{CE (sat)} = 0.3 V (typ.)$

Absolute Maximum Ratings (Tc = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V _{CBO}	60	V	
Collector-emitter voltage		V _{CEO}	60	V	
Emitter-base voltage		V _{EBO}	7	V	
Collector current	DC	Ι _C	2	A	
	Pulse	I _{CP}	4		
Base current		Ι _Β	0.4	А	
Collector power dissipation	Ta = 25°C	Pc	2	W	
	Tc = 25°C	FC	25		
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

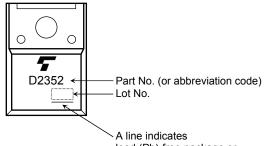
reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Electrical Characteristics (Tc = 25°C)

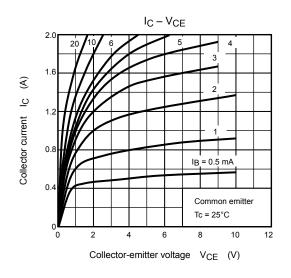
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	$V_{CB} = 60 \text{ V}, \text{ I}_{E} = 0$	_	_	100	μA
Emitter cut-off current	I _{EBO}	V _{EB} = 7 V, I _C = 0	_	_	100	μA
Collector-emitter breakdown voltage	V _{CEO}	I _C = 50 mA, I _B = 0	60	_	_	V
DC current gain	h _{FE (1)}	V _{CE} = 5 V, I _C = 0.1 A	800	_	3200	
	h _{FE (2)}	V _{CE} = 5 V, I _C = 1 A	350	_	_	
Collector-emitter saturation voltage	V _{CE (sat)}	I _C = 0.5 A, I _B = 5 mA	_	0.3	1.0	V
Base-emitter voltage	V _{BE}	V _{CE} = 5 V, I _C = 0.5 A	_	0.7	1.0	V
Transition frequency	f _T	V _{CE} = 5 V, I _C = 0.5 A	_	17	_	MHz
Collector output capacitance	C _{ob}	V _{CB} = 10 V, I _E = 0, f = 1 MHz	_	30	_	pF

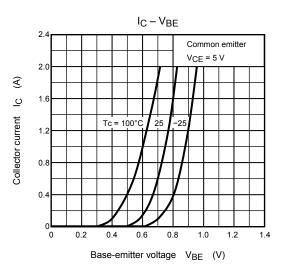
Marking

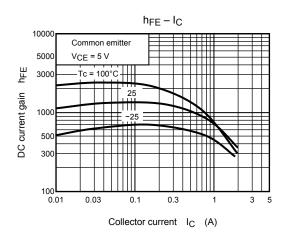


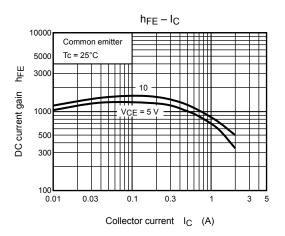
A line indicates lead (Pb)-free package or lead (Pb)-free finish.

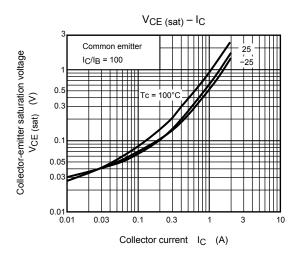
TOSHIBA

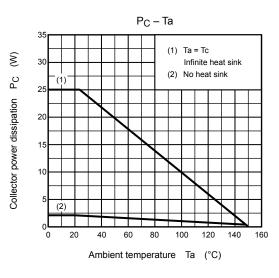


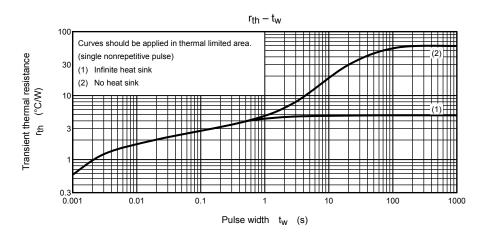












Safe Operating Area 10 IC max (pulsed)* 100 ms* 5 -10 ms³ 3 Π IC max (continuous) € DC operation Tc = 25°C Collector current IC 0.5 Π 0.3 0.1 0.05 *: Single nonrepetitive pulse Tc = 25°C 0.03 Curves must be derated linearly with increase in VCEO max temperature. 0.01 0.3 1 3 10 30 100 Collector-emitter voltage V_{CE} (V)

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