

Bi-Directional Triode Thyristor
Features

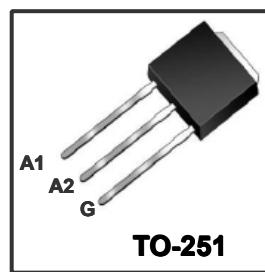
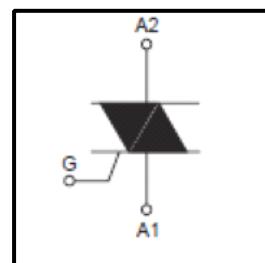
- ◆ Repetitive Peak Off-State Voltage : 600V
- ◆ R.M.S On-State Current (IT(RMS)= 4 A)
- ◆ Low On-State Voltage (1.6V(Typ.) @ I_{TM})
- ◆ High Commutation dv/dt
- ◆ High Junction temperature(T_J=150°C)

General Description

Winsemi Triac **STF4A60H** is designed for full wave AC control applications. It can be used as an ON/OFF function or for phase control operation.

Typical Application

- Home Appliances : Washing Machines, Vacuum Cleaners, Rice Cookers, Micro Wave Ovens, Hair Dryers, other control applications
- Industrial Use : SMPS, Copier Machines, Motor Controls, Dimmer, SSR, Heater Controls, Vending Machines, other control applications


Absolute Maximum Ratings (T_J= 25°C unless otherwise specified)

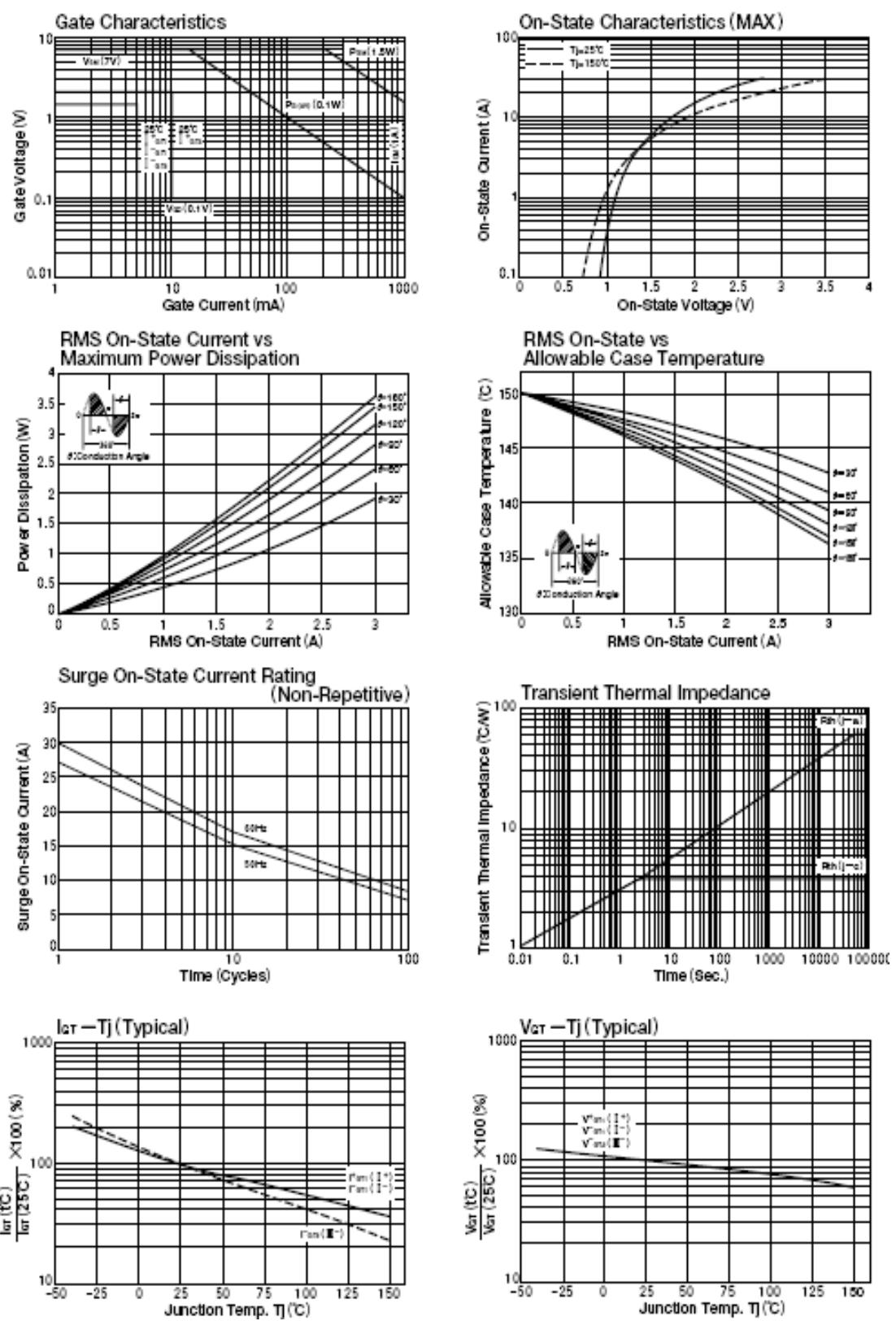
Symbol	Para	Condition	Ratings	Units
V _{DRM/V_{RRM}}	Repetitive Peak Off-State Voltage		600	V
I _{T(RMS)}	R.M.S On-State Current	T _J = 1118 °C	4.0	A
I _{TSM}	Surge On-State Current	50/60Hz, One cycle, Peak value, non-repetitive	27/30	A
I ² t	I ² t		3.7	A ² s
P _{GM}	Peak Gate Power Dissipation		1.5	W
P _{G(AV)}	Average Gate Power Dissipation		0.1	W
I _{GM}	Peak Gate Current		1.0	A
V _{GM}	Peak Gate Voltage		7.0	V
T _J	Operating Junction Temperature		-40~+150	°C
T _{STG}	Storage Temperature		-40~+150	°C

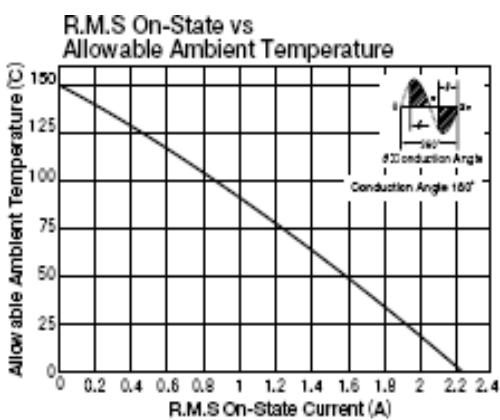
Thermal Characteristics

Symbol	Parameter	Value	Units
R _{θJC}	Thermal Resistance Junction to Case(DC)	3	°C/W
R _{θJA}	Thermal Resistance Junction to Ambient(DC)	75	°C/W

Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Characteristics		Min	Typ.	Max	Unit
I_{DRM}/I_{RRM}	off-state leakage current ($V_{AK}=V_{DRM}/V_{RRM}$ Single phase, half wave)	$T_J=50^\circ\text{C}$	-	-	1	mA
V_{TM}	Forward "On" voltage ($I_T=4.5\text{A}$, Inst. Measurement)		-	1.2	1.7	V
I_{GT}	Gate trigger current (continuous dc) ($V_{AK} = 6 \text{ Vdc}$, $RL = 10 \Omega$)	T2+,G+	-	-	5	mA
		T2+,G-	-	-	5	
		T2-,G-	-	-	5	
V_{GT}	Gate Trigger Voltage (Continuous dc)) ($V_{AK} = 6 \text{ Vdc}$, $RL = 10 \Omega$)	T2+,G+	-	-	1.5	V
		T2+,G-	-	-	1.5	
		T2-,G-	-	-	1.5	
V_{GD}	Gate threshold Voltage $V_D=1/2V_{DRM}$,	$T_J=150^\circ\text{C}$	0.2	-	-	V
$(dv/dt)c$	Critical Rate of Rise of Off-State Voltage at Commutation ($V_D=0.67V_{DRM}$; $(di/dt)C=-1.5\text{A/ms}$)	$T_J=150^\circ\text{C}$	3	-	-	V/ μs
I_H	Holding Current		-	2	-	mA
I_L	latching current		-	2	6	mA





TO-251 Package Dimension