

NPN SILICON RF POWER TRANSISTOR

DESCRIPTION:

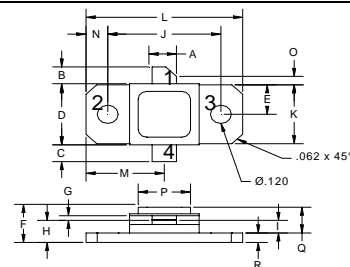
The **ASI ALR100** is Designed for 1200 – 1400 MHz, L-Band Applications.

FEATURES:

- Internal Input/Output Matching Network
- $P_G = 6.0$ dB at 100 W/1400 MHz
- **Omnigold™** Metalization System

MAXIMUM RATINGS

I_C	13.5 A
V_{CC}	32 V
P_{DISS}	270 W @ $T_C = 25^\circ\text{C}$
T_J	-65 °C to +250 °C
T_{STG}	-65 °C to +200 °C
θ_{JC}	0.55 °C/W

PACKAGE STYLE .400 2L FLG


DIM	MINIMUM inches / mm	MAXIMUM inches / mm
A	.140 / 3.56	
B	.110 / 2.80	
C	.110 / 2.80	
D	.395 / 10.03	.407 / 10.34
E	.193 / 4.90	
F		.230 / 5.84
G	.003 / 0.08	.006 / 0.15
H	.118 / 3.00	.131 / 3.33
I	.063 / 1.60	
J	.650 / 16.51	
K	.386 / 9.80	
L	.900 / 22.86	
M	.450 / 11.43	
N	.125 / 3.18	
O	.050 / 1.27	
P	.405 / 10.29	
Q	.170 / 4.32	
R	.062 / 1.58	

1 = COLLECTOR 2&3 = BASE 4 = EMITTER

ORDER CODE: ASI10514
CHARACTERISTICS $T_C = 25^\circ\text{C}$

SYMBOL	TEST CONDITIONS	MINIMUM	TYPICAL	MAXIMUM	UNITS
BV_{CBO}	$I_C = 50$ mA	65			V
BV_{CES}	$I_C = 100$ mA	65			V
BV_{EBO}	$I_E = 10$ mA	3.5			V
I_{CES}	$V_{CE} = 32$ V			20	mA
h_{FE}	$V_{CE} = 5.0$ V $I_C = 5.0$ A	15		---	---
P_G	$V_{CC} = 28$ V $P_{OUT} = 100$ W $f = 1.2$ to 1.4 GHz	6.0			dB
η_C		50			%