



PJDLC05W

VOLTAGE 5 Volts **POWER** 120 Watts

ULTRA LOW CAPACITANCE DUAL TRANSIENT VOLTAGE SUPPRESSOR FOR HIGH SPEED DATA LINES

This transient overvoltage suppressor is intended to protect sensitive equipment against electrostatic discharge events as well to offer a minimum insertion loss in data transmission lines in communications ports used in portable consumer, computing and networking applications. This dual transient voltage suppressor comes in a single SOT-323, offering board space reduction, where the application requires it.

FEATURES

- Improved leakage current, maximum of 20 μA @ 5Vdc
- Maximum capacitance @ 0 Vdc Bias of 1.2 pF between terminals 1-3 or terminals 2-3
- IEC61000-4-2 esd 15kV Air, 8kV contact compliance
- IEC61000-4-5 lightning 5 Amps peak, 8x20 usec waveform
- In compliance with EU RoHS 2002/95/EC directives

MECHANICAL DATA

Case: SOT-323, plastic

Terminals: solderable per MIL-STD-750, Method 2026

Approx. Weight : 0.0048gram

Marking : 2S

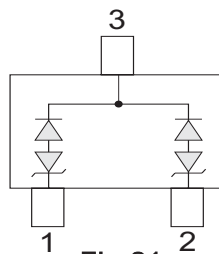
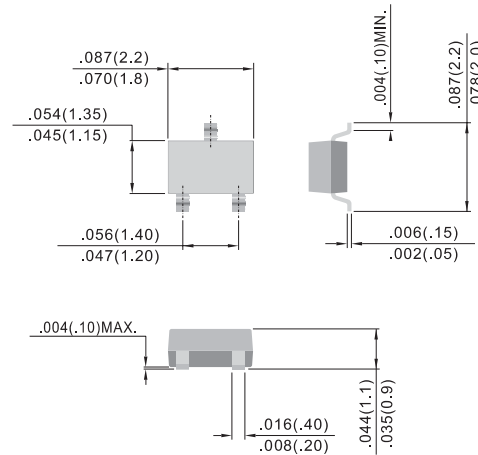


Fig.21

SOT-323

Unit: inch (mm)



MAXIMUM RATINGS

Rating	Symbol	Value	Units
Peak Pulse Power 8x20 μs Waveform	P _{PP}	120	W
Peak Pulse Current 8x20 μs Waveform	I _{PP}	10	A
ESD Voltage (HBM)	V _{ESD}	>25	kV
Operating Temperature	T _L	-55 to +125	°C
Storage Temperature	T _{STG}	-55 to +150	°C

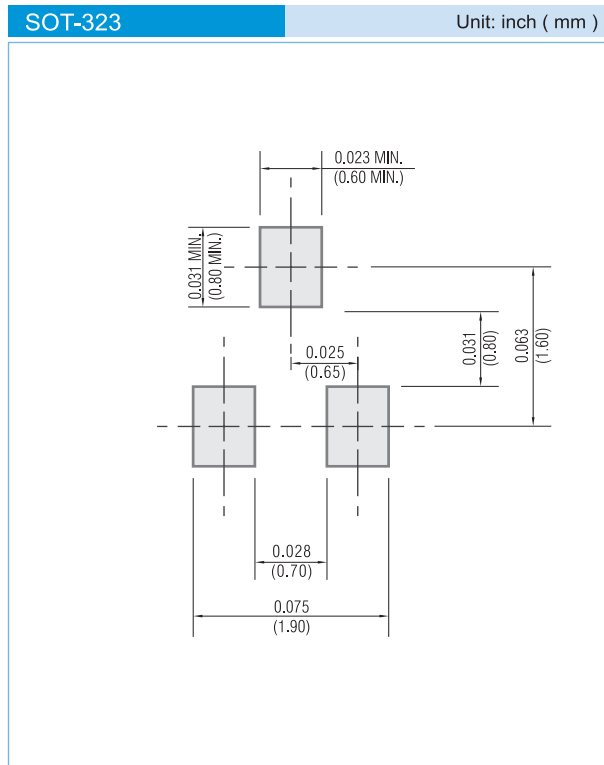
ELECTRICAL CHARACTERISTICS

PJDLC05W						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V _{RWM}				5	V
Reverse Breakdown Voltage	V _{BR}	I _t = 1mA	6			V
Reverse Leakage Current	I _R	V _{RWM} = 5V, T = 25°C			20	μA
Clamping Voltage	V _C	I _{PP} = 1A t _p = 8/20 μs			9.8	V
Clamping Voltage	V _C	I _{PP} = 5A t _p = 8/20 μs			11	V
Peak Pulse Current	I _{PP}	t _p = 8/20 μs			10	A
Junction Capacitance	C _J	Pin 1 or 2 to pin 3(GND) V _R = 0V, f = 1MHz			1.2	pF



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MOUNTING PAD LAYOUT



ORDER INFORMATION

- Packing information
 - T/R - 12K per 13" plastic Reel
 - T/R - 3K per 7" plastic Reel

LEGAL STATEMENT

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