

Silicon PNP Power Transistors

2SA1360

DESCRIPTION

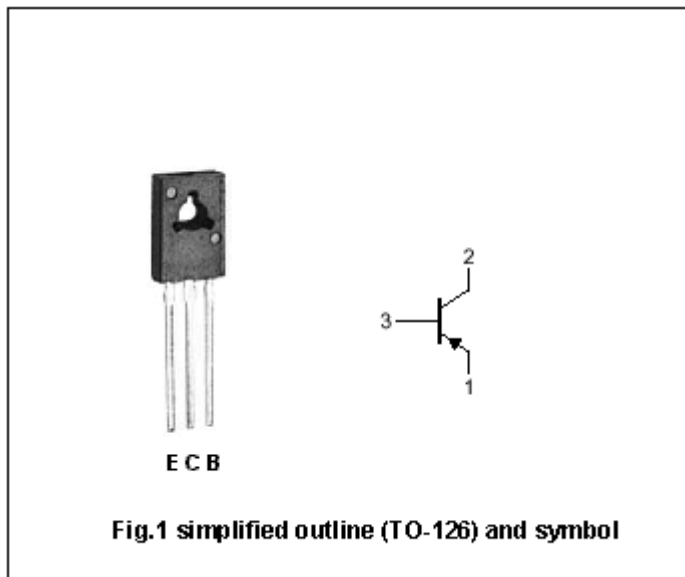
- With TO-126 package
- Complement to type 2SC3423
- High transition frequency

APPLICATIONS

- Audio frequency amplifier applications

PINNING

PIN	DESCRIPTION
1	Emitter
2	Collector;connected to mounting base
3	Base



Absolute maximum ratings(Ta=25)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	-150	V
V _{CEO}	Collector-emitter voltage	Open base	-150	V
V _{EBO}	Emitter-base voltage	Open collector	-5	V
I _C	Collector current		-50	mA
I _B	Base current		-5	mA
P _D	Total power dissipation	T _a =25	1.2	W
		T _C =25	5	
T _j	Junction temperature		150	
T _{stg}	Storage temperature		-55 ~ +150	

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-1mA ; I _B =0	-150			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-10mA ; I _B =-1mA			-1.0	V
V _{BE}	Base-emitter on voltage	I _C =-10mA ; V _{CE} =-5V			-0.8	V
I _{CBO}	Collector cut-off current	V _{CB} =-150V ; I _E =0			-0.1	μ A
I _{EBO}	Emitter cut-off current	V _{EB} =-5V ; I _C =0			-0.1	μ A
h _{FE}	DC current gain	I _C =-10mA ; V _{CE} =-5V	80		240	
C _{ob}	Output capacitance	I _E =0 ; V _{CB} =-10V f=1MHz		2.5		pF
f _T	Transition frequency	I _C =-10mA ; V _{CE} =-10V		200		MHz

◆ h_{FE} Classifications

O	Y
80-160	120-240

PACKAGE OUTLINE

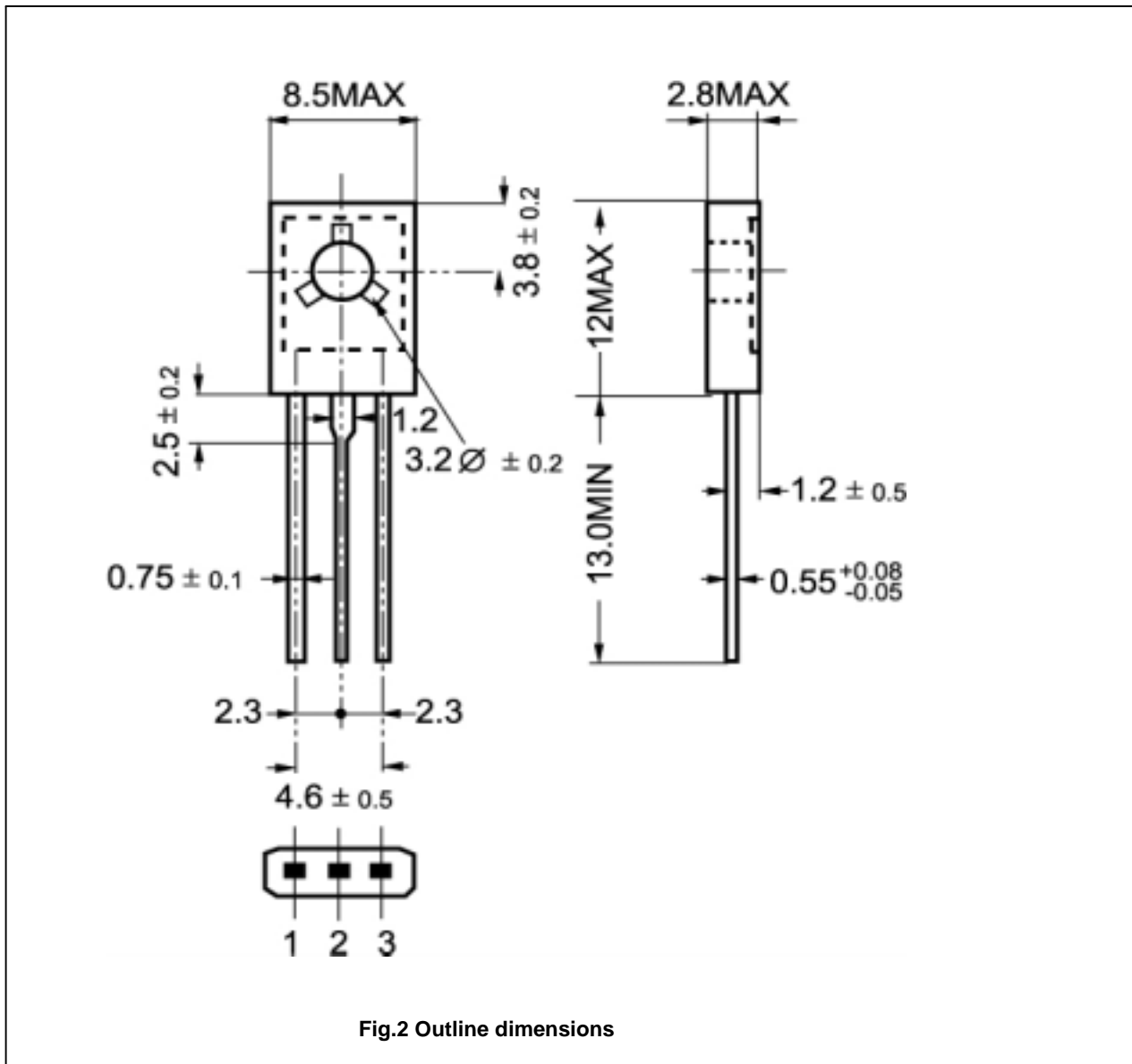


Fig.2 Outline dimensions