

EPA1155-17 • EPA1155G-17

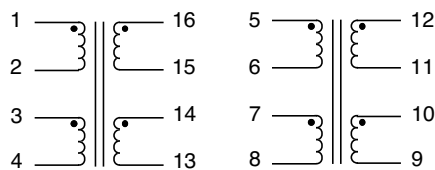
- Meets CCITT 1.430 Four Wire "S" Interface
- Thru Hole and SMD Packages
- Hipot : 2000 Vrms

Electrical Parameters @ 25° C

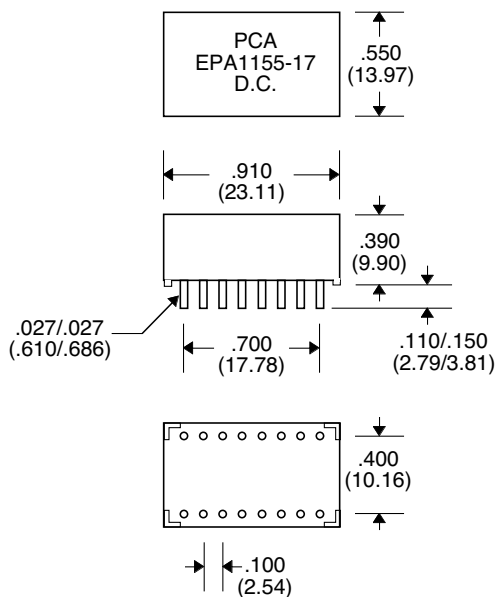
Primary Inductance (mH Min.)	Leakage Inductance (μH Max.)	Interwinding Capacitance (pF Max.)	Pri. Dist. Capacitance (pF Max.)	Turns Ratio (± 2%)	DCR (Ω Max.)	
22	8	100	110	1:2.5	3	7

• Primary Inductance : 10 KHz, 1.0 Vrms • Longitudinal Balance : -60 dB Min. @ 10 KHz - 300 KHz •

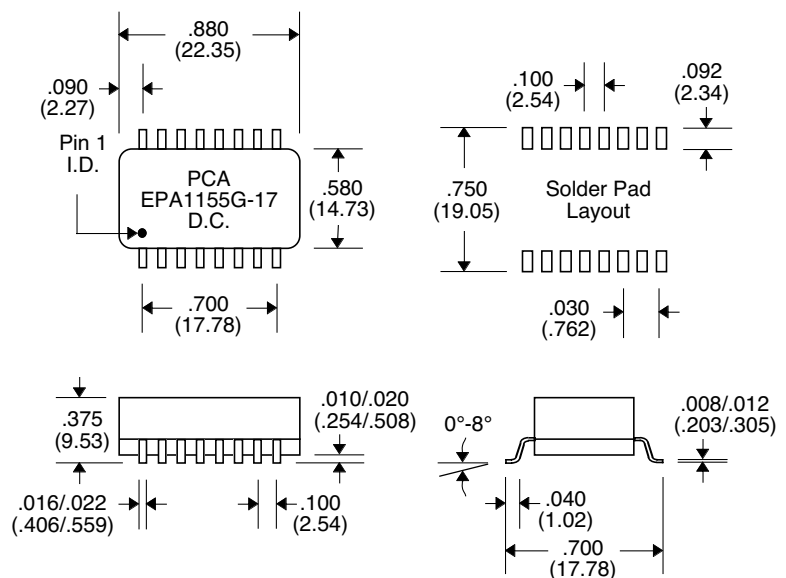
Schematic



DIL Package



SMD Package



Unless Otherwise Specified Dimensions are in Inches /mm ± .010 / .25