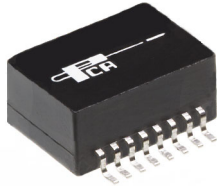


EPA3191S

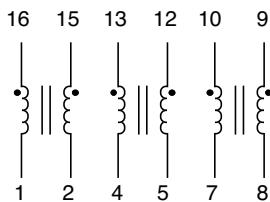


- Applications including FDDI-TP, Token Ring, Ethernet/10BT, ISDN, T1/Cept, and Fast Ethernet
- High impedance to minimize common mode noise
- Complies with or exceeds IEEE Requirements

Electrical Parameters @ 25° C

OCL (μ H Min.)	Interwinding Capacitance (pF Max.)	Leakage Inductance (μ H Max.)	DCR (Ω Max.)
100 KHz, 0.1 Vrms	100 KHz, 0.1 Vrms	100 KHz, 0.1 Vrms	
100	8	0.1	0.20

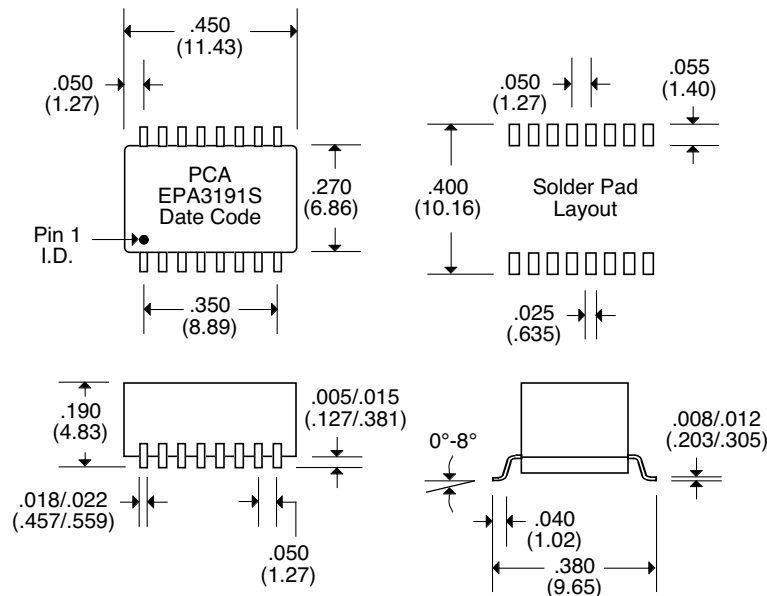
Schematic



Impedance

Freq.	85°C to 0°C			-10°C to -40°C		
	Impedance Limit			Impedance Limit		
100KHz	40	Ω	Min.	10	Ω	Min.
300KHz	150	Ω	Min.	50	Ω	Min.
1MHz	550	Ω	Min.	250	Ω	Min.
3MHz	1000	Ω	Min.	700	Ω	Min.
10MHz	1400	Ω	Min.	1400	Ω	Min.
20MHz	1400	Ω	Min.	1400	Ω	Min.
30MHz	1400	Ω	Min.	1400	Ω	Min.
50MHz	1300	Ω	Min.	1300	Ω	Min.
80MHz	1150	Ω	Min.	1150	Ω	Min.
100MHz	1000	Ω	Min.	1000	Ω	Min.
200MHz	600	Ω	Min.	600	Ω	Min.

Package



Unless Otherwise Specified Dimensions are in Inches /mm $\pm .010$ / .25