

# 500mA / 12V Low V<sub>CE</sub> (sat) Digital transistors

# (with built-in resistors)

### DTD513ZE / DTD513ZM

#### Applications

Inverter, Interface, Driver

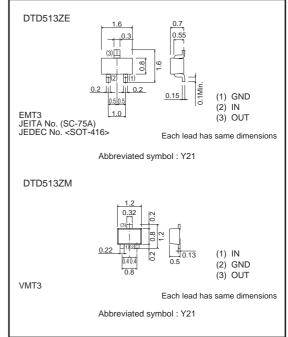
#### Feature

- 1) VCE (sat) is lower than conventional products.
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 4) Only the on / off conditions need to be set for operation, making the device design easy.

#### Structure

NPN epitaxial plannar silicon transistor (Resistor built-in type)

#### Dimensions (Unit : mm)



#### Packaging specifications

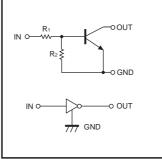
	Package	EMT3	VMT3
	Packaging type	Taping	Taping
	Code	TL	T2L
Part No.	Basic ordering unit (pieces)	3000	8000
DTD513ZE		0	_
DTD513ZM		-	0

#### Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Lin	nits	Unit	
Farameter	Symbol	DTD513ZE	DTD513ZM	Unit	
Supply voltage	Vcc	1	2	V	
Input voltage	Vin	–5 to	+10	V	
Collector current *1	IC (max)	50	00	mA	
Power dissipation *2	Po	15	50	mW	
Junction temperature	Tj	15	50	C	
Storage temperature	Tstg	–55 to	+150	°C	

\*1 Characteristics of built-in transistor. \*2 Each terminal mounted on a recommended land.

Inner circuit



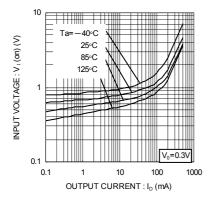
 $R_1=1.0k\Omega / R_2=10k\Omega$ 

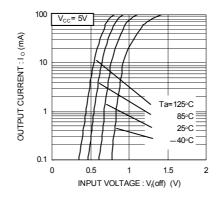
#### •Electrical characteristics (Ta=25°C)

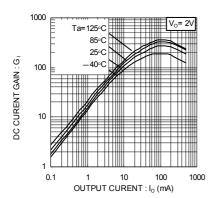
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI(off)	-	-	0.3	v	Vcc=5V, Io=100µA
Input voltage	VI(on)	2.5	-	-	V	Vo=0.3V, Io=20mA
Output voltage	VO(on)	-	60	300	mV	lo/l=100mA / 5mA
Input current	h	-	-	6.4	mA	Vi= 5V
Output current	IO(off)	-	-	0.5	μΑ	Vcc=12V, VI=0V
DC current gain	Gi	140	_	_	-	Vo=2V, Io=100mA
Transition frequency *	f⊤	_	260	_	MHz	Vce=10V, Ie= -5mA, f=100MHz
Input resistance	R1	0.7	1.0	1.3	kΩ	_
Resistance ratio	R2/R1	8.0	10	12	-	_

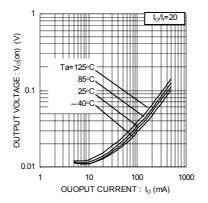
\* Characteristics of built-in transistor.

#### •Electrical characteristics (Ta=25°C)









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