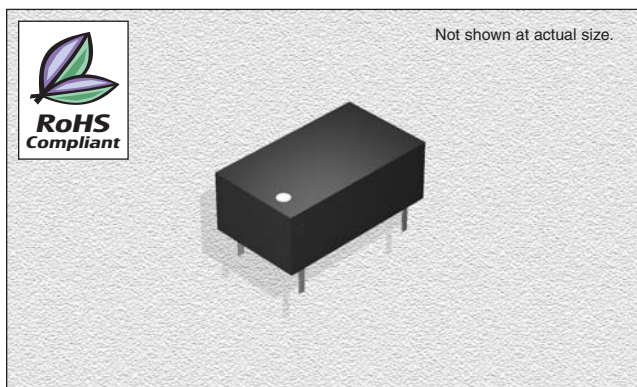


## CTDD2010DF-SU3K-2 Series

### Fixed Input Isolated & Unregulated

### 2W Output, Single Output



### FEATURES

- Efficiency:** To 86%
- Temperature Range:** -40°C to +85°C
- Isolation:** 3KVDC
- Package:** UL94-V0
- Miscellaneous:** Single Output Voltage. No external component required. No heat sink required. Industry standard pinout. DIP package style. Small Footprint. Power Density 1.42W/cm<sup>3</sup>. PCB Mounting.
- Samples available.**

### CHARACTERISTICS

- Short Circuit Protection:** 1 second
- Temperature Rise at Full Load:** 25°C Max. 15°C Typ.
- Cooling:** Free air convection
- No-load Power Consumption:** 10% nominal power (typical)
- Operating Temperature Range:** -40°C to +85°C
- Storage Temperature Range:** -55°C to +125°C
- Lead Temperature:** 300°C (1.5mm from case for 10 seconds)
- Storage Humidity Range:** ≤ 95%
- Case Material:** Plastic (UL94-V0)
- MTBF:** >3,500,000 hours
- Miscellaneous:** RoHS Compliant.

### SPECIFICATIONS

Part Number	Vin Nom. (VDC)	Vin Range (VDC)	Voltage Out (VDC)	Io Max. (mA)	Io Min. (mA)	Effi. Typ. (%)
CTDD2010DF-0503-SU3K-2	5	4.5-5.5	3.3	606	61	65
CTDD2010DF-0505-SU3K-2	5	4.5-5.5	5	400	40	80
CTDD2010DF-0509-SU3K-2	5	4.5-5.5	9	222	22.2	81
CTDD2010DF-0512-SU3K-2	5	4.5-5.5	12	167	16.7	82
CTDD2010DF-0515-SU3K-2	5	4.5-5.5	15	133	13.3	84
CTDD2010DF-1205-SU3K-2	12	10.8-13.2	5	400	40	80
CTDD2010DF-1209-SU3K-2	12	10.8-13.2	9	222	22.2	83
CTDD2010DF-1212-SU3K-2	12	10.8-13.2	12	167	16.7	84
CTDD2010DF-1215-SU3K-2	12	10.8-13.2	15	133	13.3	85
CTDD2010DF-2405-SU3K-2	24	21.6-26.4	5	400	40	81
CTDD2010DF-2409-SU3K-2	24	21.6-26.4	9	222	22.2	84
CTDD2010DF-2412-SU3K-2	24	21.6-26.4	12	167	16.7	85
CTDD2010DF-2415-SU3K-2	24	21.6-26.4	15	133	13.3	86

### ISOLATION SPECIFICATIONS

Item	Test Condition	MIN	TYP	MAX	Units
Isolation Voltage	Tested for 1 minute	3000			VDC
Isolation Resistance	Test at 500VDC	1000			MΩ

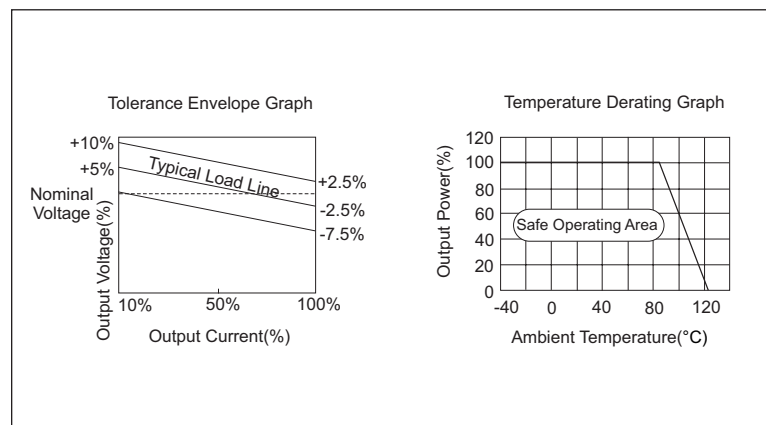
### OUTPUT SPECIFICATIONS

Item	Test Condition	MIN	TYP	MAX	Units
Output Power		0.2		2	W
Line regulation	For Vin change of 1%			1.5	%
Load regulation	10% to 100% full load		10	15	%
Output voltage accuracy		See tolerance envelope graph			
Temperature drift	100% full load			0.03	%/°C
Ripple & Noise	20Hz Bandwidth		100	200	mVp-p
Switching frequency	Full load, nominal input voltage		75		KHz

Note:

1. All specifications measured at TA=25°C, humidity<75%, nominal input voltage and rated output unless otherwise specified.

### TYPICAL CHARACTERISTICS

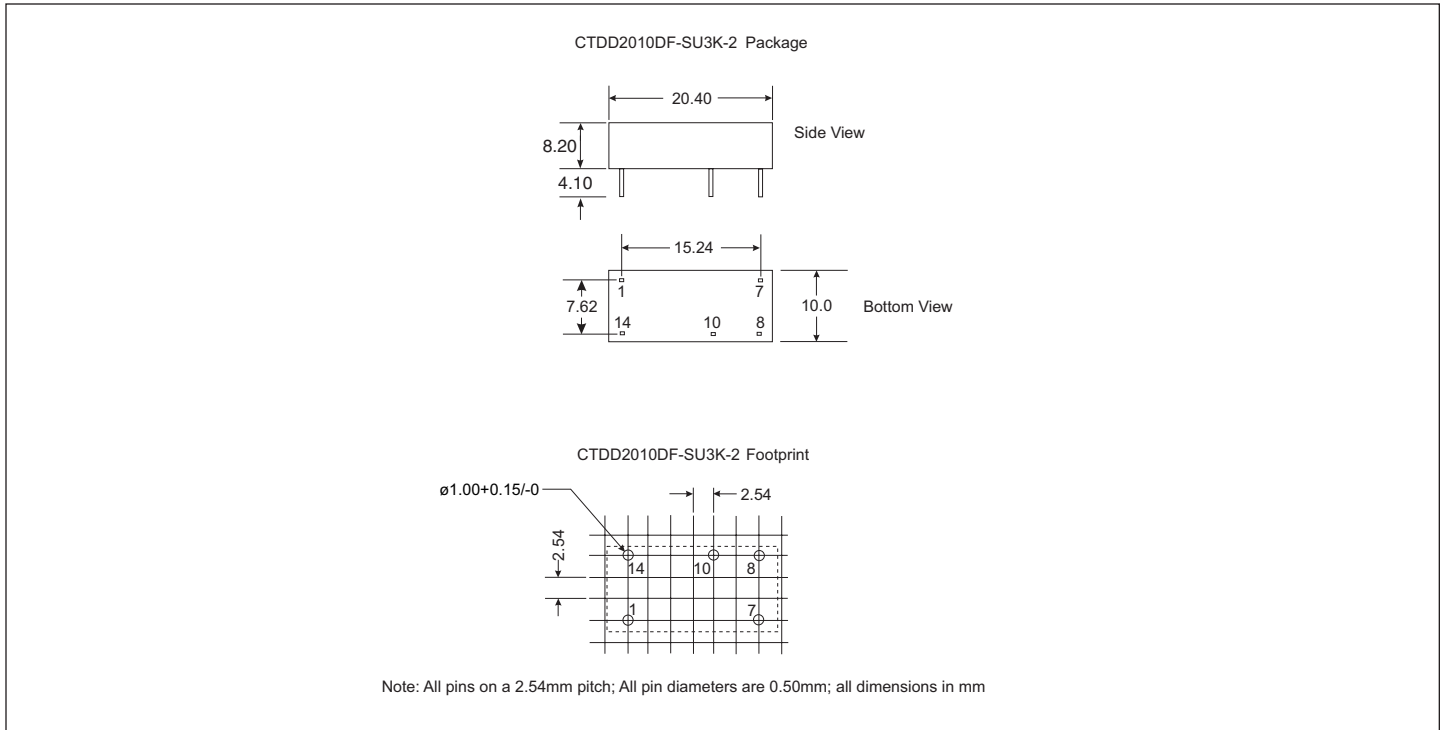


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## CTDD2010DF-SU3K-2 Series

Fixed Input Isolated & Unregulated  
2W Output, Single Output

### OUTLINE DIMENSIONS & RECOMMENDED FOOTPRINT DETAILS



### PIN CONNECTIONS

Pin	Function
1	GND
7	NC
8	+Vo
10	OV
14	Vin



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