

SPI-8000A Series**Surface Mount, Separate Excitation Step-down Switching Mode Regulator ICs****■Features**

- Surface-mount 16 pin package
- Output current: 3.0A
- High efficiency: 91% (at $V_{IN} = 10V$, $I_O = 1A$, $V_O = 5V$)
- Capable of downsizing a choke-coil due to IC's high switching frequency (125kHz). (Compared with conventional Sanken devices)
- The output-voltage-variable type can vary its output voltage from 1V to 14V because of its low reference voltage (V_{REF}) of 1V.
- Wide Input Voltage Range (8 to 50V)
- Output ON/OFF available
- Built-in overcurrent and thermal protection circuits

■Absolute Maximum Ratings

(T _a =25°C)			
Parameter	Symbol	Ratings	Unit
DC Input Voltage	V_{IN}	53	V
Power Dissipation	P_D ^{1, 2}	2.4	W
Junction Temperature	T_J	+125	°C
Storage Temperature	T_{STG}	-40 to +125	°C
Thermal Resistance (junction to case)	θ_{J-C} ²	18	°C/W
Thermal Resistance (junction to ambient air)	θ_{J-A} ²	50	°C/W

¹: Limited due to thermal protection.²: When mounted on glass-epoxy board 700cm² (copper laminate area 30.8cm²).**■Applications**

- Onboard local power supplies
- OA equipment
- For stabilization of the secondary-side output voltage of switching power supplies

■Recommended Operating Conditions

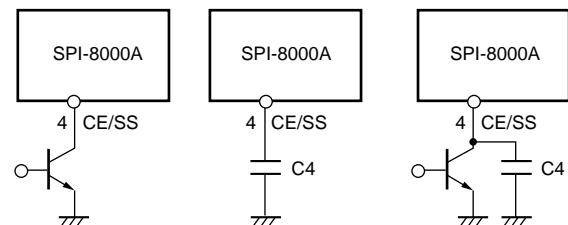
Parameter	Symbol	Ratings		
		SPI-8010A		
DC Input Voltage Range	V_{IN}	(8 or V_O+3) ¹	to 50	
Output Voltage Range	V_O	1	to 14	
Output Current Range	I_O	0.02	to 3.0	
Operating Junction Temperature Range	T_{JOP}	-30	to +125	
Operating Temperature Range	T_{OP}	-30	to +125	

¹: The minimum value of an input voltage range is the higher of either 8V or V_O+3V .**■Electrical Characteristics**

Parameter	Symbol	Rating			Unit
		SPI-8010A (Variable type)			
Reference Voltage	V_{ADJ}	min.	0.97	typ.	V
	Conditions			$V_{IN}=12V$, $I_O=1A$	
Efficiency	Eff		86		%
	Conditions		$V_{IN}=20V$, $I_O=1A$, $V_O=5V$		
Oscillation Frequency	F_{OSC}		250		kHz
	Conditions		$V_{IN}=12V$, $I_O=1A$		
Line Regulation	ΔV_{OLINE}		20	40	mV
	Conditions		$V_{IN}=10$ to 30V, $I_O=1A$		
Load Regulation	ΔV_{OLoad}		10	30	mV
	Conditions		$V_{IN}=12V$, $I_O=0.1$ to 1.5A		
Temperature Coefficient of Reference Voltage	$\Delta V_{ADJ}/\Delta T_a$		±0.5		mV/°C
Overcurrent Protection Starting Current	I_S	3.1			A
Quiescent Circuit Current	I_Q		7		mA
Circuit Current at Output OFF	$I_Q(\text{off})$			400	μA
CE/SS Terminal	V_{SSL}			0.5	V
	I_{SSL}			50	μA
	Conditions		$V_{SSL}=0V$		

* Pin 4 is the CE/SS pin. Soft start at power on can be performed with a capacitor connected to this pin. The output can also be turned ON/OFF with this pin. The output is stopped by setting the voltage of this pin to V_{SSL} or lower. CE/SS-pin voltage can be changed with an open-collector drive circuit of a transistor. When using both the soft-start and ON/OFF functions together, the discharge current from C_4 flows into the ON/OFF control transistor. Therefore, limit the current securely to protect the transistor if C_3 capacitance is large.

The CE/SS pin is pulled up to the power supply in the IC, so applying the external voltage is prohibited.



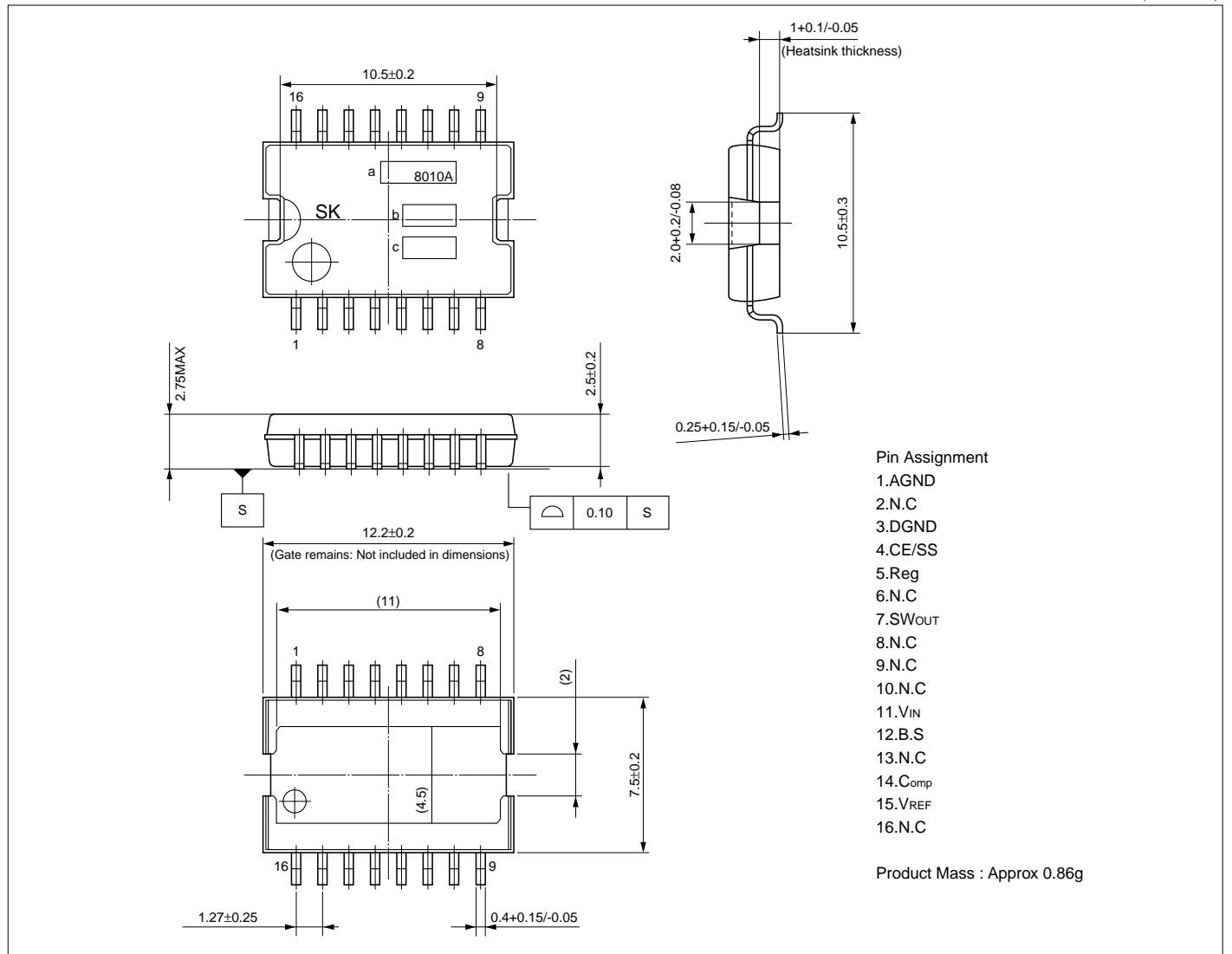
Vo. ON/OFF

Soft start

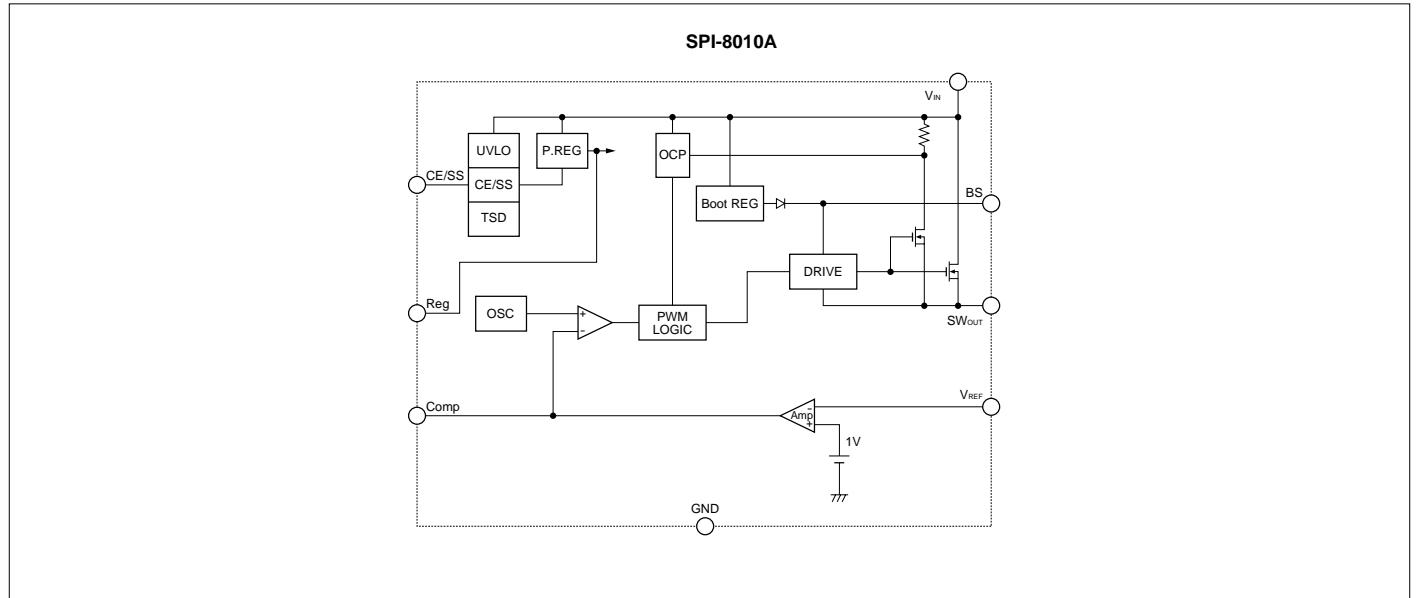
Soft start
+Vo. ON/OFF

■External Dimensions (HSOP16)

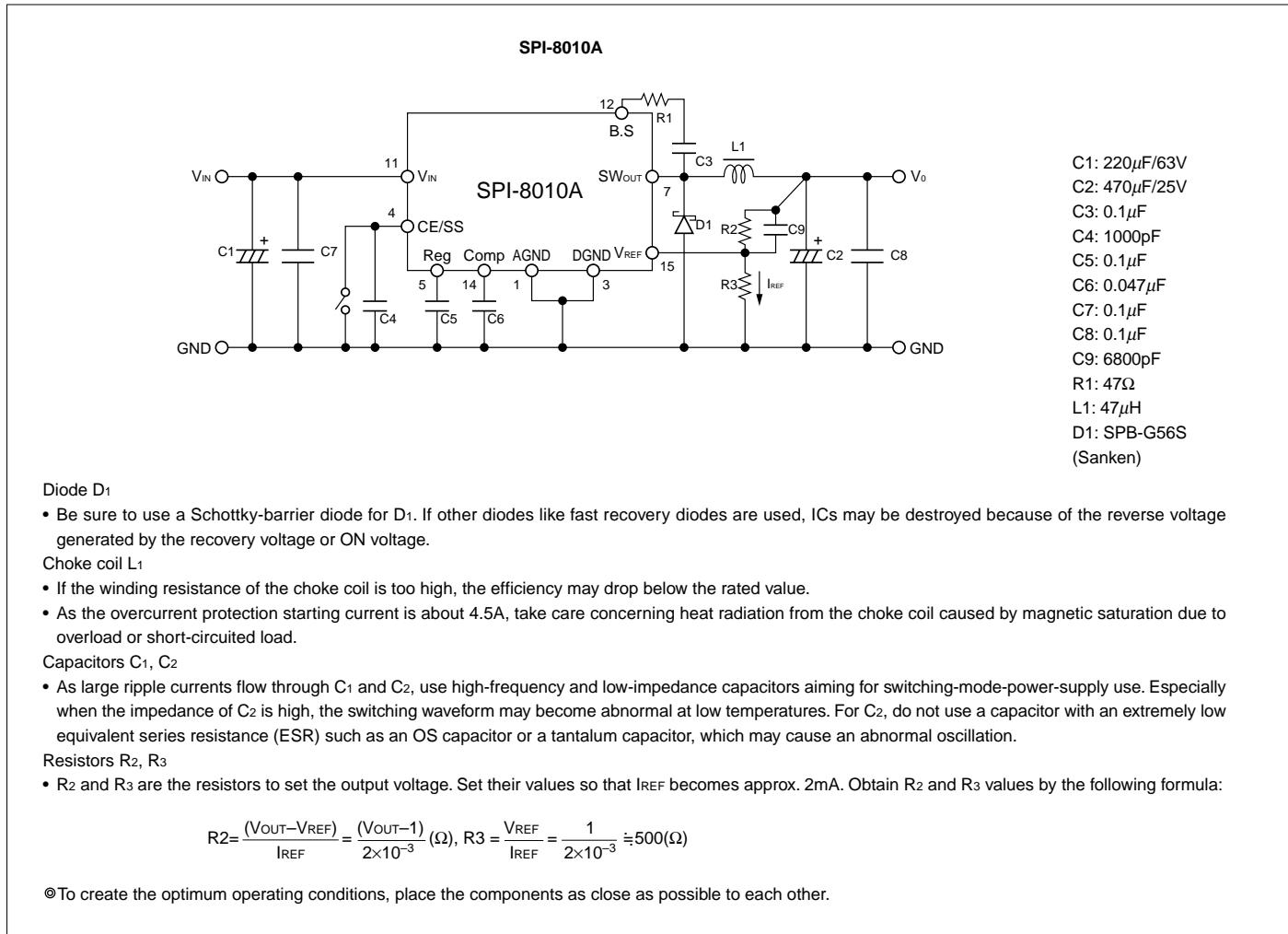
(Unit : mm)



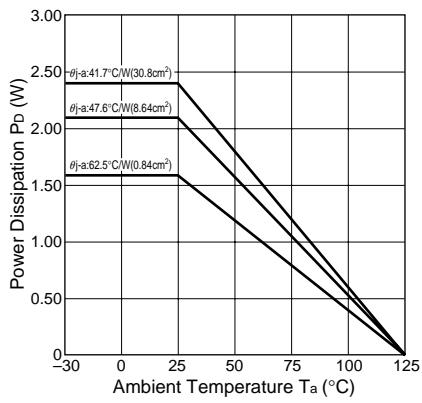
■Block Diagram



■Typical Connection Diagram



■Ta-Pd Characteristics



$$P_d = V_o \cdot I_o \left(\frac{100}{\eta\chi} - 1 \right) - V_F \cdot I_o \left(1 - \frac{V_o}{V_{IN}} \right)$$

Note 1: The efficiency depends on the input voltage and the output current. Therefore, obtain the value from the efficiency graph and substitute the percentage in the formula above.

Note 2: Thermal design for D1 must be considered separately.

Vo : Output voltage
 VIN : Input voltage
 Io : Output current
 ηχ : Efficiency (%)
 VF : Diode D1 forward voltage
 SPB-G56S...0.4V(Io=2A)