

Features

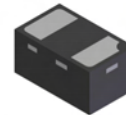
- Ultra Low Forward Voltage
- Excellent High Temperature Stability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- **Lead Free Finish, RoHS Compliant (Note 1)**
- **“Green” Molding Compound (No Br, Sb)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: DFN1006-2 (Equivalent to SOD-882)
- Case Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Polarity Indicator: Cathode Dot
- Terminals: Finish - NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.001 grams (Approximate)



Top View



Bottom View

Maximum Ratings @T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	30	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _{RM}		
Average Rectified Output Current (See Figure 1)	I _O	0.2	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	5.0	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance	R _{θJS}	18	°C/W
Thermal Resistance Junction to Soldering (Note 2)		263	
Thermal Resistance Junction to Ambient (Note 3)			
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage Drop	V _F	-	0.34	0.40	V	I _F = 0.1A, T _J = 25°C
			-	0.48		I _F = 0.2A, T _J = 25°C
			0.39	0.45		I _F = 0.2A, T _J = 125°C
Leakage Current (Note 4)	I _R	-	4	50	μA	V _R = 30V, T _J = 25°C
			0.5	10		mA

- Notes:
1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/quality/lead_free.html.
 2. Theoretical R_{θJS} calculated from the top center of the die straight down to the PCB cathode tab solder junction.
 3. FR-4 PCB, 2oz. Copper, minimum recommended pad layout per <http://www.diodes.com/datasheets/ap02001.pdf>.
 4. Short duration pulse test used to minimize self-heating effect.

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SBR02U30LP

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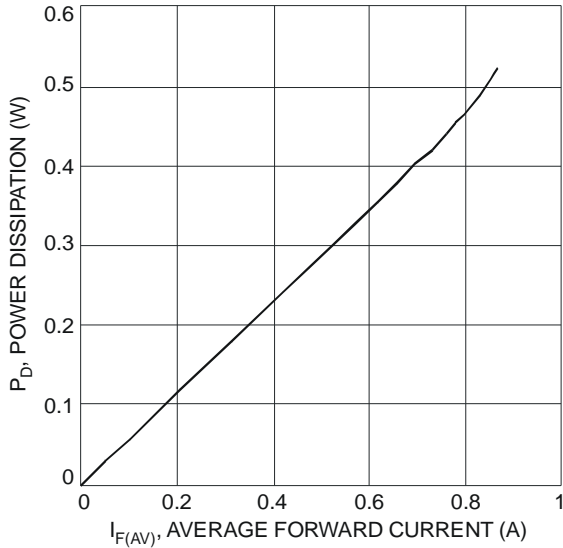


Fig. 1 Forward Power Dissipation

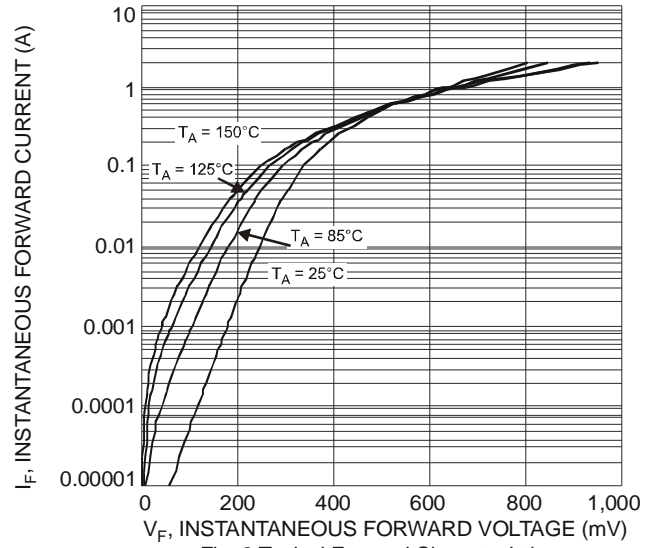


Fig. 2 Typical Forward Characteristics

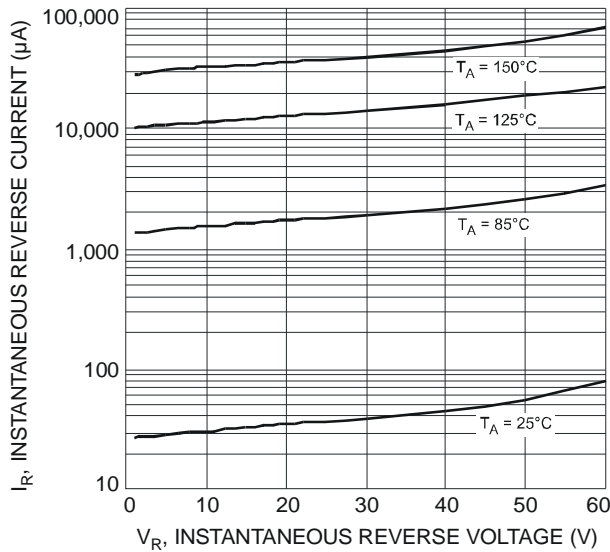


Fig. 3 Typical Reverse Characteristics

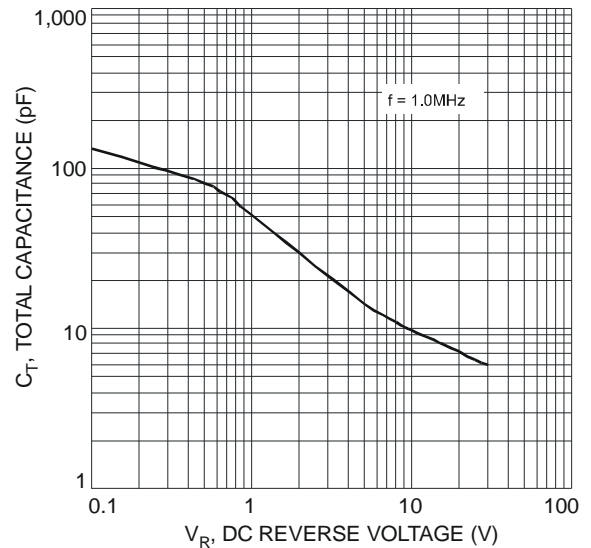


Fig. 4 Total Capacitance vs. Reverse Voltage

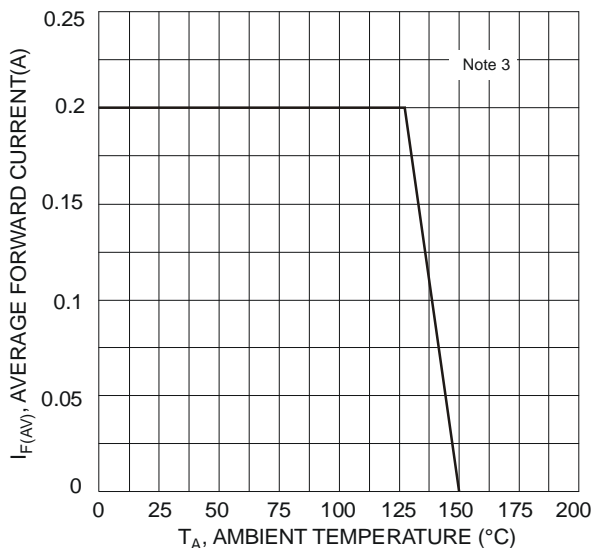


Fig. 5 Forward Current Derating Curve

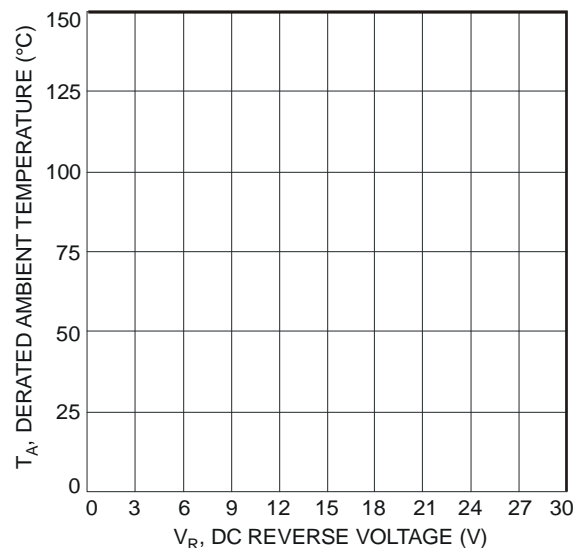


Fig. 6 Operating Temperature Derating

Ordering Information (Note 5)

Part Number	Case	Packaging
SBR02U30LP-7	DFN1006-2	3000/Tape & Reel

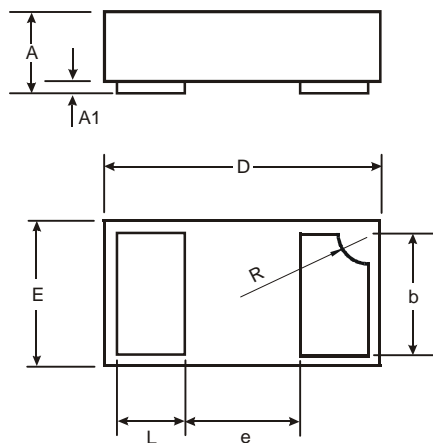
Notes: 5. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



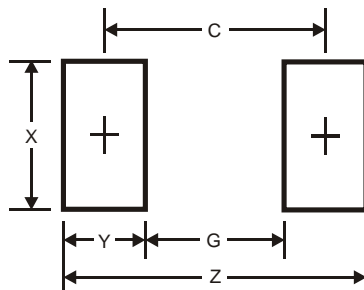
23 = Product Type Marking Code
Dot Denotes Cathode Side

Package Outline Dimensions



DFN1006-2			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0	0.05	0.03
b	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	-	-	0.40
L	0.20	0.30	0.25
R	0.05	0.15	0.10
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.1
G	0.3
X	0.7
Y	0.4
C	0.7

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