Silicon NPN Power Transistors BU2515AF

DESCRIPTION

·With TO-3PFa package
·High voltage
·High speed switching

APPLICATIONS

·For use in horizontal deflection circuits of PC monitors

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

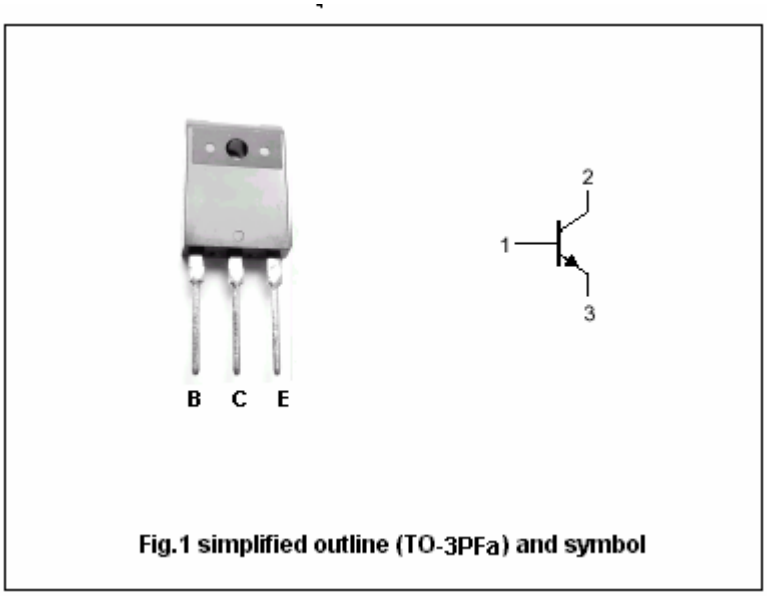

Fig.1 simplified outline (TO-3PFa) and symbol

Absolute maximum ratings(Ta=25□)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	1500	V
V_{CEO}	Collector-emitter voltage	Open base	800	V
V_{EBO}	Emitter-base voltage	Open collector	7.5	V
I_C	Collector current (DC)		9	A
I_{CM}	Collector current (Pulse)		20	A
I_B	Base Collector current (DC)		5	A
I_{BM}	Base current (Pulse)		7.5	A
P_{tot}	Total power dissipation	T_C=25□	45	W
T_j	Junction temperature		150	□
T_{stg}	Storage temperature		-55~150	□

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CHARACTERISTICS

Tj=25□ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{CEO(SUS)}$	Collector-emitter sustaining voltage	I_C=100mA ;I_B=0	700			V
V_{EBO}	Emitter-base breakdown voltage	I_E=1mA ;I_C=0	7.5	13.5		V
V_{CEsat}	Collector-emitter saturation voltage	I_C=4.5A ;I_B=0.9A			5.0	V
V_{BEsat}	Emitter-base saturation voltage	I_C=4.5A ;I_B=0.9A			1.0	V
I_{CES}	Collector cut-off current	V_{CE}=$BV_{CES;}$ V_{BE}=0 T_j=125□			1.0 2.0	mA
I_{EBO}	Emitter cut-off current	V_{EB}=7.5V; I_C=0			1.0	mA
h_{FE-1}	DC current gain	I_C=0.5A ; V_{CE}=5V		17.2		
h_{FE-2}	DC current gain	I_C=4.5A ; V_{CE}=5V	5		10.8	

PACKAGE OUTLINE

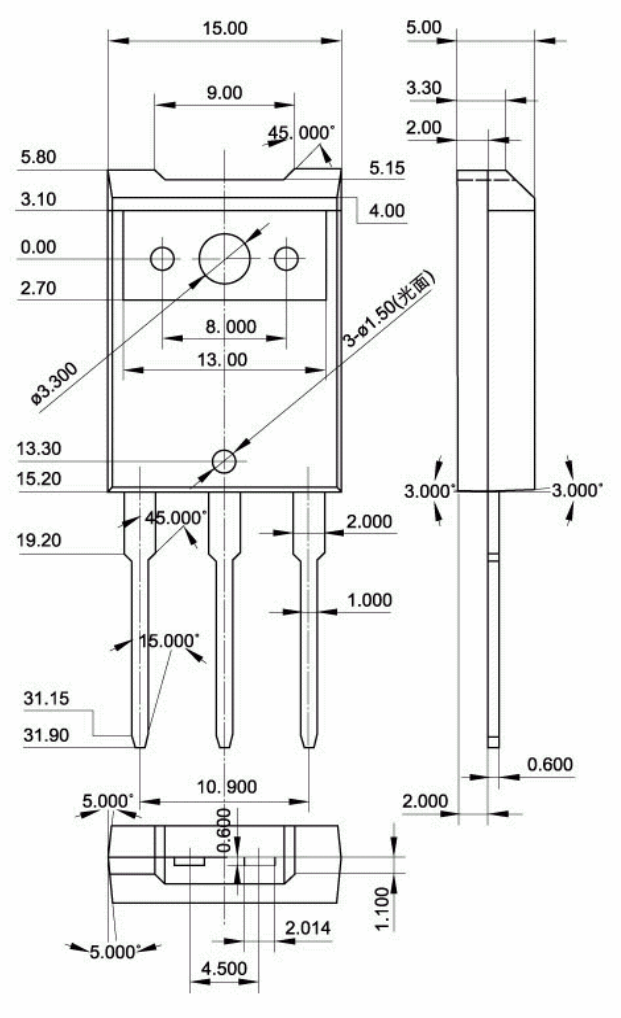

Fig.2 Outline dimensions (unindicated tolerance:±0.30mm)