

# T1/CEPT/ISDN-PRI TRANSFORMER

## P/N: S5122 DATA SHEET

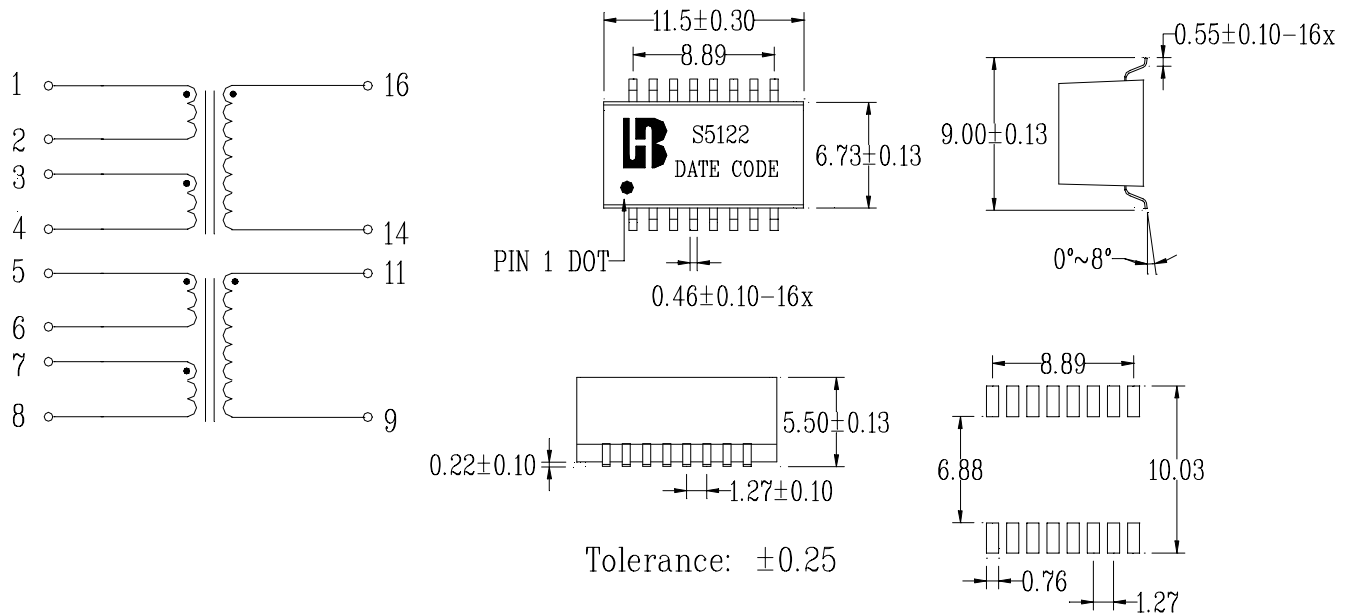
### A. GENERAL SPECIFICATIONS:

1. EXTENDED AND STANDARD TEMPERATURE RANGE MODELS.
2. DUAL SURFACE MOUNT PACKAGE CONTAINS BOTH TRANSMIT AND RECEIVE.
3. MODELS MATCHED TO LEADING TRANSCEIVER ICs
4. UL 1459 RECONGNIZED.

### B. ELECTRICAL SPECIFICATIONS: (25°C)

1. TURNS RATIO-POLARITY( $\pm 2\%$ ): (1-2):(3-4): (16-14)=1:1:1.577  
(5-6):(7-8):(11-9)=1:1:1.577
2. INDUCTANCE(16-14)=(11-9):1.5mH Min @ 100KHz/0.2V
3. LEAKAGE INDUCTANCE: 16-14 with(short1,2,3,4) : 0.5uH Max  
11-9 with (short5, 6, 7,8): 0.5 uH Max
4. INTERWINDING CAPACITANCE: (16-14) TO (1-4)(CONNECT 2,3): 40pF Max.  
(11-9) TO (5-8)(CONNECT 5,7): 40pF Max.
5. DCR: (1-2)=(5-6): 0.9 $\Omega$  Max & (16-14)=(11-9): 1.4 $\Omega$  Max.
5. HI-POT: (16,14) to (1,2,3,4) 1500 Vrms/0.5mA 1Sec.  
(11,9) to (5,6,7,8) 1500 Vrms/0.5mA 1Sec.

### C. DIMENSIONS & SCHEMATIC & MARKING (UNIT: mm)



PCB Suggest pad layout