



AN6650 LINEAR INTEGRATED CIRCUIT

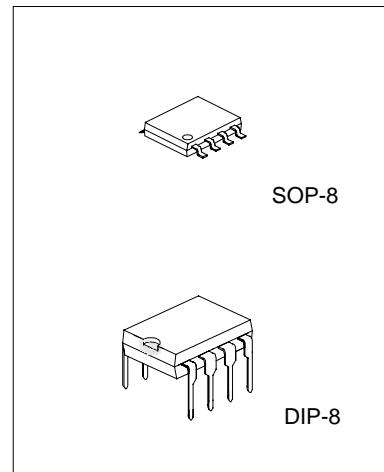
MOTOR SPEED CONTROL CIRCUIT

DESCRIPTION

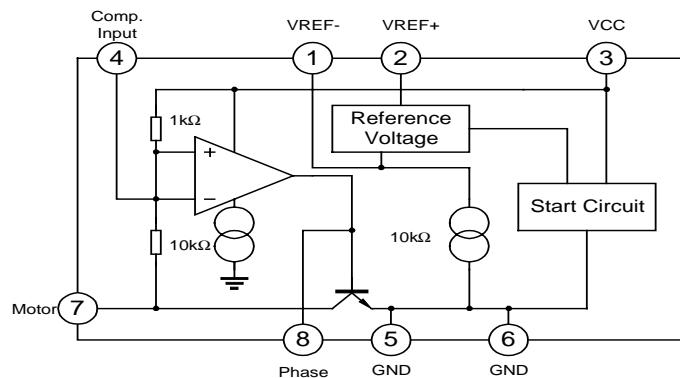
The AN6650 is a monolithic integrated circuit, designed for the tape recorder.

FEATURES

- *Wide operating supply voltage: Vcc=1.8V-7V
- *Few external components
- *Easy speed control mode



BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$)

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	V_{CC}	7.5	V
Terminal Voltage	$V_n(n=1,2,3,4)$	-0.5 to 7.5	V
Terminal 8 Voltage	V_8	-0.5 to 1	V
Supply Current	I_{CC}^*	1000	mA
Terminal 7 Current	I_7	1000	mA
Power Dissipation	PD		mW
DIP-8		750	
SOP-8		360	
Operating Temperature	T_{OPR}	-20 to 70	$^\circ\text{C}$
Storage Temperature	T_{STQ}	-40 to 150	$^\circ\text{C}$

*Test Time<5μs

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ELECTRICAL CHARACTERISTICS ($T_a=25^\circ\text{C}$, $V_{CC}=6\text{V}$, $f=1\text{KHZ}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	TEST CIRCUIT
Quiescent Circuit Current	I_{CC}	$V_{CC}=3\text{V}$		2	3	mA	1
Reference Voltage	V_{REF}	$V_{CC}=3\text{V}$, $R_2>10\text{k}\Omega$	1.20	1.28	1.35	V	4
Start Voltage	$V_{CC(S)}$	30mA current flow to R_a		1.0	1.2	V	2
Saturation Voltage	$V_{(SAT)}$	$V_{CC}=1.8\text{V}$, $R_a=4.7\Omega$		0.2	0.5	V	2
Reference Voltage Characteristics	$\frac{\Delta V_{REF}}{V_{REF}}$ $\frac{\Delta V_{CC}}{V_{CC}}$	$V_{CC}=1.8\text{V}\sim 7.0\text{V}$	-1.25	0.1	1.25	%/V	1
Output Voltage Characteristics	$\frac{\Delta V_A}{V_A}$ $\frac{\Delta V_{CC}}{V_{CC}}$	$V_{CC}=1.8\text{V}\sim 7.0\text{V}$	-1.2	0.1	1.2	%/V	3
Reference Voltage Current Characteristics	$\frac{\Delta V_{REF}}{V_{REF}}$ $\frac{\Delta I}{I}$	$I=1\text{mA}\sim 20\text{mA}$	-0.2	0.01	0.2	%/mA	4
Reference Voltage Temperature Characteristics	$\frac{\Delta V_{REF}}{V_{REF}}$ $\frac{\Delta T_A}{T_A}$	$T_A=-20\text{~}+60^\circ\text{C}$, $V_{CC}=3.0\text{V}$		0.01		%/ $^\circ\text{C}$	4

TEST CIRCUIT

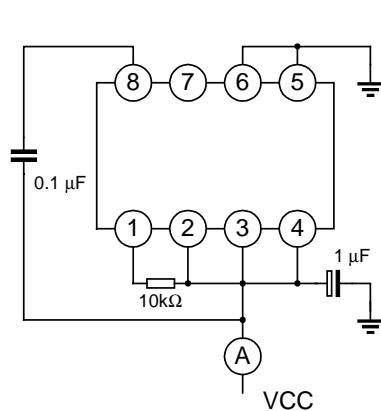


FIG.1 (I_{CC} , $\frac{\Delta V_{REF}}{V_{REF}}$, $\frac{\Delta V_{CC}}{V_{CC}}$)

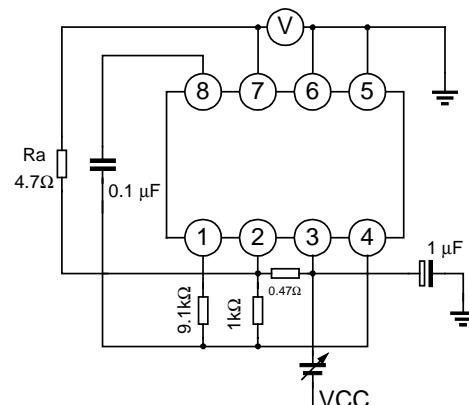


FIG 2 ($V_{CC(S)}$, V_{SAT})

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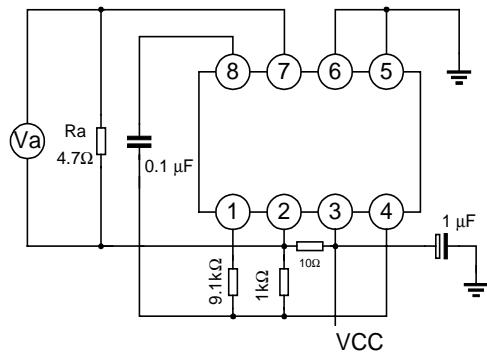


FIG.3

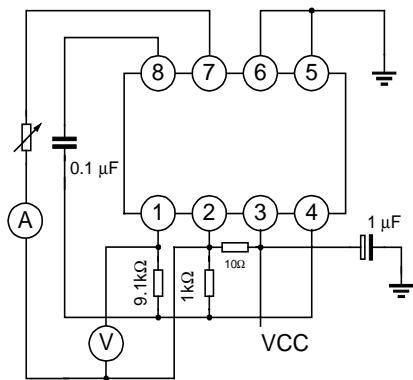


FIG 4