

EMV9GT Series TCXO

- 8 pin DIL package with trimmer
- Wide frequency range: 27.0MHz to 200.0MHz
- Supply voltage 3.3 Volts
- Frequency stability from ±1ppm over -30 to +75°C
- **RoHS** compliant

DESCRIPTION

EMV9GT series TCXOs are packaged in an 8 pin DIL package with mechanical trimmer. With squarewave (CMOS) output, tolerances are available from ± 1.0 ppm over -30° to +75°C. The part has a 0.01μ F decoupling capacitor built in.

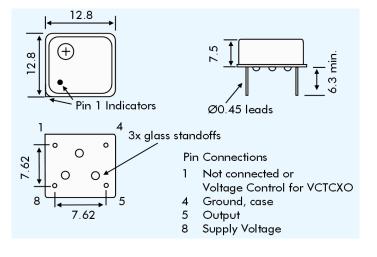
SPECIFICATION

Product Series C	Code					
	TCXO:	EMV9GT				
	VCTCXO:	VEMV9GT				
Frequency Rang	e:	27.0MHz to 200.0MHz				
Output Wavefor	m:	Squarewave, HCMOS				
Initial Calibratio	n Tolerance	•				
Model	s without trimmer:	<±2.0ppm				
Model	s with trimmer:	<±1.0ppm				
Standard Freque	encies:	30.0, 32.768, 38.880, 40.0,				
		50.0, 54.0, 64.0, 65.536,				
		77.76, 80.0, 128.0, 160.0				
		and 200.0MHz				
		(Partial list)				
Operating Temp		See table				
Mechanical Free		±3.0ppm minimum				
Frequency Stabi	,					
vs. Ag		±1.0 ppm max. first year				
	ltage Change:	± 0.3 ppm max. $\pm 5\%$ change				
	ad Change:	± 0.3 ppm max. $\pm 10\%$ change				
vs. Re	flow (SMD type):	±1.0ppm max. for one reflow				
		(Measured after 24 hours)				
Supply Voltage:		+3.3 Volts				
Output Logic Le	vels:	Logic High: 90% Vdd min.				
		Logic Low: 10% Vdd max.				
Current Consum		40mA maximum				
Rise and Fall Tin	nes:	10ns typical				
Duty Cycle:		50%±10% standard,				
Start-up Time:		5ms typical, 10ms max.				
Current Consum	iption:	See table below				
Output Load: Storage Temper		15pF				
Storage Lemper	ature:	-55~+125°C				

HCMOS 8 pin DIL, 'V' Group



EMV9GT - OUTLINES AND DIMENSIONS



VEMV9GT VOLTAGE CONTROL SPECIFICATION

Control Voltage:	Standard = +1.5±1.0Volts for all input voltages. (Contact technical sales if +2.5±2.0 Volts is required.)
Frequency Deviation:	± 6.0 ppm min. (Vcon = $+4.5V \pm 1.0V$)
Slope Polarity:	Positive (increase of control voltage increases output frequency.)
Input Impedance:	2MΩ minimum
Modulation Bandwidth:	25kHz minimum
Linearity:	±10% maximum

SSB PHASE NOISE at 25°C

(Offset		100Hz	1kHz	10kHz	100kHz
Part = EMV9GT33	at 77.760Mhz (dBc/Hz)	-80	-110	-135	-130	-132
	at 155.520Mhz (dBc/Hz)	-80	-110	-125	-120	-125

PART NUMBERING SCHEDULE

Example:	EMV9	GT3	3-200.	.00-2	.5/-30	+75
Series Description TCXO = EMV9GT* VCTCXO = VEMV9G	ЭT					
Supply Voltage 33 = 3.3 \						
Frequency (MHz) Stability over OTR (± Operating Temperat Lower and upper lim	ure Ran	ige (C	PTR) (°C)			

* Note, 'G' indicates RoHS Compliant part

FREQUENCY STABILITY

Stability (ppm)		±0.5	±1.0	±1.5	±2.0	±2.5	±3.0	
Temp. Range (°C)	0~+50	~	~	~	~	~	~	
	-10 ~ +60	ASK	~	~	~	~	~	
	-20 ~ +70	Х	~	~	~	~	~	
	-30 ~ +75	х	~	~	~	~	~	
	-40 ~ +85	Х	х	Х	ASK	ASK	~	