

Description

The 74LVC1G04 is a single inverter gate with a standard totem pole output. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using $I_{\rm OFF}$. The $I_{\rm OFF}$ circuitry disables the output preventing damaging current backflow when the device is powered down.

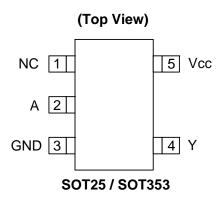
The gate performs the positive Boolean function:

$$Y = \overline{A}$$

Features

- Wide Supply Voltage Range from 1.65 to 5.5V
- ± 24mA Output Drive at 3.3V
- CMOS low power consumption
- I_{OFF} Supports Partial-Power-Down Mode Operation
- Inputs accept up to 5.5V
- ESD Protection Exceeds JESD 22
- 200-V Machine Model (A115-A)
- 2000-V Human Body Model (A114-A)
- Latch-Up Exceeds 100mA per JESD 78, Class II
- Range of Package Options
- · Direct Interface with TTL Levels
- SOT25 and SOT353: Assembled with "Green" Molding Compound (no Br, Sb)
- Lead Free Finish/ RoHS Compliant (Note 1)

Pin Assignments



Applications

- Voltage Level Shifting
- · General Purpose Logic
- Power Down Signal Isolation
- Wide array of products such as.
 - PCs, networking, notebooks, netbooks, PDAs
 - Computer peripherals, hard drives, CD/DVD ROM
 - TV, DVD, DVR, set top box
 - Cell Phones, Personal Navigation / GPS
 - MP3 players ,Cameras, Video Recorders

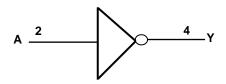
Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/products/lead_free.html.



Pin Descriptions

Pin Name	Pin NO.	Description
NC	1	No Connection
Α	2	Data Input
GND	3	Ground
Y	4	Data Output
Vcc	5	Supply Voltage

Logic Diagram



Function Table

Inputs	Output
Α	Υ
Н	L
L	Н



Absolute Maximum Ratings (Note 2)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD MM	Machine Model ESD Protection	200	V
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage applied to output in high impedance or I _{OFF} state	-0.5 to 6.5	V
Vo	Voltage applied to output in high or low state.	-0.3 to V _{CC} +0.5	V
I _{IK}	Input Clamp Current V _I <0	-50	mA
I _{OK}	Output Clamp Current	-50	mA
lo	Continuous output current	±50	mA
	Continuous current through Vdd or GND	±100	mA
TJ	Operating Junction Temperature	-40 to 150	°C
T _{STG}	Storage Temperature	-65 to 150	°C

Notes: 2. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.



Recommended Operating Conditions (Note 3)

Symbol		Parameter	Min	Max	Unit	
\/	Operating Voltage	Operating	1.65	5.5	V	
Vcc	Operating voitage	Data retention only	1.5		V	
		V _{CC} = 1.65 V to 1.95 V	0.65 X V _{CC}			
	High lovel Input Voltage	V _{CC} = 2.3 V to 2.7 V	1.7		V	
V_{IH}	High-level Input Voltage	V _{CC} = 3 V to 3.6 V	2		V	
		V _{CC} = 4.5 V to 5.5 V	0.7 X V _{CC}			
		V _{CC} = 1.65 V to 1.95 V		0.35 X V _{CC}		
		V _{CC} = 2.3 V to 2.7 V		0.7	.,	
V_{IL}	Low-level input voltage	V _{CC} = 3 V to 3.6 V		0.8	V	
		V _{CC} = 4.5 V to 5.5 V		0.3 X V _{CC}		
VI	Input Voltage	,	0	5.5	V	
Vo	Output Voltage		0	Vcc	V	
		V _{CC} = 1.65 V		-4		
		V _{CC} = 2.3 V		-8		
I _{OH}	High-level output current			-16	mA	
		V _{CC} = 3 V		-24		
		V _{CC} = 4.5 V		-32		
		V _{CC} = 1.65 V		4		
		V _{CC} = 2.3 V		8		
I_{OL}	Low-level output current	V 2.V		16	mA	
		$V_{CC} = 3 V$		24		
		V _{CC} = 4.5 V		32		
		$V_{CC} = 1.8 \text{ V} \pm 0.15 \text{V}, 2.5 \text{ V} \pm 0.2 \text{ V}$		20		
$\Delta t/\Delta V$	Input transition rise or fall rate	$V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$		10	ns/V	
	Tale	V _{CC} = 5 V ± 0.5 V		5		
T _A	Operating free-air temperature		-40	85	٥С	

Notes: 3. Unused inputs should be held at Vcc or Ground.



Electrical Characteristics (All typical values are at Vcc = 3.3V, T_A = 25°C)

Over recommended free-air temperature range (unless otherwise noted)

Symbol	Parameter	Test Conditions	Vcc	Min	Тур.	Max	Unit
		I _{OH} = -100μA	1.65V to 5.5V	V _{CC} - 0.1			
		I _{OH} = -4mA	1.65V	1.2			
\/	High Level Output	I _{OH} = -8mA	2.3V	1.9			V
V _{OH}	Voltage	I _{OH} = -16mA	-3V	2.4			V
		I _{OH} = -24mA	30	2.3			
		I _{OH} = -32mA	4.5V	3.8			
		I _{OL} = 100μA	1.65V to 5.5V			0.1	
		I _{OL} = 4mA	1.65V			0.45	
\ /	High-level Input	I _{OL} = 8mA	2.3V			0.3	V
V_{OL}	Voltage	I _{OL} = 16mA	-3V			0.4	V
		I _{OL} = 24mA	-3v			0.55	
		I _{OL} = 32mA	4.5			0.55	
lį	Input Current	V _I = 5.5 V or GND	0 to 5.5V			± 5	μA
I _{OFF}	Power Down Leakage Current	V_I or $V_O = 5.5V$	0			± 10	μA
Icc	Supply Current	V _I = 5.5V of GND I _O =0	1.65V to 5.5V			10	μΑ
ΔI _{CC}	Additional Supply Current	Input at V _{CC} –0.6 V	3 V to 5.5V			500	μA
Ci	Input Capacitance	$V_i = V_{CC} - \text{or GND}$	3.3		4		pF
θЈΑ	Thermal Resistance	SOT25	(Note 4)		204		°C/W
OJA	Junction-to-Ambient	SOT353	(Note 4)		371		°C/W
θ_{JC}	Thermal Resistance	SOT25	(Note 4)		52		°C/W
OJC	Junction-to-Case	SOT353	(Note 4)		143		°C/W

Notes: 4. Test condition for SOT25 and SOT353: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



Switching Characteristics

Over recommended free-air temperature range, CL = 15pF (see Figure 1)

Parameter	From	ТО	Vcc = 1.8 V ± 0.15V		TO $\pm 0.15V$ $\pm 0.2V$			Vcc = 3.3 V ± 0.3V		Vcc = 5 V ± 0.5V		Unit
	(Input)	(OUTPUT)	Min	Max	Min	Max	Min	Max	Min	Max		
t _{pd}	Α	Υ	2.0	6.4	1.0	4.2	0.7	3.3	0.7	3.1	ns	

Over recommended free-air temperature range, CL = 30 or 50pF as noted (see Figure 2)

Parameter	From	TO (OUTPUT)	Vcc = ± 0.	_	Vcc = ± 0	2.5 V .2V	Vcc = ± 0			= 5 V 0.5V	Unit
	(Input)	(OUTPUT)	Min	Max	Min	Max	Min	Max	Min	Max	
t _{pd}	А	Y	3.0	7.5	1.4	5.2	1.0	4.2	1.0	3.7	ns

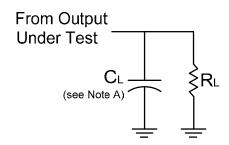
Operating Characteristics

 $T_A = 25 \, {}^{\circ}C$

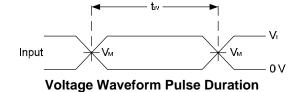
	Parameter C		Vcc = 1.8 V	Vcc = 2.5 V	Vcc = 3.3 V	Vcc = 5 V	Unit
C _{pd}	Power dissipation capacitance	f = 10 MHz	16	18	18	20	pF

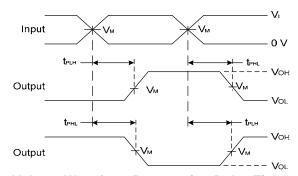


Parameter Measurement Information



Vcc	In	puts	V _M	CL	RL
•••	VI	t _r /t _f	▼ IVI	O _L	IXL
1.8V±0.15V	V _{CC}	≤2ns	V _{CC} /2	15pF	1ΜΩ
2.5V±0.2V	V _{CC}	≤2ns	V _{CC} /2	15pF	1ΜΩ
3.3V±0.3V	3V	≤2.5ns	1.5V	15pF	1ΜΩ
5V±0.5V	V _{CC}	≤2.5ns	V _{CC} /2	15pF	1ΜΩ





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1. Load Circuit and Voltage Waveforms

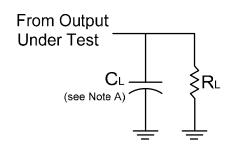
Notes: A. Includes test lead and test apparatus capacitance.

B. All pulses are supplied at pulse repetition rate \leq 10 MHz.

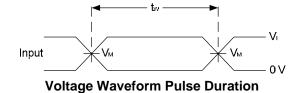
C. tpLH and tpHL are the same as tpD.

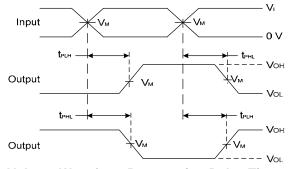


Parameter Measurement Information (Continued)



Vcc	Inp	outs	V _M	CL	R_L
700	VI	t _r /t _f	V IVI	O _L	IXL
1.8V±0.15V	Vcc	≤2ns	V _{CC} /2	30pF	1ΚΩ
2.5V±0.2V	Vcc	≤2ns	V _{CC} /2	30pF	500Ω
3.3V±0.3V	3V	≤2.5ns	1.5V	50pF	500Ω
5V±0.5V	Vcc	≤2.5ns	V _{CC} /2	50pF	500Ω





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 2. Load Circuit and Voltage Waveforms

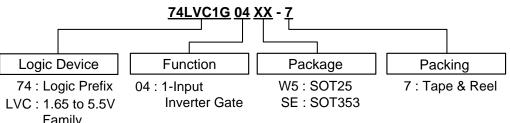
Notes: A. Includes test lead and test apparatus capacitance.

B. All pulses are supplied at pulse repetition rate \leq 10 MHz.

C. tpLH and tpHL are the same as tpD.



Ordering Information



Family 1G : One gate

	Device	Package	Packaging	7" Tape a	nd Reel
	Device	Code	(Note 5)	Quantity	Part Number Suffix
P	74LVC1G04W5-7	W5	SOT25	3000/Tape & Reel	-7
Pb ,	74LVC1G04SE-7	SE	SOT353	3000/Tape & Reel	-7

Notes: 5. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

Marking Information

(Top View)

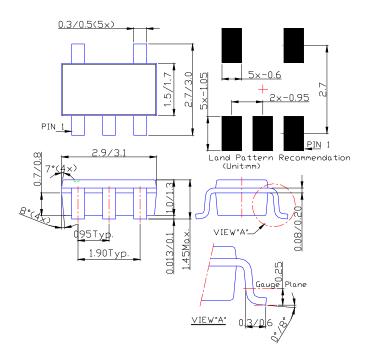
a~z : 27~52 week; z represents

Part Number	Package	Identification Code
74LVC1G04W5	SOT25	UU
74LVC1G04SE	SOT353	UU

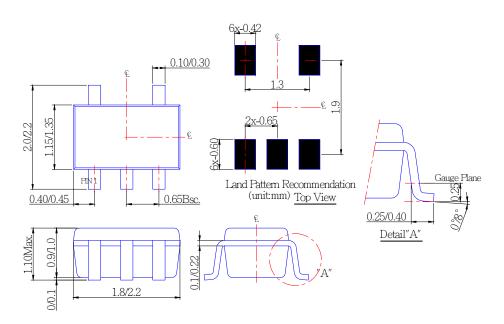


Package Outline Dimensions (All Dimensions in mm)

(1) Package Type: SOT25



(2) Package Type: SOT353





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