

SANYO Semiconductors DATA SHEET

EFC4601R — General-Purpose Switching Device Applications

Features

- · 2.5V drive.
- Best suited for LiB charging and discharging switch.
- · Common-drain type.

Specifications

Absolute Maximum Ratings at Ta=25°C

| Parameter | Symbol | Conditions | Ratings | Unit |
|--------------------------|------------------|---|-------------|------|
| Source-to-Source Voltage | V _{SSS} | | 24 | V |
| Gate-to-Source Voltage | VGSS | | ±12 | V |
| Source Current (DC) | IS | | 6 | Α |
| Source Current (Pulse) | ISP | PW≤10μs, duty cycle≤1% | 60 | Α |
| Total Dissipation | PT | When mounted on ceramic substrate (5000mm²×0.8mm) | 1.6 | W |
| Channel Temperature | Tch | | 150 | °C |
| Storage Temperature | Tstg | | -55 to +150 | °C |

Electrical Characteristics at Ta=25°C

| Parameter | Symbol | Conditions - | | Ratings | | | Unit |
|---|-----------------------|---|----------------|---------|-----|-----|-----------|
| Farameter | Syllibol | | | min | typ | max | Offic |
| Source-to-Source Breakdown Voltage | V _(BR) SSS | I _S =1mA, V _{GS} =0V | Test Circuit 1 | 24 | | | V |
| Zero-Gate Voltage Source Current | Isss | Vss=20V, Vgs=0V | Test Circuit 1 | | | 1 | μΑ |
| Gate-to-Source Leakage Current | IGSS | V _{GS} =±8V, V _{SS} =0V | Test Circuit 2 | | | ±10 | μΑ |
| Cutoff Voltage | V _{GS} (off) | V _{SS} =10V, I _S =1mA | Test Circuit 3 | 0.5 | | 1.3 | V |
| Forward Transfer Admittance | yfs | VSS=10V, IS=3A | Test Circuit 4 | 3.7 | 6.2 | | S |
| Static Source-to-Source On-State Resistance | Rss(on)1 | I _S =3A, V _{GS} =4.5V | Test Circuit 5 | 23.5 | 34 | 44 | mΩ |
| | Rss(on)2 | IS=3A, VGS=4.0V | Test Circuit 5 | 25 | 36 | 47 | mΩ |
| | Rss(on)3 | IS=3A, VGS=3.7V | Test Circuit 5 | 27 | 38 | 49 | $m\Omega$ |
| | RSS(on)4 | I _S =3A, V _{GS} =3.1V | Test Circuit 5 | 27 | 42 | 55 | mΩ |
| | Rss(on)5 | IS=3A, VGS=2.5V | Test Circuit 5 | 30 | 50 | 70 | mΩ |

Marking: FC Continued on next page.

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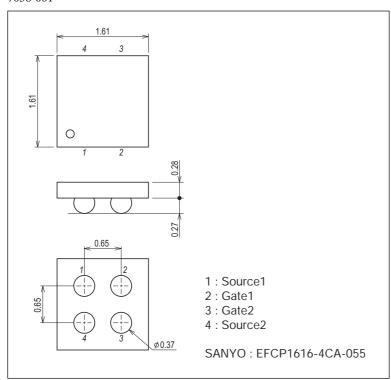
EFC4601R

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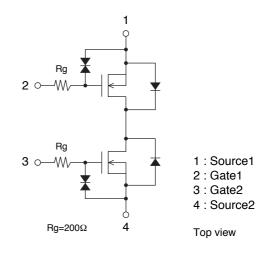
| Parameter | Symbol | Conditions | | Ratings | | | Unit |
|----------------------------------|----------------------|---|----------------|---------|-----|-----|------|
| | | | | min | typ | max | Oill |
| Input Capacitance | Ciss | VSS=10V, f=1MHz | Test Circuit 8 | | 680 | | pF |
| Output Capacitance | Coss | V _{SS} =10V, f=1MHz | Test Circuit 8 | | 125 | | pF |
| Reverse Transfer Capacitance | Crss | VSS=10V, f=1MHz | Test Circuit 8 | | 42 | | pF |
| Turn-ON Delay Time | t _d (on) | See specified Test Circuit. | Test Circuit 7 | | 28 | | ns |
| Rise Time | tr | See specified Test Circuit. | Test Circuit 7 | | 105 | | ns |
| Turn-OFF Delay Time | t _d (off) | See specified Test Circuit. | Test Circuit 7 | | 220 | | ns |
| Fall Time | t _f | See specified Test Circuit. | Test Circuit 7 | | 190 | | ns |
| Total Gate Charge | Qg | V _{SS} =10V, V _{GS} =4.5V, I _S =6A | | | 9.3 | | nC |
| Forward Source-to-Source Voltage | VF(S-S) | IS=6A, VGS=0V | Test Circuit 6 | | 1 | 1.2 | V |

Package Dimensions

unit : mm (typ) 7058-001



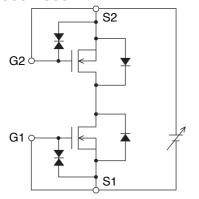
Electrical Connection



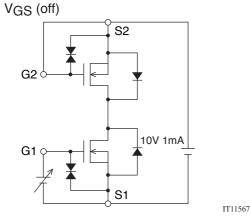
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Test Circuits are example of measuring FET1 side

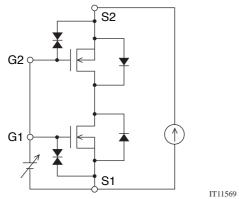
Test Circuit 1 VSSS / ISSS



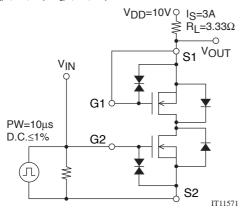
Test Circuit 3



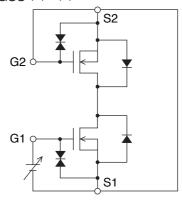
Test Circuit 5 RSS (on)



Test Circuit 7 t_d (on), t_r, t_d (off), t_f



Test Circuit 2 IGSS (+) / (-)

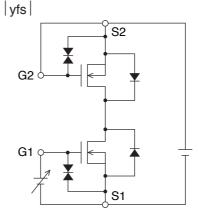


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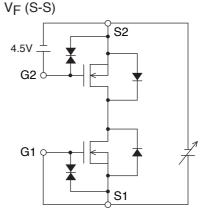
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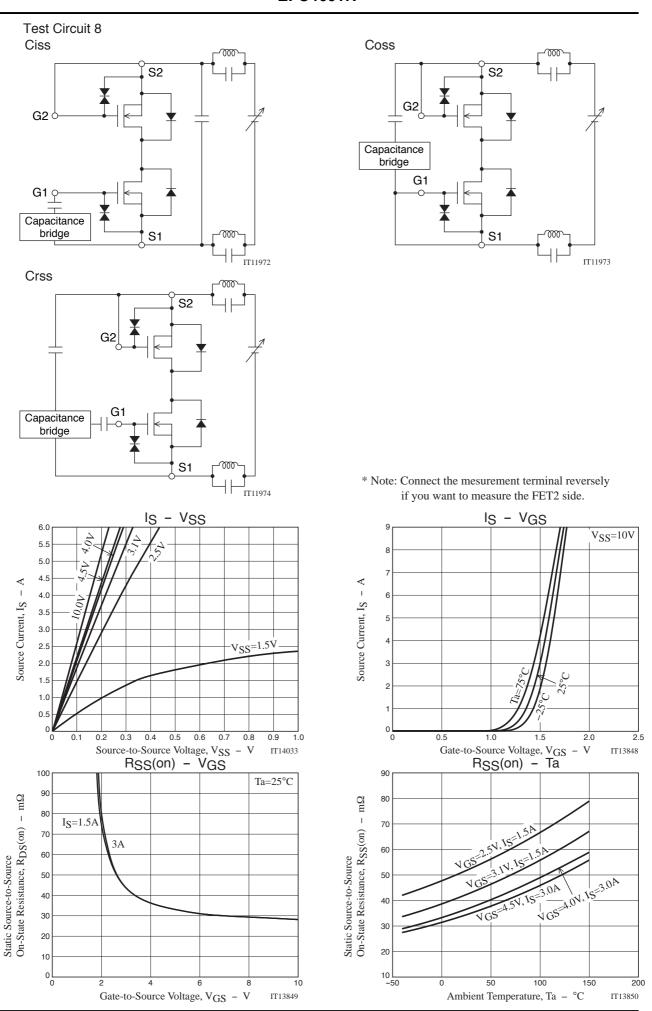
Test Circuit 4

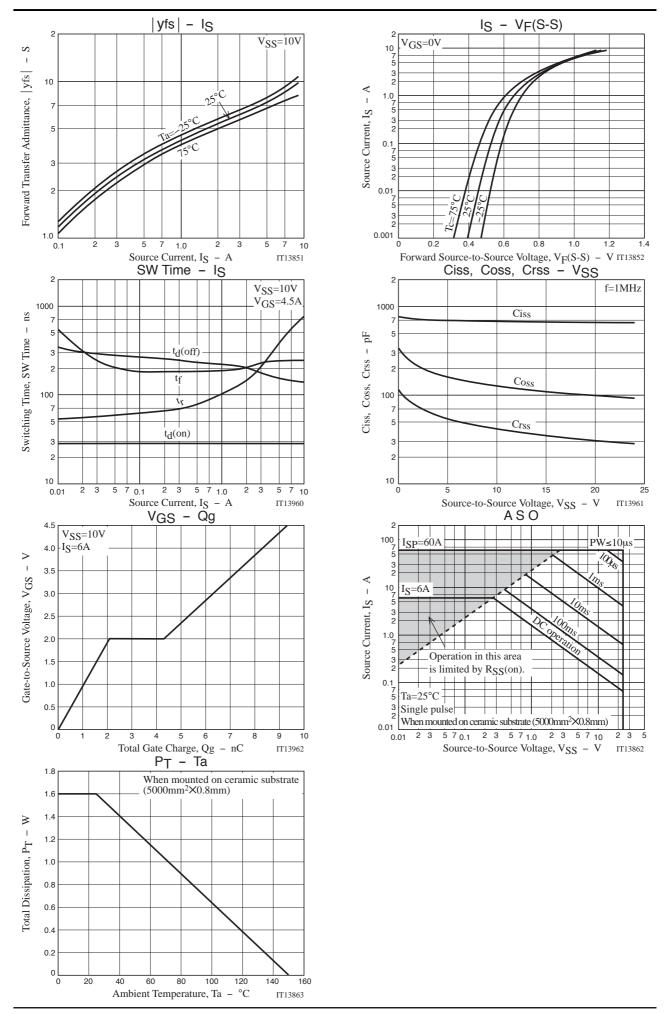


Test Circuit 6



^{*} Note: Connect the mesurement terminal reversely if you want to measure the FET2 side.





Note on usage: Since the EFC4601R is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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