Silicon Epitaxial Planer Schottky Barrier Diode

Features

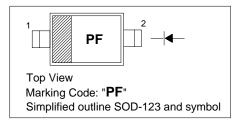
- · High reliablility
- · Low forward voltage

Application

- · For switching power supplies
- · Battery protection against reversal current

PINNING

| PIN | DESCRIPTION |
|-----|-------------|
| 1 | Cathode |
| 2 | Anode |



Absolute Maximum Ratings (T_a = 25 °C)

| Parameter | Symbol | Value | Unit |
|--|------------------|------------|------|
| Reverse Voltage (Repetitive Peak) | V_{RM} | 40 | V |
| Reverse Voltage (DC) | V _R | 40 | V |
| Average Rectified Forward Current | Io | 1 | А |
| Peak Forward Surge Current (60Hz / cyc.) | I _{FSM} | 30 | А |
| Junction Temperature | TJ | 150 | °C |
| Storage Temperature | T _{stg} | -40 to 150 | °C |

Electrical Characteristics (T_a = 25 °C)

| Parameter | Symbol | Min. | Max. | Unit |
|--|----------------|------|------|------|
| Forward Voltage at I _F = 1 A | V _F | - | 0.51 | V |
| Reverse Current at V _R = 40 V | I _R | - | 30 | μΑ |







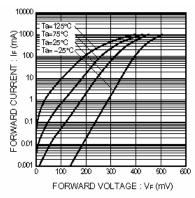


Fig. 1 Forward Temperature Characteristics

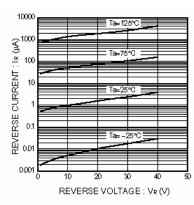


Fig.2 Reverse Temperature Characteristics

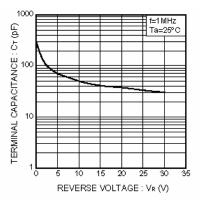
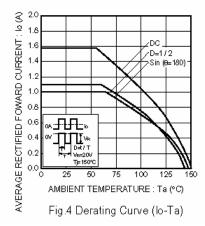
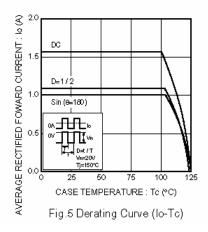


Fig.3 Capacitance Between Terminals Characteristics





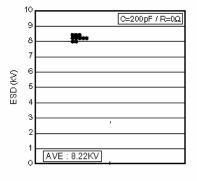


Fig.6 ESD

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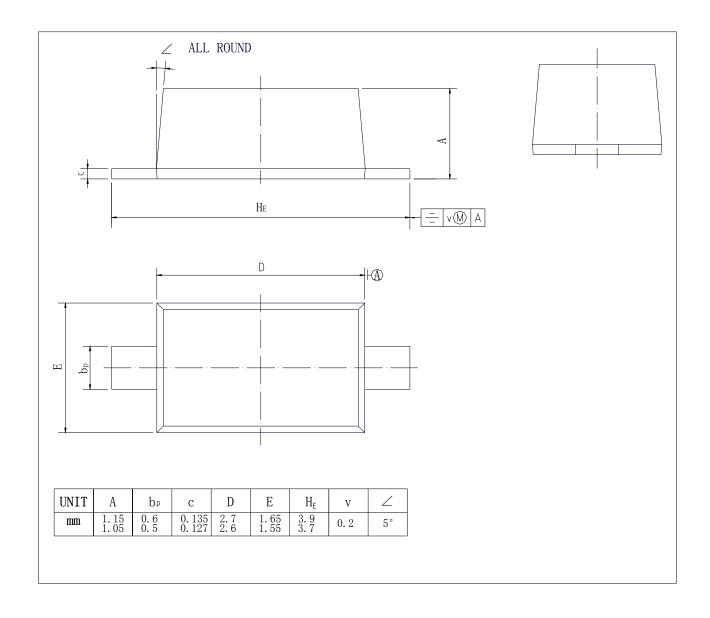


Dated: 22/04/2006

PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SOD-123





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