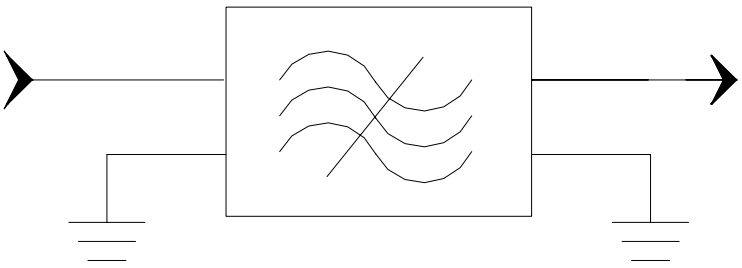
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	84.72	84.8	84.88
Insertion Loss	dB		17.5	19
1dB Bandwidth	MHz	7.1	7.2	
3 dB Bandwidth	MHz	7.4	7.78	
40 dB Bandwidth	MHz		9.63	9.7
Group delay Variation(f0±2.84MHz)	nsec		30	100
Phase Linearity (f0±2.84MHz)	degree		3	5
Passband Variation	dB		1	1.2
Absolute Delay	usec		2	2.2
Ultimate Rejection(f0±15MHz)	dB	54	60	
Subatrate Material		YZ		
Ambient Temperature	°C	25		
Package Size	DIP2712 (27.0x12.8x4.7mm^3)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

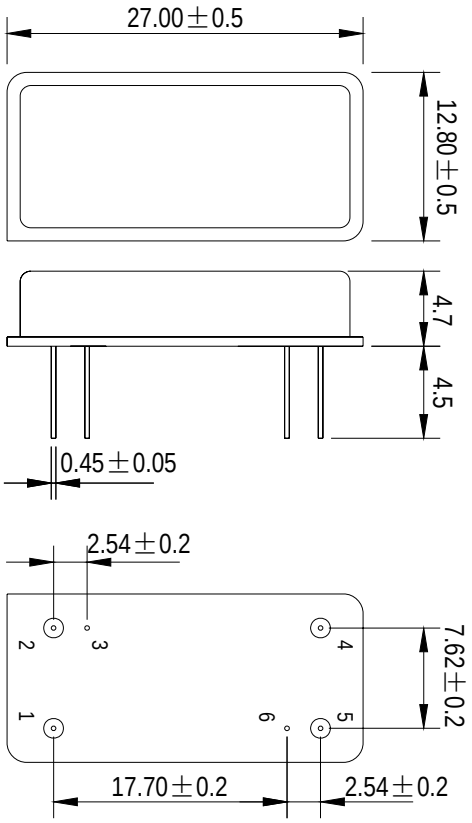
Matching Configuration



Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

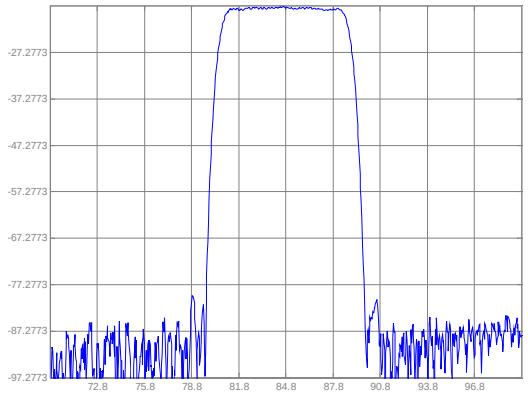
Package Dimension



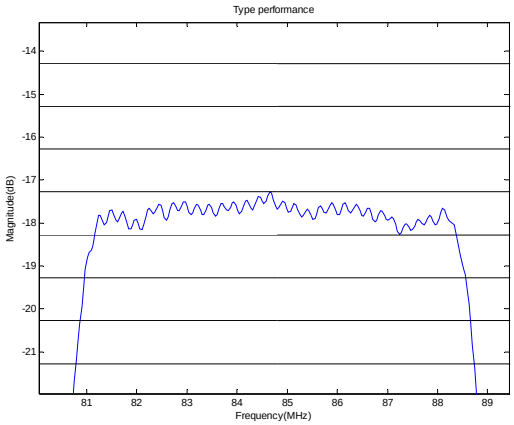
Pin 1: input
Pin 5:output
Others:Grounded

Typical Performance

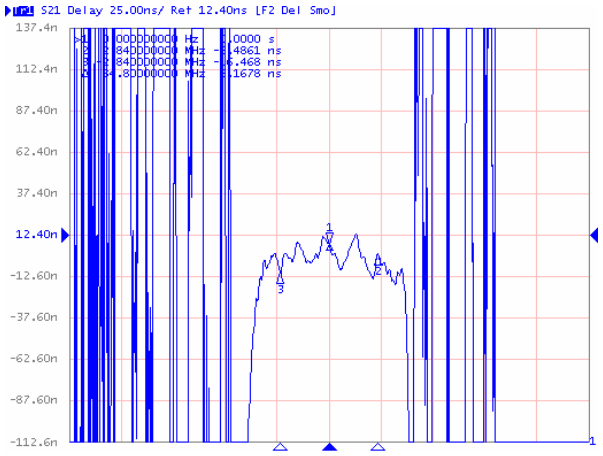
Frequency Respond



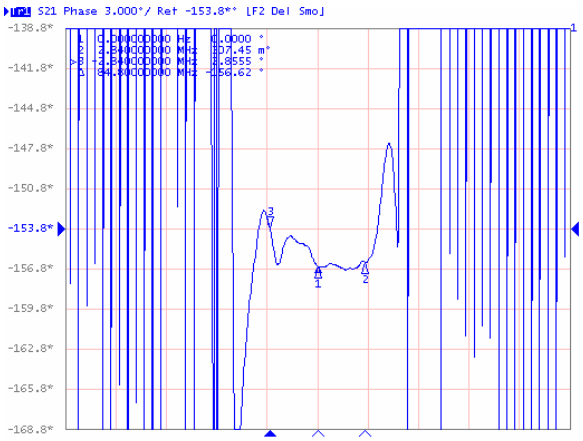
Passband Respond



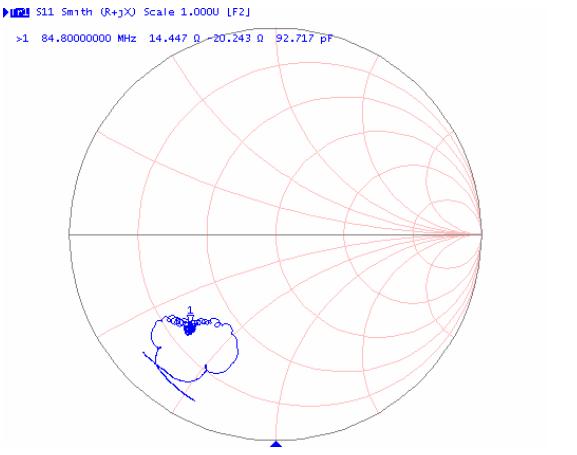
Group Delay Variation(f0±2.84MHz,smooth)



Phase Linearity(f0±2.84MHz,smooth)



Simth Chart S11



Simth Chart S22