

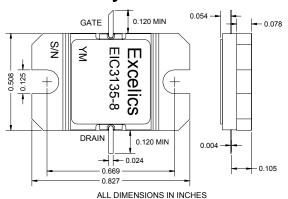
EIC3135-8

UPDATED 01/10/2005

3.10-3.50 GHz 8W Internally Matched Power FET

FEATURES

- 3.10-3.50 GHz Bandwidth
- Input/Output Impedance Matched to 50 Ohms
- +39.5 dBm Output Power at 1dB Compression
- 13.0 dB Power Gain at 1dB Compression
- 37% Power Added Efficiency
- -46 dBc IM3 at Po = 28.5 dBm SCL
- Hermetic Metal Flange Package





Caution! ESD sensitive device.

ELECTRICAL CHARACTERISTICS (T_a = 25°C)

SYMBOL	PARAMETERS/TEST CONDITIONS ¹	MIN	TYP	MAX	UNITS
P _{1dB}	Output Power at 1dB Compression $f = 3.10-3.50GHz$ $V_{DS} = 10 \text{ V}, I_{DSQ} \approx 2200\text{mA}$	38.5	39.5		dBm
G _{1dB}	Gain at 1dB Compression $f = 3.10-3.50GHz$ $V_{DS} = 10 \text{ V}, I_{DSQ} \approx 2200\text{mA}$	12.0	13.0		dB
ΔG	Gain Flatness $f = 3.10-3.50GHz$ $V_{DS} = 10 \text{ V}, I_{DSQ} \approx 2200\text{mA}$			±0.6	dB
PAE	Power Added Efficiency at 1dB Compression $V_{DS} = 10 \text{ V}, I_{DSQ} \approx 2200 \text{mA}$ f = 3.10-3.50GHz		37		%
Id_{1dB}	Drain Current at 1dB Compression f = 3.10-3.50GHz		2300	2800	mA
IM3	Output 3rd Order Intermodulation Distortion Δf = 10 MHz 2-Tone Test; Pout = 28.5 dBm S.C.L ² V_{DS} = 10 V, I_{DSQ} ≈ 65% IDSS f = 3.50GHz	-43	-46		dBc
I _{DSS}	Saturated Drain Current $V_{DS} = 3 \text{ V}, V_{GS} = 0 \text{ V}$		4000	5000	mA
V_P	Pinch-off Voltage $V_{DS} = 3 \text{ V}, I_{DS} = 40 \text{ mA}$		-2.5	-4.0	V
R _{TH}	Thermal Resistance ³		3.5	4.0	°C/W

Note: 1) Tested with 100 Ohm gate resistor.

2) S.C.L. = Single Carrier Level.

3) Overall Rth depends on case mounting.

ABSOLUTE MAXIMUM RATINGS FOR CONTINUOUS OPERATION^{1,2}

SYMBOL	CHARACTERISTIC	CTERISTIC VALUE	
V_{DS}	Drain to Source Voltage	10 V	
V_{GS}	Gate to Source Voltage	-4.5 V	
I _{DS}	Drain Current	IDSS	
I _{GSF}	Forward Gate Current	80 mA	
P _{IN}	Input Power	@ 3dB compression	
P _T	Total Power Dissipation	32 W	
T _{CH}	Channel Temperature	150°C	
T _{STG}	Storage Temperature	-65/+150°C	

Note: 1. Exceeding any of the above ratings may result in permanent damage.

2. Exceeding any of the above ratings may reduce MTTF below design goals.