

# **RJK03E2DNS**

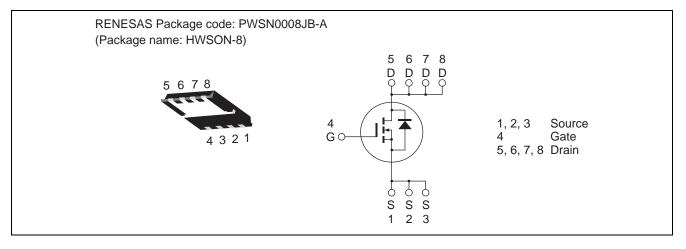
Silicon N Channel Power MOS FET Power Switching

REJ03G1904-0200 Rev.2.00 Apr 06, 2010

#### Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance
- $R_{DS(on)} = 6.9 \text{ m}\Omega \text{ typ.}$  (at  $V_{GS} = 10 \text{ V}$ )
- Pb-free
- Halogen-free

#### Outline



## **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	ID	16	А
Drain peak current	Note1 I <sub>D(pulse)</sub>	64	А
Body-drain diode reverse drain current	I <sub>DR</sub>	16	А
Avalanche current	I <sub>AP</sub> Note 2	8	А
Avalanche energy	E <sub>AR</sub> Note 2	6.4	mJ
Channel dissipation	Pch Note3	12.5	W
Channel to case thermal impedance	θch-c <sup>Note3</sup>	10.0	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1% 2. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$ 

2. Value at 101 = 2



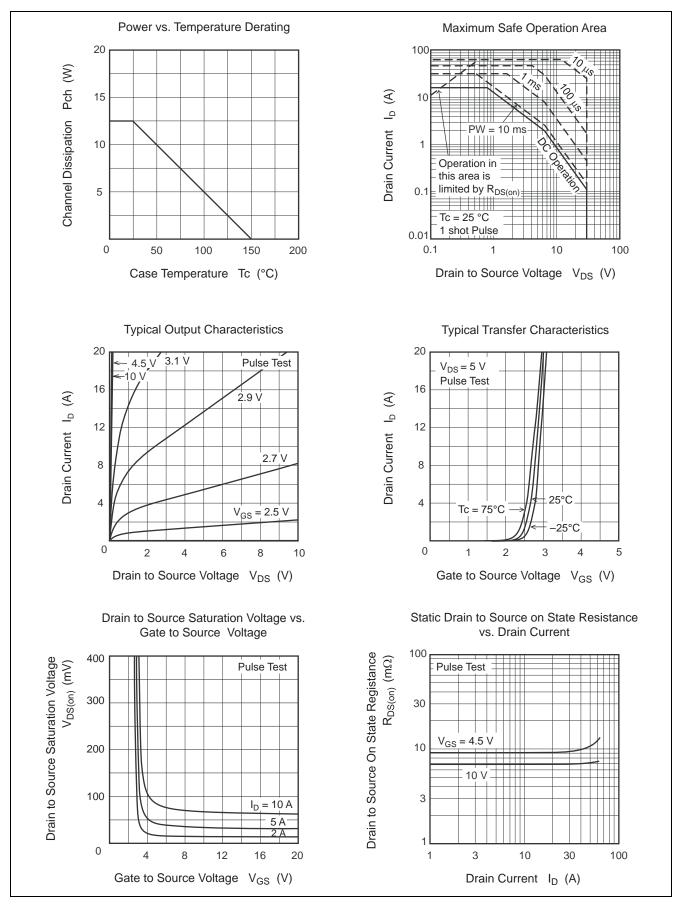
## **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	30	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	—	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	—	1	μΑ	$V_{DS} = 30 V, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.2	—	2.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	6.9	9.0	mΩ	$I_D = 8 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>	_	9.1	12.7	mΩ	$I_D = 8 \text{ A}, V_{GS} = 4.5 \text{ V}^{Note4}$
Forward transfer admittance	y <sub>fs</sub>	_	33	—	S	$I_D = 8 A, V_{DS} = 5 V^{Note4}$
Input capacitance	Ciss	_	1100	1540	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	157	—	pF	V <sub>GS</sub> = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	78	—	pF	
Gate Resistance	Rg	_	1.2	2.4	Ω	
Total gate charge	Qg	_	7.3	—	nC	$V_{DD} = 10 V$ $V_{GS} = 4.5 V$ $I_D = 16 A$
Gate to source charge	Qgs	_	3.1	—	nC	
Gate to drain charge	Qgd	_	1.8	—	nC	
Turn-on delay time	t <sub>d(on)</sub>	_	8.6	—	ns	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 8 \text{ A}$
Rise time	tr	_	3.9	—	ns	$V_{DD} \cong 10 \text{ V}$ $R_{L} = 1.25 \Omega$ $Rg = 4.7 \Omega$
Turn-off delay time	t <sub>d(off)</sub>		33		ns	
Fall time	t <sub>f</sub>		4.7	—	ns	
Body–drain diode forward voltage	$V_{DF}$		0.82	1.07	V	$I_F = 16 \text{ A}, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery	t <sub>rr</sub>		13		ns	$I_{\rm F}$ =16 A, $V_{\rm GS}$ = 0
time						di <sub>F</sub> / dt = 100 A/ μs

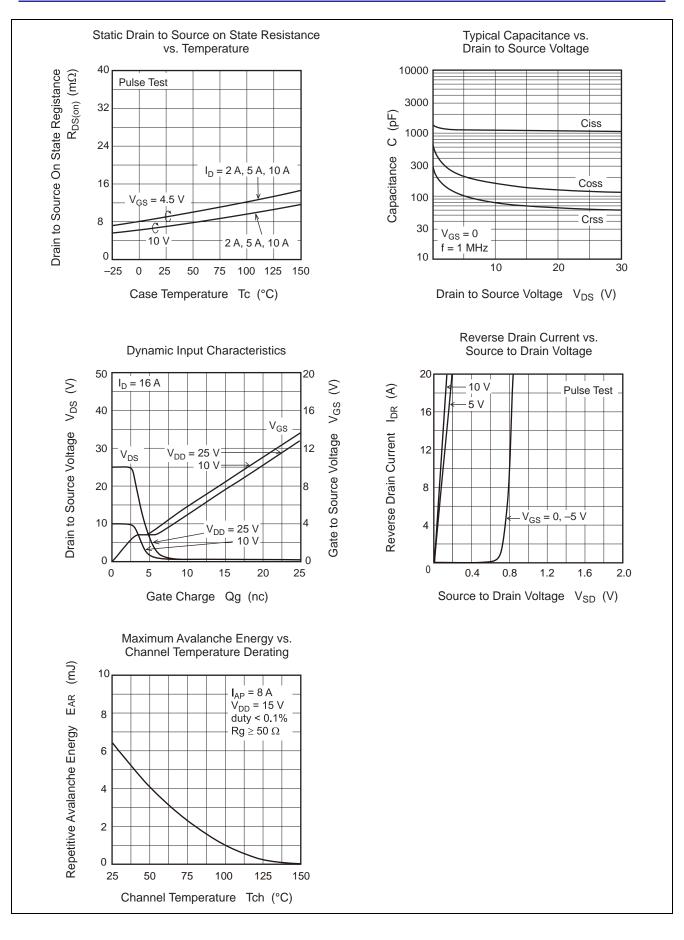
Notes: 4. Pulse test



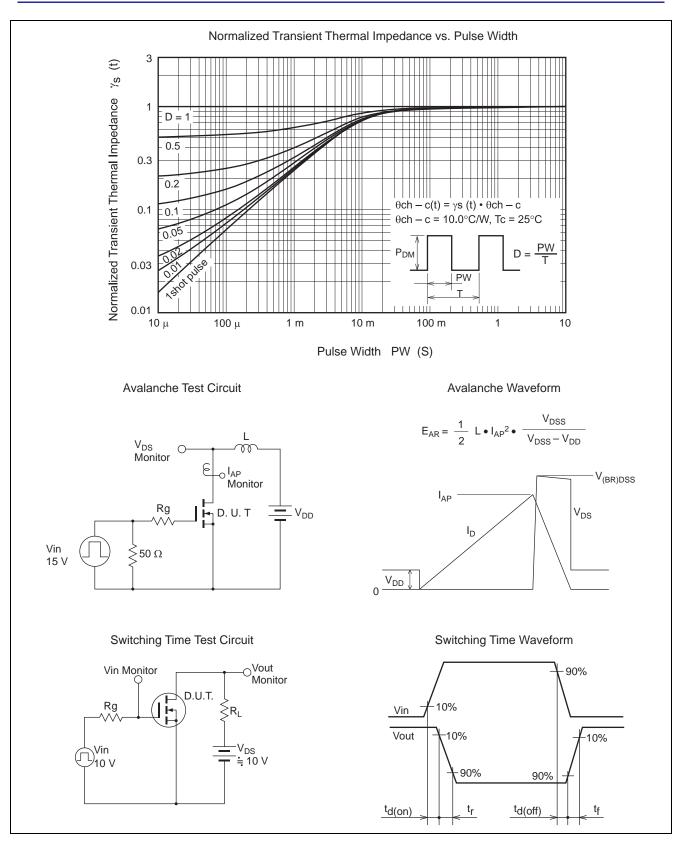
#### **Main Characteristics**



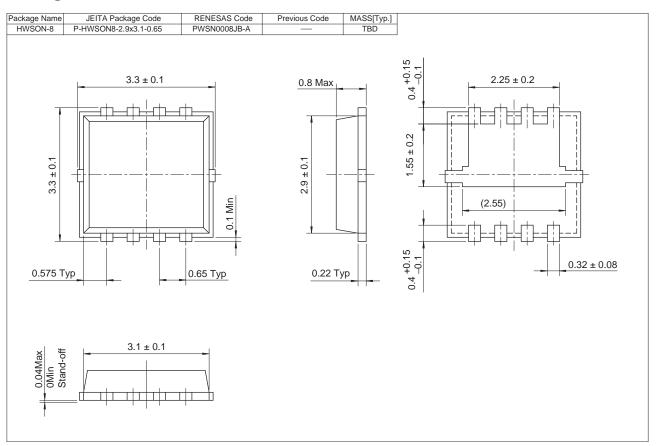








### **Package Dimensions**



## **Ordering Information**

Part No.	Quantity	Shipping Container
RJK03E2DNS-00-J5	5000 pcs	Taping



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