

# Single N-channel MOSFET with schottky diode

ELM16704EA-S

## ■ General description

ELM16704EA-S uses advanced trench technology to provide excellent  $R_{ds(on)}$  and low gate charge.

## ■ Features

- $V_{ds}=30V$
- $I_d=3.6A$  ( $V_{gs}=10V$ )
- $R_{ds(on)} < 65m\Omega$  ( $V_{gs}=10V$ )
- $R_{ds(on)} < 75m\Omega$  ( $V_{gs}=4.5V$ )
- $R_{ds(on)} < 160m\Omega$  ( $V_{gs}=2.5V$ )
- Schottky diode
- $V_{ds(V)}=20V$
- $I_f=1A$
- $V_f < 0.5V@0.5A$

## ■ Maximum absolute ratings

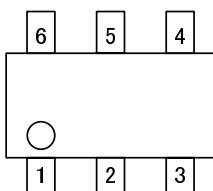
Parameter	Symbol	MOSFET	Schottky	Unit	Note
Drain-source voltage	$V_{ds}$	30		V	
Gate-source voltage	$V_{gs}$	$\pm 12$		V	
Continuous drain current	$I_d$	3.6		A	1
		2.9			
Pulsed drain current	$I_{dm}$	10		A	2
Schottky reverse voltage	$V_{ka}$		20	V	
Continuous forward current	$I_f$		1.5	A	1
			1.0		
Pulsed forward current	$I_{fm}$		10	A	2
Power dissipation	$P_d$	1.39	0.78	W	
		0.89	0.50		
Junction and storage temperature range	$T_j, T_{stg}$	-55 to 150	-55 to 150	°C	

## ■ Thermal characteristics

Parameter (MOSFET)	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$R_{\theta ja}$	70	90	°C/W	1
Maximum junction-to-ambient		102	130	°C/W	
Maximum junction-to-lead	$R_{\theta jl}$	51	80	°C/W	3
Parameter (Schottky)	Symbol	Typ.	Max.	Unit	
Maximum junction-to-ambient	$R_{\theta ja}$	129	160	°C/W	1
Maximum junction-to-ambient		158	200	°C/W	
Maximum junction-to-lead	$R_{\theta jl}$	52	80	°C/W	3

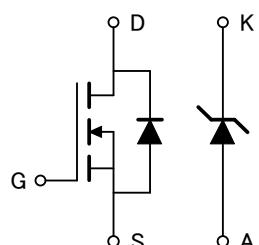
## ■ Pin configuration

SOT-26 (TOP VIEW)



Pin No.	Pin name
1	CATHODE
2	SOURCE
3	GATE
4	DRAIN
5	DRAIN
6	ANODE

## ■ Circuit



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### ■ Electrical characteristics

Ta=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>						
Drain-source breakdown voltage	BVdss	Id=250 μA, Vgs=0V	30			V
Zero gate voltage drain current	Idss	Vds=24V			1	μ A
		Vgs=0V	Tj=55°C		5	
Gate-body leakage current	Igss	Vds=0V, Vgs=±12V			100	nA
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=250 μ A	1.0	1.4	1.8	V
On state drain current	Id(on)	Vgs=4.5V, Vds=5V	10			A
Static drain-source on-resistance	Rds(on)	Vgs=10V		44	65	m Ω
		Id=3.6A	Tj=125°C	64	90	
		Vgs=4.5V, Id=3.4A		53	75	m Ω
		Vgs=2.5V, Id=1A		106	160	m Ω
Forward transconductance	Gfs	Vds=5V, Id=3.6A		11.7		S
Diode forward voltage	Vsd	Is=1A, Vgs=0V		0.81	1.00	V
Max. body-diode continuous current	Is				2.5	A
<b>DYNAMIC PARAMETERS</b>						
Input capacitance	Ciss	Vgs=0V, Vds=15V, f=1MHz		226	270	pF
Output capacitance	Coss			39		pF
Reverse transfer capacitance	Crss			29		pF
Gate resistance	Rg	Vgs=0V, Vds=0V, f=1MHz		1.4	1.7	Ω
<b>SWITCHING PARAMETERS</b>						
Total gate charge	Qg	Vgs=4.5V, Vds=15V, Id=3.6A		3.00	3.60	nC
Gate-source charge	Qgs			1.40		nC
Gate-drain charge	Qgd			0.55		nC
Turn-on delay time	td(on)	Vgs=10V, Vds=15V		2.6		ns
Turn-on rise time	tr			3.2		ns
Turn-off delay time	td(off)		RI=3.9 Ω, Rgen=6 Ω	14.5		ns
Turn-off fall time	tf			2.1		ns
Body diode reverse recovery time	trr	If=3.6A, dl/dt=100A/μ s		10.2	13.0	ns
Body diode reverse recovery charge	Qrr	If=3.6A, dl/dt=100A/μ s		3.8		nC
<b>SCHOTTKY PARAMETERS</b>						
Forward voltage drop	Vf	If=0.5A		0.39	0.50	V
Max. reverse leakage current	Irm	Vr=16V			0.10	mA
		Vr=16V, Tj=125°C			20.00	
Junction capacitance	Ct	Vr=10V		34		pF
Schottky reverse recovery time	trr	If=1A, dl/dt=100A/μ s		5.2	10.0	ns
Schottky reverse recovery charge	Qrr	If=1A, dl/dt=100A/μ s		0.8		nC

### NOTE :

1. The value of Rθja is measured with the device mounted on 1in<sup>2</sup> FR-4 board of 2oz. Copper, in still air environment with Ta=25°C. The value in any given applications depends on the user's specific board design, The current rating is based on the t ≤ 10s thermal resistance rating.
2. Repetitive rating, pulse width limited by junction temperature.
3. The Rθja is the sum of the thermal impedance from junction to lead Rθjl and lead to ambient.
4. The static characteristics in Figures 1 to 6 are obtained using 80μs pulses, duty cycle 0.5%max.
5. These tests are performed with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with Ta=25°C. The SOA curve provides a single pulse rating.

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## ■ Typical electrical and thermal characteristics

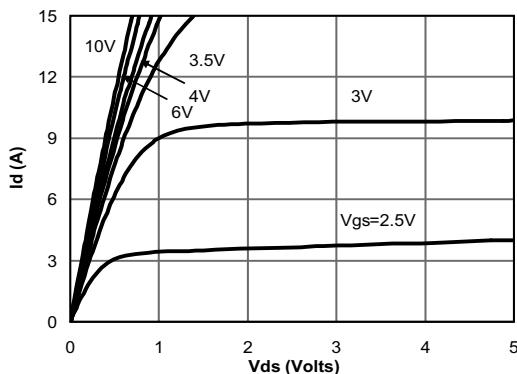


Fig 1: On-Region Characteristics

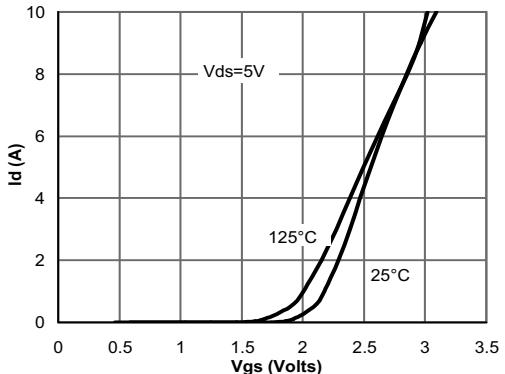


Figure 2: Transfer Characteristics

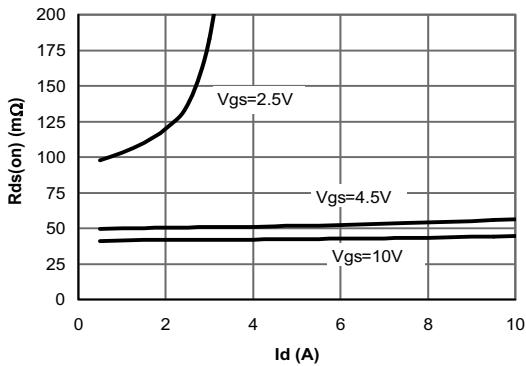


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

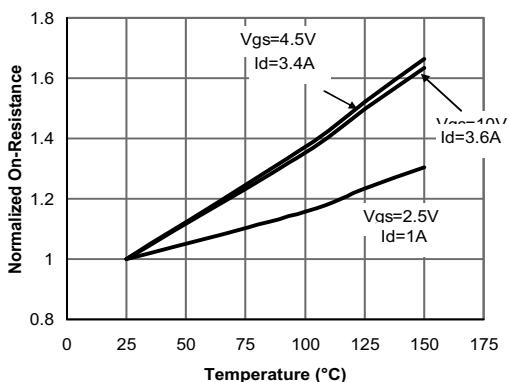


Figure 4: On-Resistance vs. Junction Temperature

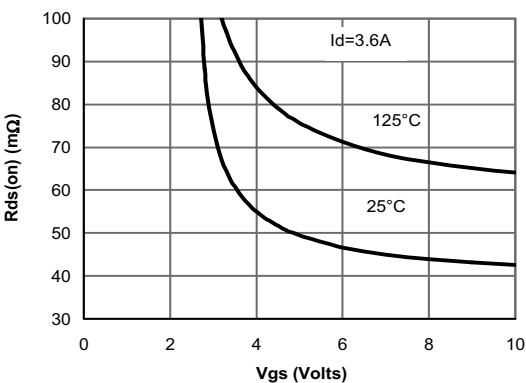


Figure 5: On-Resistance vs. Gate-Source Voltage

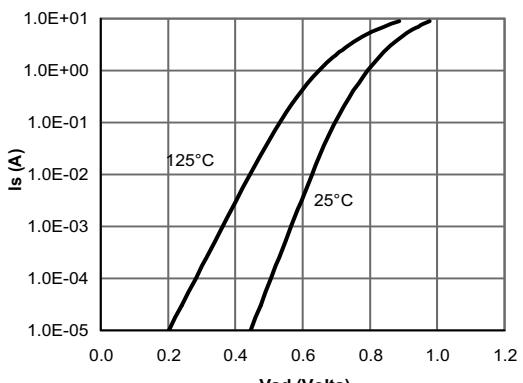


Figure 6: Body-Diode Characteristics

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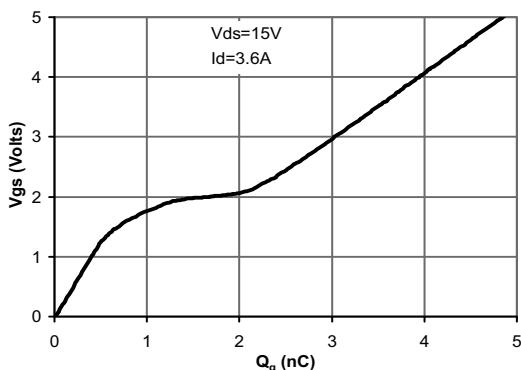


Figure 7: Gate-Charge Characteristics

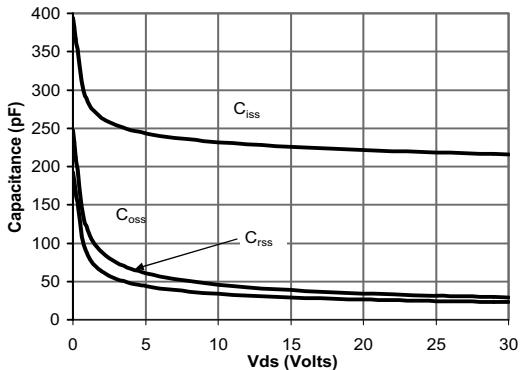


Figure 8: Capacitance Characteristics

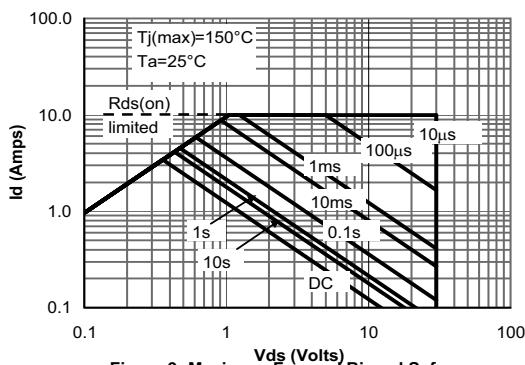


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

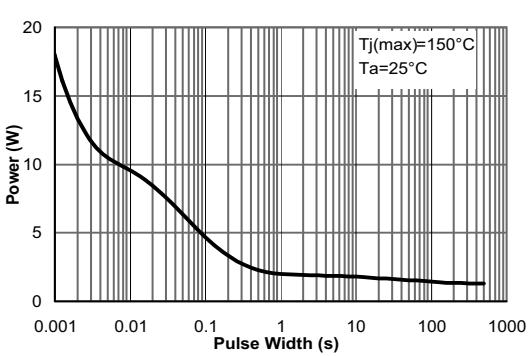


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

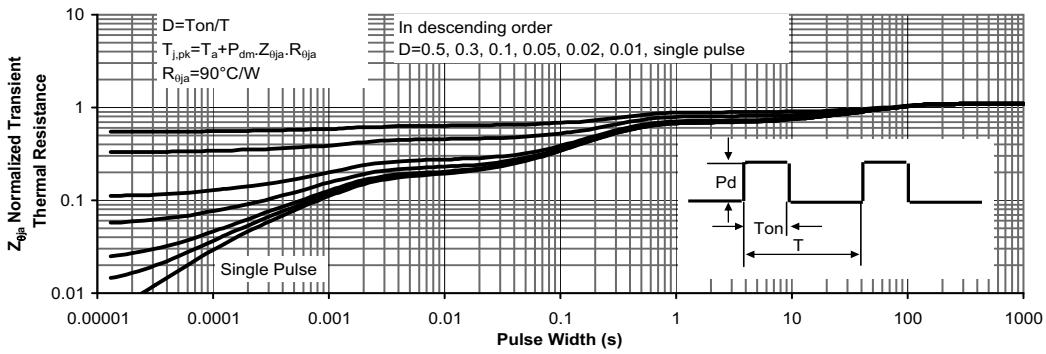


Figure 11: Normalized Maximum Transient Thermal Impedance

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## ■ Typical electrical and thermal characteristics (Schottky)

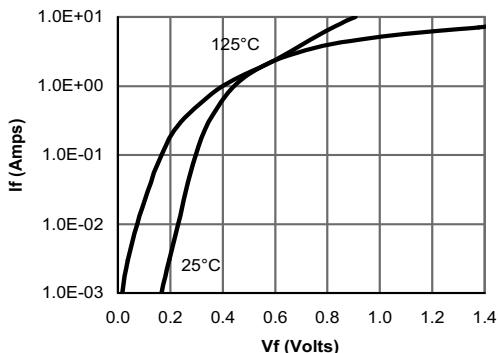


Figure 12: Schottky Forward Characteristics

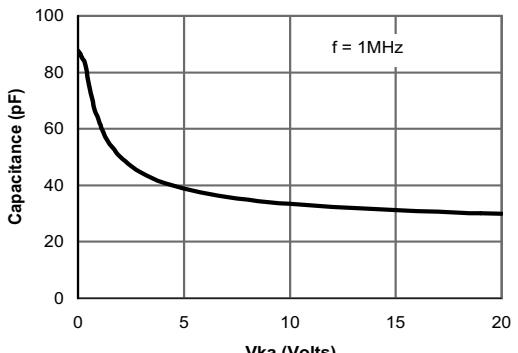


Figure 13: Schottky Capacitance Characteristics

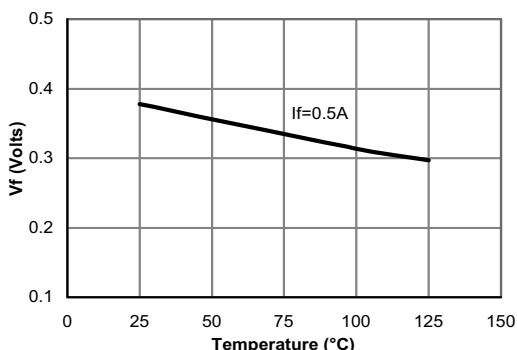


Figure 14: Schottky Forward Drop vs. Junction Temperature

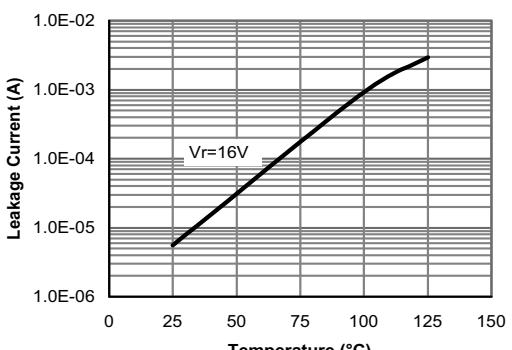


Figure 15: Schottky Leakage current vs. Junction Temperature

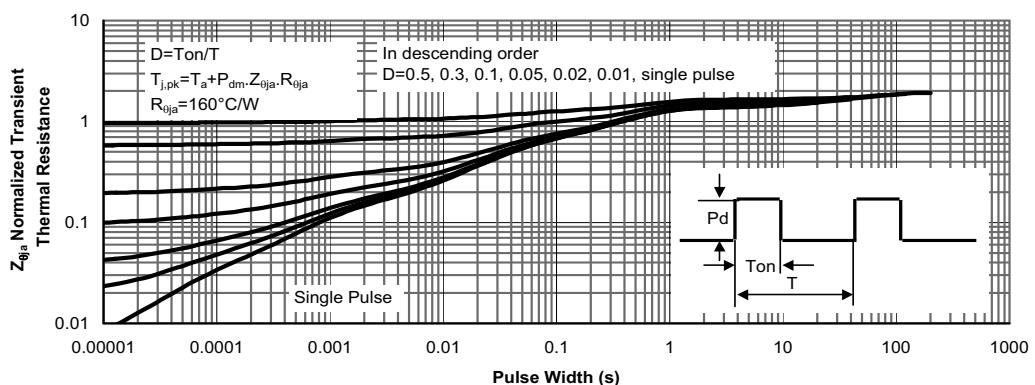


Figure 15: Schottky Normalized Maximum Transient Thermal Impedance