

# Single N-channel MOSFET with schottky diode

ELM16702EA-S

## General description

ELM16702EA-S uses advanced trench technology to provide excellent  $R_{ds(on)}$  and low gate charge.

## Features

- $V_{ds}=20V$
- $I_d=3.8A$  ( $V_{gs}=4.5V$ )
- $R_{ds(on)} < 50m\Omega$  ( $V_{gs}=4.5V$ )
- $R_{ds(on)} < 65m\Omega$  ( $V_{gs}=2.5V$ )
- $R_{ds(on)} < 95m\Omega$  ( $V_{gs}=1.8V$ )
- Schottky diode
- $V_{ds(V)}=20V$
- $I_f=1A$
- $V_f < 0.5V@0.5A$

## Maximum absolute ratings

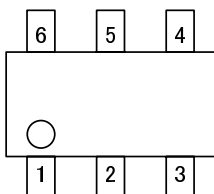
Parameter	Symbol	MOSFET	Schottky	Unit	Note
Drain-source voltage	$V_{ds}$	20		V	
Gate-source voltage	$V_{gs}$	$\pm 8$		V	
Continuous drain current	$I_d$	$T_a=25^\circ C$	3.8	A	1
		$T_a=70^\circ C$	3.0		
Pulsed drain current	$I_{dm}$	10		A	2
Schottky reverse voltage	$V_{ka}$		20	V	
Continuous forward current	$I_f$	$T_a=25^\circ C$	2	A	1
		$T_a=70^\circ C$	1		
Pulsed forward current	$I_{fm}$		10	A	2
Power dissipation	$P_d$	$T_a=25^\circ C$	1.15	0.92	W
		$T_a=70^\circ C$	0.70	0.59	
Junction and storage temperature range	$T_j, T_{stg}$	-55 to 150	-55 to 150	$^\circ C$	

## Thermal characteristics

Parameter (MOSFET)	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$R\theta_{ja}$	$t \leq 10s$	80.3	110.0	$^\circ C/W$
Maximum junction-to-ambient		Steady-state	117.0	150.0	
Maximum junction-to-lead	$R\theta_{jl}$	43.0	80.0	$^\circ C/W$	3
Parameter (Schottky)	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$R\theta_{ja}$	$t \leq 10s$	109.4	135.0	$^\circ C/W$
Maximum junction-to-ambient		Steady-state	136.5	175.0	
Maximum junction-to-lead	$R\theta_{jl}$	58.5	80.0	$^\circ C/W$	3

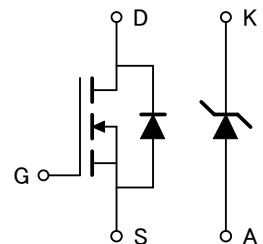
## Pin configuration

SOT-26 (TOP VIEW)



Pin No.	Pin name
1	ANODE
2	SOURCE
3	GATE
4	DRAIN
5	No connection
6	CATHODE

## Circuit



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### Electrical characteristics

Ta=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>						
Drain-source breakdown voltage	BVdss	Id=250 μA, Vgs=0V	20			V
Zero gate voltage drain current	Idss	Vds=16V Vgs=0V			1	μA
		Tj=55°C			5	
Gate-body leakage current	Igss	Vds=0V, Vgs=±8V			100	nA
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=250 μA	0.4	0.6	1.0	V
On state drain current	Id(on)	Vgs=4.5V, Vds=5V	10			A
Static drain-source on-resistance	Rds(on)	Vgs=4.5V Id=3.8A		41.6	50.0	mΩ
		Tj=125°C		63.0	80.0	
		Vgs=2.5V, Id=3.3A		54.0	65.0	mΩ
		Vgs=1.8V, Id=2.8A		74.0	95.0	mΩ
Forward transconductance	Gfs	Vds=5V, Id=3.8A		10.5		S
Diode forward voltage	Vsd	Is=1A, Vgs=0V		0.8	1.0	V
Max. body-diode continuous current	Is				1.8	A
<b>DYNAMIC PARAMETERS</b>						
Input capacitance	Ciss			449.0		pF
Output capacitance	Coss	Vgs=0V, Vds=10V, f=1MHz		74.0		pF
Reverse transfer capacitance	Crss			51.6		pF
Gate resistance	Rg	Vgs=0V, Vds=0V, f=1MHz		4.9		Ω
<b>SWITCHING PARAMETERS</b>						
Total gate charge	Qg			5.90		nC
Gate-source charge	Qgs	Vgs=4.5V, Vds=10V, Id=3.8A		0.36		nC
Gate-drain charge	Qgd			1.30		nC
Turn-on delay time	td(on)			4.5		ns
Turn-on rise time	tr	Vgs=5V, Vds=10V		6.0		ns
Turn-off delay time	td(off)	RI=2.6 Ω, Rgen=0 Ω		32.7		ns
Turn-off fall time	tf			7.1		ns
Body diode reverse recovery time	trr	If=3.8A, dl/dt=100A/μs		13.0		ns
Body diode reverse recovery charge	Qrr	If=3.8A, dl/dt=100A/μs		3.3		nC
<b>SCHOTTKY PARAMETERS</b>						
Forward voltage drop	Vf	If=0.5A		0.39	0.50	V
Max. reverse leakage current	Irm	Vr=16V			0.02	mA
		Vr=16V, Tj=125°C			20.00	
Junction capacitance	Ct	Vr=10V		34		pF
Schottky reverse recovery time	trr	If=1A, dl/dt=100A/μs		5.2	10.0	ns
Schottky reverse recovery charge	Qrr	If=1A, dl/dt=100A/μs		0.8		nC

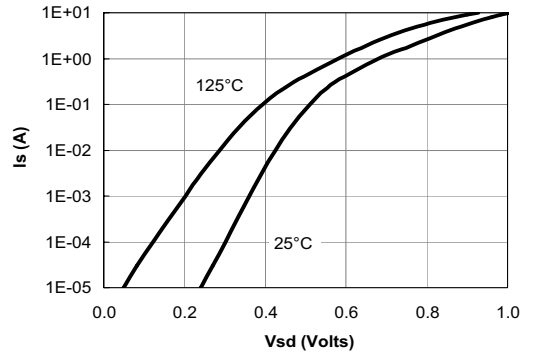
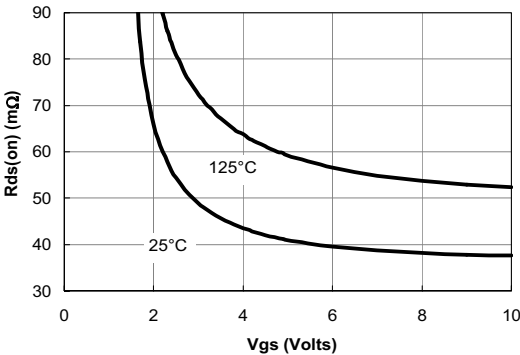
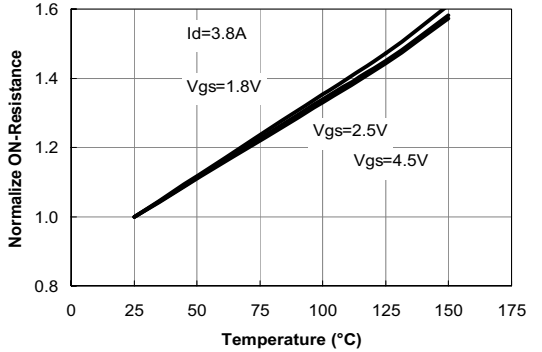
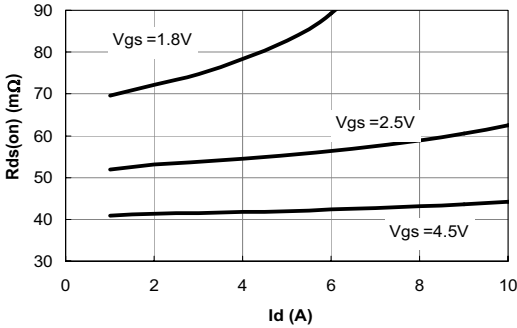
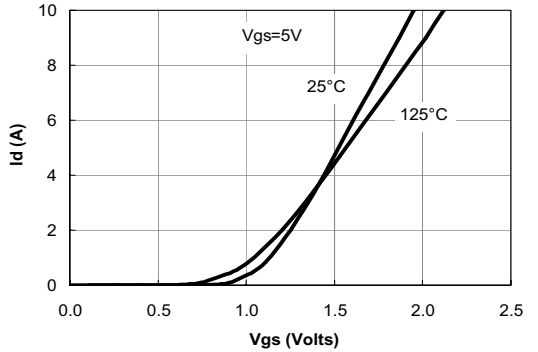
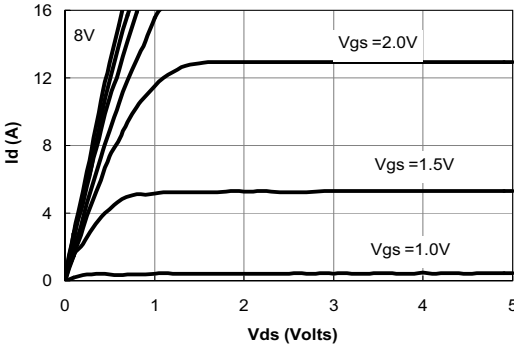
#### NOTE :

- The value of Rθja is measured with the device mounted on 1in<sup>2</sup> FR-4 board of 2oz. Copper, in still air environment with Ta=25°C. The value in any given applications depends on the user's specific board design. The current rating is based on the t ≤ 10s thermal resistance rating.
- Repetitive rating, pulse width limited by junction temperature.
- The Rθja is the sum of the thermal impedance from junction to lead Rθjl and lead to ambient.
- The static characteristics in Figures 1 to 6 are obtained using 80μs pulses, duty cycle 0.5%max.
- These tests are performed with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with Ta=25°C. The SOA curve provides a single pulse rating.

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## Typical electrical and thermal characteristics



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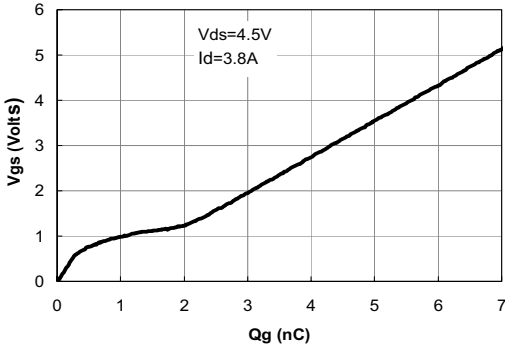


Figure 7: Gate-Charge Characteristics

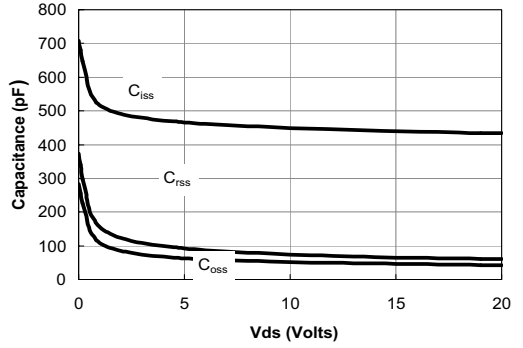


Figure 8: Capacitance Characteristics

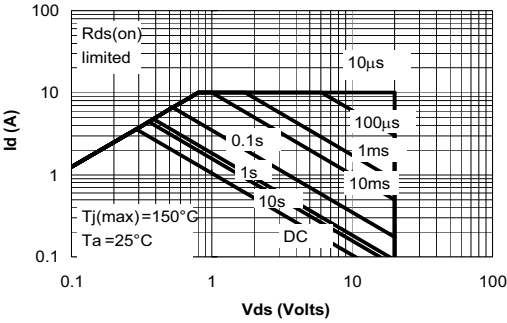


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

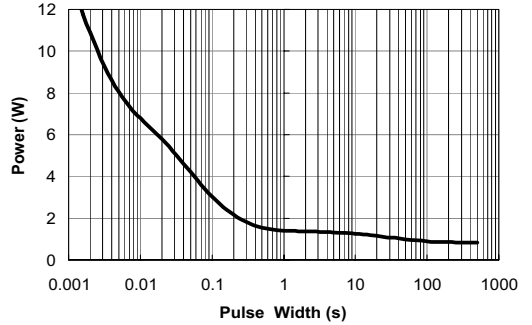


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

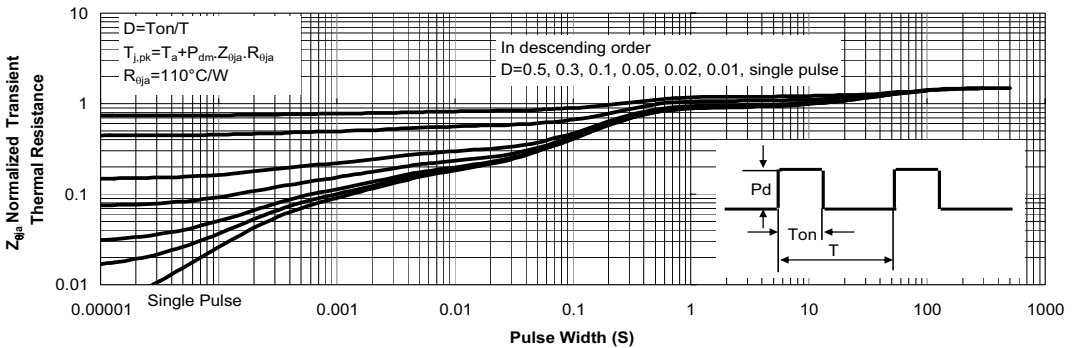


Figure 11: Normalized Maximum Transient Thermal Impedance

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## Typical electrical and thermal characteristics (Schottky)

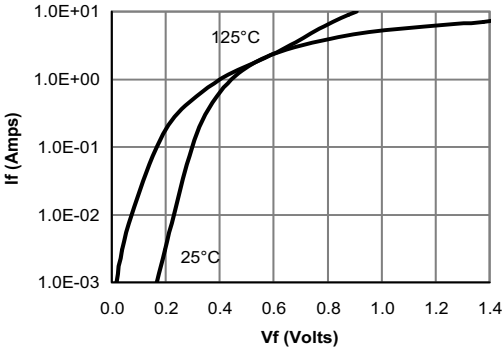


Figure 12: Schottky Forward Characteristics

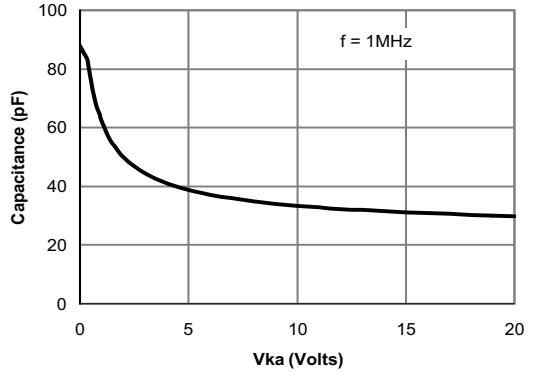


Figure 13: Schottky Capacitance Characteristics

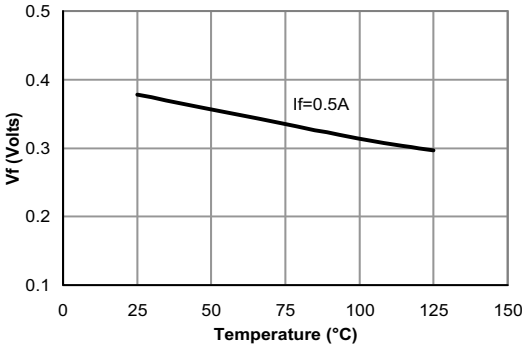


Figure 14: Schottky Forward Drop vs. Junction Temperature

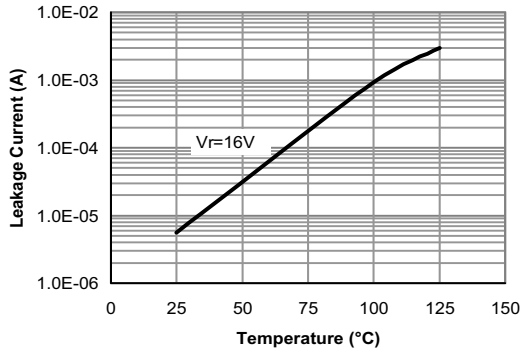


Figure 15: Schottky Leakage current vs. Junction Temperature

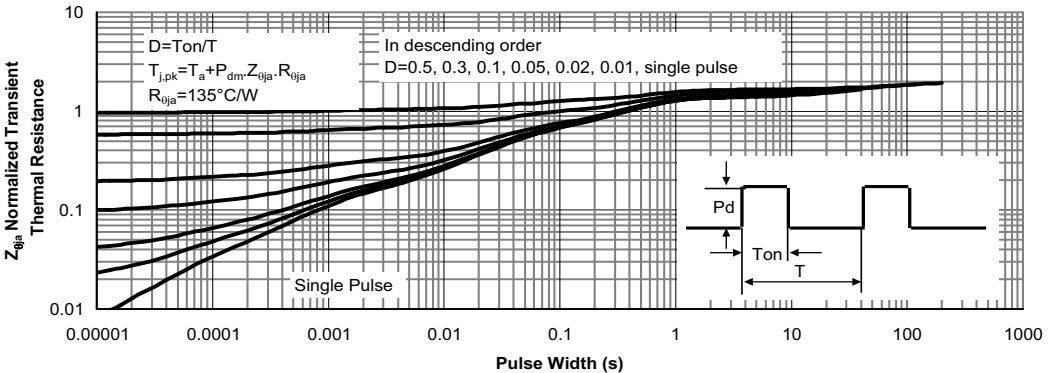


Figure 15: Schottky Normalized Maximum Transient Thermal Impedance