## Features

- Quad TVS in Common Anode Configuration
- Ultra-Small Surface Mount Package
- Ideal For Transient Suppression and ESD Protection
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green Device" (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability


## ESD Capability

## Mechanical Data

- Case: SOT-953
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish: Matte Tin, Annealed Over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.002 grams (approximate)
- IEC 61000-4-2 Contact Method $\pm 8 \mathrm{kV}$
- IEC 61000-4-2 Air Discharge Method $\pm 15 \mathrm{kV}$


Top View


Device Schematic

## Thermal Characteristics

| Characteristic | Symbol | Value |  |
| :--- | :---: | :---: | :---: |
| Peak Power Dissipation, $8 \times 20 \mu$ S Waveform (Note 5) | $\mathrm{P}_{\mathrm{pk}}$ | 18 | Unit |
| Thermal Resistance, Junction-to-Ambient (Note 4) | $\mathrm{R}_{\theta \mathrm{JA}}$ | W |  |
| Operating and Storage Temperature Range | $\mathrm{T}_{\mathrm{J},} \mathrm{T}_{\mathrm{STG}}$ | -517 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

Electrical Characteristics @ $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Type Number | Marking Code | Breakdown Voltage (Note 3) |  |  | Leakage Current (Note 3) |  | Capacitance @oV Bias(pF) (Note 6) |  | Capacitance @3V Bias(pF) (Note 6) |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\mathrm{V}_{\mathrm{BR}} @ \mathrm{IT}^{\text {¢ }}=5 \mathrm{~mA}$ |  |  | IRM @ $\mathrm{V}_{\mathrm{RM}}$ |  | $\mathrm{C}_{\text {T }}$ |  | $\mathrm{C}_{\text {T }}$ |  |
|  |  | Min (V) | Nom (V) | Max (V) | $\operatorname{Max}(\mu \mathrm{A})$ | (V) | Typ | Max | Typ | Max |
| DUP412VP5 | V1 | 11.4 | 12 | 12.7 | 0.5 | 9.0 | 6.5 | 10 | 3.5 | 5 |

Notes: 1. No purposefully added lead.
2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
3. Short duration pulse test used to minimize self-heating effect.
4. Device mounted on FR-4 PCB, 1 inch $\times 0.85$ inch $\times 0.062$ inch; pad layout as shown on Diodes Inc. Suggested Pad Layout Document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
5. Non-repetitive current pulse per Figure 3 and derate above $T_{A}=25^{\circ} \mathrm{C}$ per Figure 1 .
6. Per element, $f=1 \mathrm{MHZ}, \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$

DUP412VP5


Fig. 1 Pulse Derating Curve


Fig. 3 Typical Forward Characteristics


Fig. 5 Typical Total Capacitance vs. Reverse Voltage (Per Element)


Fig. 2 Pulse Waveform


Fig. 4 Instantaneous Reverse Current vs. Ambient Temperature

## Ordering Information (Note 7)

| Part Number | Case | Packaging |
| :---: | :---: | :---: |
| DUP412VP5-7 | SOT-953 | 10,000/Tape \& Reel |

Notes: 7. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## Marking Information



## Package Outline Dimensions



| SOT-953 |  |  |  |
| :---: | :---: | :---: | :---: |
| Dim | Min | Max | Typ |
| A | 0.40 | 0.50 | 0.45 |
| A1 | 0 | 0.05 | - |
| b | 0.10 | 0.20 | 0.15 |
| c | 0.12 | 0.18 | 0.15 |
| D | 0.95 | 1.05 | 1.00 |
| E | 0.95 | 1.05 | 1.00 |
| E1 | 0.75 | 0.85 | 0.80 |
| e | - | - | 0.35 |
| e1 | - | - | 0.70 |
| L | 0.05 | 0.15 | 0.10 |
| All Dimensions in $\mathbf{~ m m}$ |  |  |  |

## Suggested Pad Layout



| Dimensions | Value (in mm) |
| :---: | :---: |
| $\mathbf{C}$ | 0.350 |
| $\mathbf{X}$ | 0.200 |
| $\mathbf{Y}$ | 0.200 |
| $\mathbf{Y 1}$ | 1.100 |

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