

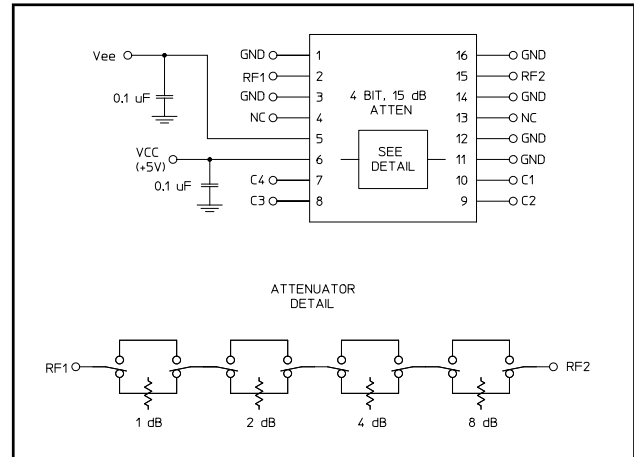
Features

- Attenuation: 1.0 dB steps to 15 dB
- Low DC Power Consumption
- Integral TTL Driver
- 50 Ohm Impedance
- Temperature Stability: ± 0.18 dB from -40°C to $+85^{\circ}\text{C}$ Typ.
- SOIC-16 Package

Description

M/A-COM's AT65-0413 is a GaAs FET 4-bit digital attenuator with a 1.0 dB minimum step size and a 15 dB total attenuation range. This device is in a SOIC-16 plastic surface mount package. The AT65-0413 is ideally suited for use where accuracy, fast speed, very low power consumption and low costs are required. Typical applications include dynamic range setting in precision receiver circuits and other gain/leveling control circuits.

Schematic with Off-Chip Components or Functional Block Diagram



Ordering Information

Part Number	Package
AT65-0413	Bulk Packaging
AT65-0413TR	1000 piece reel
AT65-0413-TB	Sample Test Board

Note: Reference Application Note M513 for reel size information.

Pin Configuration

Pin No.	Function	Pin No.	Function
1	GND	9	C2
2	RF1	10	C1
3	GND	11	GND
4	NC ¹	12	GND
5	Vee	13	NC ¹
6	Vcc	14	GND
7	C4	15	RF2
8	C3	16	GND

1. NC = No Connection

Electrical Specifications: $T_A = 25^\circ\text{C}$

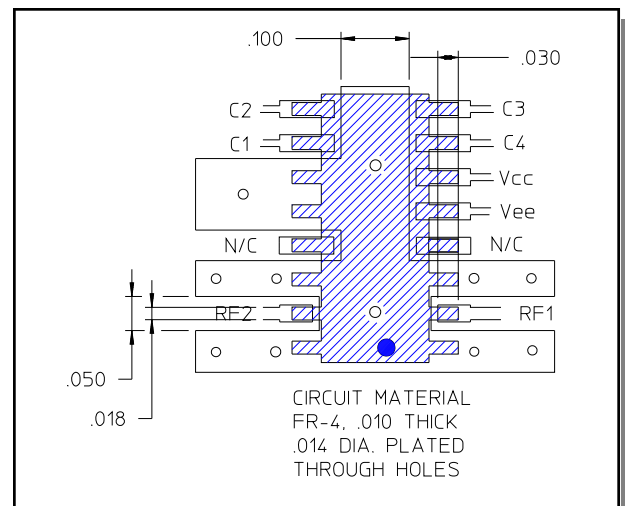
Parameter	Test Conditions	Frequency	Units	Min	Typ	Max
Insertion Loss	—	DC - 0.5 GHz	dB	—	1.3	1.6
		DC - 2.0 GHz	dB	—	1.7	2.1
		DC - 3.0 GHz	dB	—	1.9	2.4
Attenuation Accuracy	Any Bit or Combination of Bits	DC - 3.0 GHz	dB	$\pm (.25 + 3\%$ of attenuation) or $\pm .55$ dB	$\pm (.25 + 3\%$ of attenuation) or $\pm .55$ dB	$\pm (.25 + 3\%$ of attenuation) or $\pm .55$ dB
VSWR	Full Range	DC - 3.0 GHz	Ratio	—	—	1.6:1
Trise, Tfall Ton, Toff Transients	10% to 90% 50% Cntl to 90%/10% RF In-Band	10% to 90%	nS	—	10	50
		50% Cntl to 90%/10% RF In-Band	nS	—	30	150
			mV	—	35	—
1 dB Compression	Input Power Input Power	0.05 GHz	dBm	—	+20	—
		0.5 - 3.0 GHz	dBm	—	+28	—
Input IP ₃	Two-tone inputs up to +5 dBm	0.05 GHz	dBm	—	+40	—
		0.5 - 3.0 GHz	dBm	—	+50	—
Input IP ₂	Two-tone inputs up to +5 dBm	0.05 GHz	dBm	—	+45	—
		0.5 - 3.0 GHz	dBm	—	+68	—
V _{CC} V _{EE}	—	—	V	4.5	5.0	5.5
		—	V	-8.0	-5.0	-4.75
V _{IL} V _{IH}	LOW-level input voltage HIGH-level input voltage	—	V	0.0	—	0.8
		—	V	2.0	—	5.0
I _{in} (Input Leakage Current)	V _{in} = V _{CC} or GND	—	uA	-1.0	—	1.0
I _{CC} (Quiescent Supply Current)	V _{cntrl} = V _{CC} or GND	—	uA	—	250	400
Δ I _{CC} (Additional Supply Current Per TTL Input Pin)	V _{CC} = Max, V _{cntrl} = V _{CC} - 2.1 V	—	mA	—	—	1.0
I _{EE}	V _{EE} min to max, V _{in} = V _{IL} or V _{IH}	—	mA	-1.0	-0.2	—

Absolute Maximum Ratings ^{2,3}

Parameter	Absolute Maximum
Max. Input Power	+27 dBm
	+34 dBm
V _{CC}	-0.5V ≤ V _{CC} ≤ +7.0V
V _{EE}	-8.5V ≤ V _{EE} ≤ +0.5V
V _{CC} - V _{EE}	-0.5V ≤ V _{CC} - V _{EE} ≤ 14.5V
V _{in} ⁴	-0.5V ≤ V _{in} ≤ V _{CC} + 0.5V
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +125°C

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- M/A-COM does not recommend sustained operation near these survivability limits.
- Standard CMOS TTL interface, latch-up will occur if logic signal is applied prior to power supply.

Recommended PCB Configuration



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ADVANCED: Data Sheets contain information regarding a product M/A-COM Technology Solutions is considering for development. Performance is based on target specifications, simulated results, and/or prototype measurements. Commitment to develop is not guaranteed.
PRELIMINARY: Data Sheets contain information regarding a product M/A-COM Technology Solutions has under development. Performance is based on engineering tests. Specifications are typical. Mechanical outline has been fixed. Engineering samples and/or test data may be available. Commitment to produce in volume is not guaranteed.

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Handling Procedures

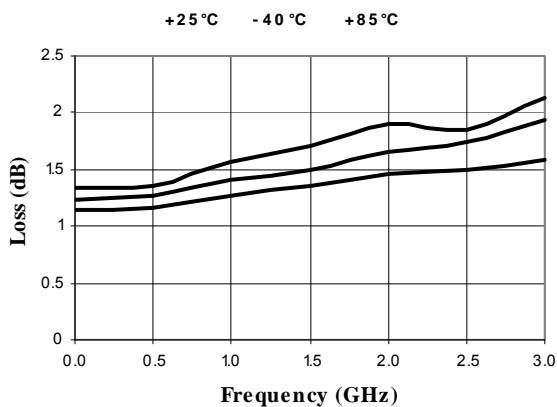
Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Typical Performance Curves

Typical Insertion Loss (dB)

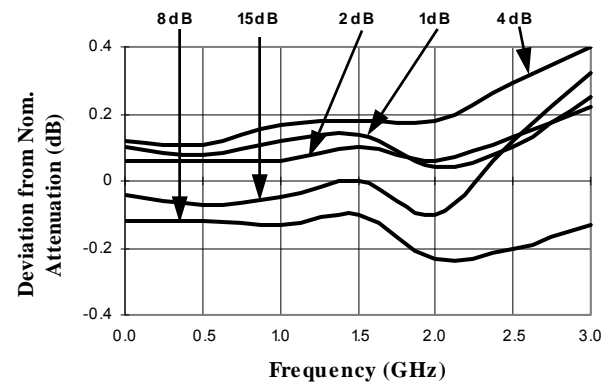


Truth Table (Digital Attenuator)

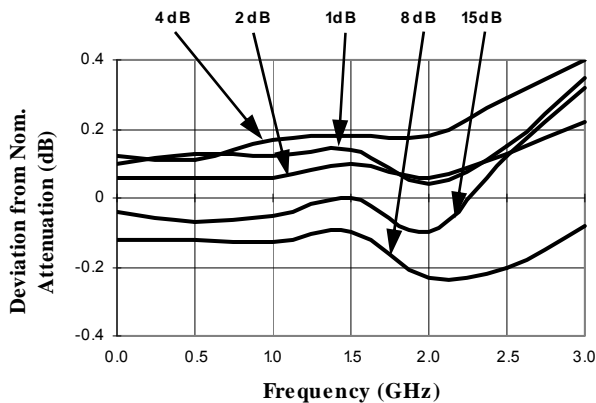
C1	C2	C3	C4	Attenuation
0	0	0	0	Loss, Reference
1	0	0	0	1.0 dB
0	1	0	0	2.0 dB
0	0	1	0	4.0 dB
0	0	0	1	8.0 dB
1	1	1	1	15.0 dB

0 = TTL Low; 1 = TTL High

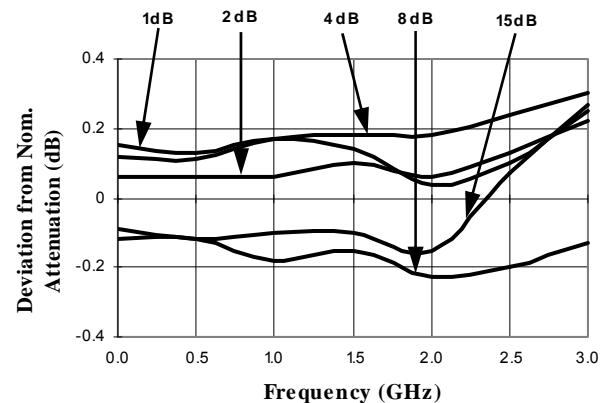
Attenuation Accuracy @ +25°C



Attenuation Accuracy @ -40°C

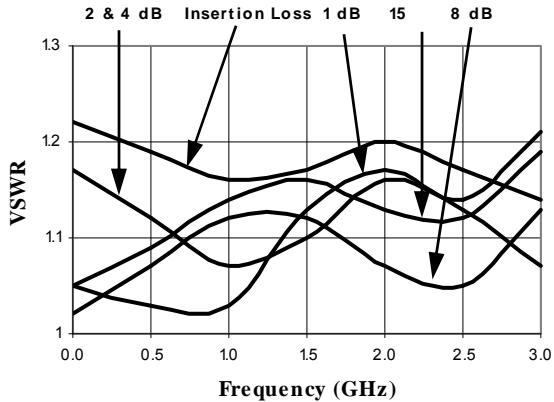


Attenuation Accuracy @ +85°C

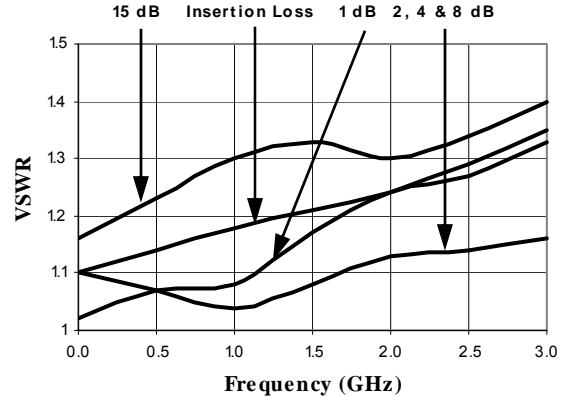


Typical Performance Curves

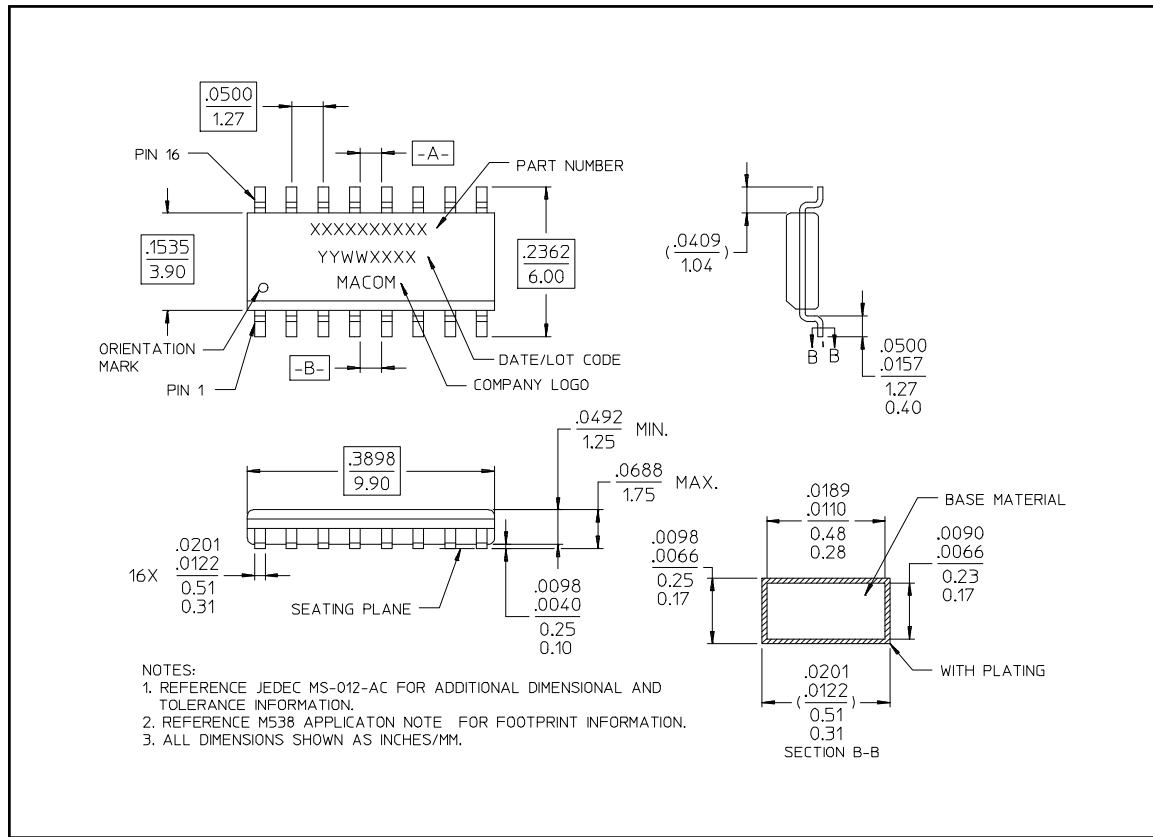
RF1 VSWR vs. Frequency



RF 2 VSWR vs. Frequency



SOIC-16[†]



[†] Reference Application Note M538 for lead-free solder reflow recommendations.