

Power management (dual transistors)

VT6T1

Structure

PNP silicon epitaxial planar transistor

Features

Very small package with two transistors.

Applications

Switch, LED driver

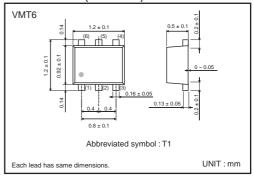
Packaging specifications

	Package	Taping			
	Code	T2R			
Туре	Basic ordering unit (pieces)	8000			
VT6T1		0			

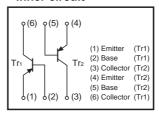
● Absolute maximum ratings (Ta=25°C)

Parameter		Limits	Unit
Collector-base voltage		-20	V
Collector-emitter voltage		-20	V
Emitter-base voltage		- 5	V
Collector current		-200	mA
	ICP *1	-400	mA
Total	Pp *2 150		mW
Element	10	120	mW
Junction temperature		150	°C
Storage temperature		-55 to +150	°C
	Total Element	voltage VcEO ge VEBO Ic Ice**1 Total Pp *2 Element Tj	tage VCBO -20 roltage VCEO -20 ge VEBO -5

●Dimensions (Unit:mm)



Inner circuit



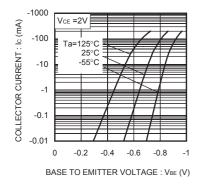
●Electrical characteristics (Ta=25°C)

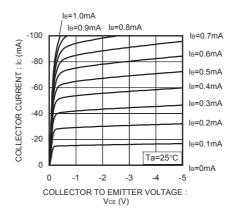
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BVceo	-20	_	_	V	Ic=-1mA
Collector-base breakdown voltage	ВУсво	-20	_	_	V	Ic=-50μA
Emitter-base breakdown voltage	ВУево	-5	_	_	V	I _E = -50μA
Collector cut-off current	Ісво	_	_	-0.1	μΑ	Vcb= -20V
Emitter cut-off current	ІЕВО	_	_	-0.1	μΑ	V _{EB} = -5V
Collector-emitter saturation voltage	VCE(sat)	_	-0.12	-0.30	V	Ic= -100mA, I _B = -10mA
DC current gain	hfe	120	_	560	_	Vce=-2V, Ic=-1mA
Transition frequency	f⊤	_	350	-	MHz	Vc=-10V, I=10mA, f=100MHz
Output capacitance	Cob	_	3	_	pF	Vсв= −10V, I∈=0A, f=1МНz

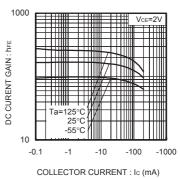
^{*1} Pw=1mS Single pulse *2 Each terminal mounted on a recommended land

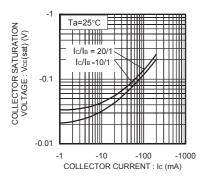
VT6T1 Data Sheet

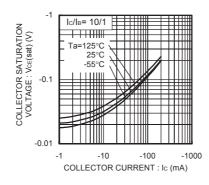
•Electrical characteristics curves

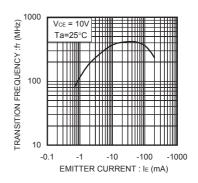


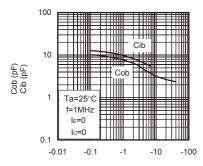












COLLECTOR TO BASE VOLTAGE : Vcb (V) EMITTER TO BASE VOLTAGE : Veb(V)

Notes

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