TAIWAN SEMICONDUCTOR

RoHS COMPLIANCE

Preliminary

TSM1426

30V N-Channel MOSFET

SOT-363

Pin Definition:
1. Drain 6. Drain
2. Drain 5. Drain
3. Gate 4. Source

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$(mΩ)	I_D (A)
20	75 @ V_{GS} = 10V	3.6
	115 @ V_{GS} = 4.5V	2.9

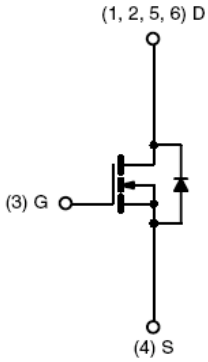
Features

- Advance Trench Process Technology
- PWM Optimized

Application

- Boost Converter in Portable devices
- Low Current Synchronous Rectifier

Block Diagram



N-Channel MOSFET

Ordering Information

Part No.	Package	Packing
TSM1424CU6 RF	SOT-363	3Kpcs / 7" Reel

Absolute Maximum Rating (Ta = 25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current		I_D	3.6	A
Pulsed Drain Current		I_{DM}	10	A
Continuous Source Current (Diode Conduction)[a,b]		I_S	1.3	A
Maximum Power Dissipation	Ta = 25°C	P_D	1.6	W
	Ta = 75°C		0.8	
Operating Junction Temperature		T_J	+150	°C
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Thermal Performance

Parameter	Symbol	Limit	Unit
Junction to Case Thermal Resistance	$R\Theta_{JF}$	45	°C/W
Junction to Ambient Thermal Resistance (PCB mounted)	$R\Theta_{JA}$	80	°C/W

Notes:

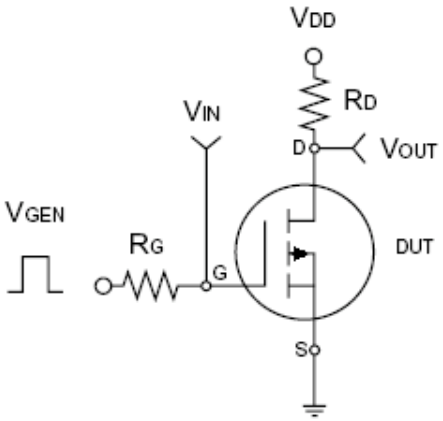
a. Pulse width limited by the Maximum junction temperature

b. Surface Mounted on FR4 Board, t ≤ 5 sec.

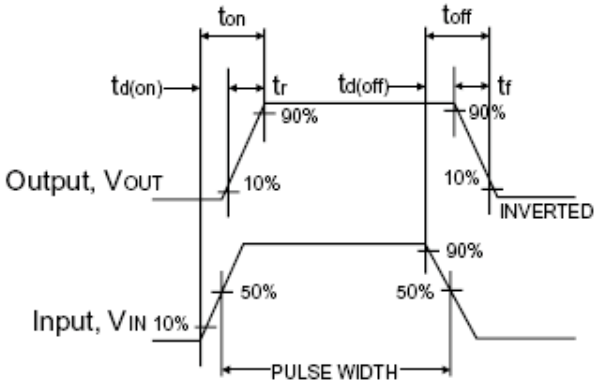
Electrical Specifications (Ta = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V_{GS} = 0V, I_D = 250μA	BV_{DSS}	30	--	--	V
Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	$V_{GS(TH)}$	0.8	--	2.5	V
Gate Body Leakage	V_{GS} = ±20V, V_{DS} = 0V	I_{GSS}	--	--	±100	nA
Zero Gate Voltage Drain Current	V_{DS} = 24V, V_{GS} = 0V	I_{DSS}	--	--	1.0	μA
On-State Drain Current	V_{DS} ≥ 10V, V_{GS} = 4.5V	$I_{D(ON)}$	10	--	--	A
Drain-Source On-State Resistance	V_{GS} = 10V, I_D = 3.6A	$R_{DS(ON)}$	--	61	75	mΩ
	V_{GS} = 4.5V, I_D = 2.9A		--	92	115	
Forward Transconductance	V_{DS} = 10V, I_D = 3.6A	g_{fs}	--	5	--	S
Diode Forward Voltage	I_S = 1.3A, V_{GS} = 0V	V_{SD}	--	0.8	1.2	V
Dynamic[b]						
Total Gate Charge	V_{DS} = 15V, I_D = 3.6A, V_{GS} = 4.5V	Q_g	--	1.9	3	nC
Gate-Source Charge		Q_{gs}	--	0.75	--	
Gate-Drain Charge		Q_{gd}	--	0.75	--	
Input Capacitance	V_{DS} = 15V, V_{GS} = 0V, f = 1.0MHz	C_{iss}	--	190	--	pF
Output Capacitance		C_{oss}	--	100	--	
Reverse Transfer Capacitance		C_{rss}	--	55	--	
Switching[c]						
Turn-On Delay Time	V_{DD} = 15V, R_L = 15Ω, I_D = 1A, V_{GEN} = 10V, R_G = 6Ω	$t_{d(on)}$	--	10	15	nS
Turn-On Rise Time		t_r	--	12	18	
Turn-Off Delay Time		$t_{d(off)}$	--	15	22	
Turn-Off Fall Time		t_f	--	9	15	

Notes:
a. pulse test: PW ≤300μS, duty cycle ≤2%
b. For DESIGN AID ONLY, not subject to production testing.
b. Switching time is essentially independent of operating temperature.

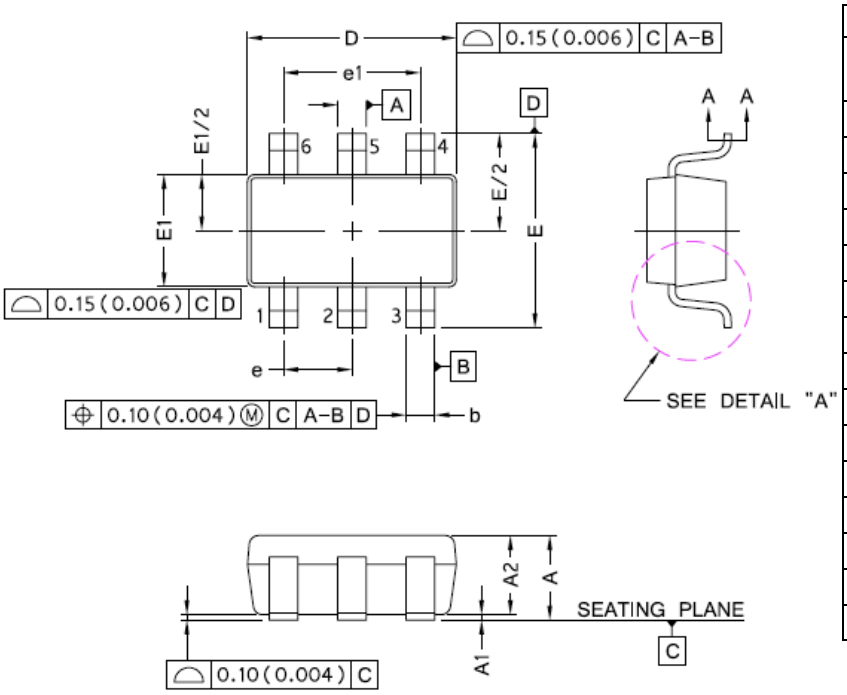


Switching Test Circuit



Switchin Waveforms

SOT-363 Mechanical Drawing



SOT-363 DIMENSION				
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX.
A	0.80	1.10	0.031	0.043
A1	0	0.10	0	0.004
A2	0.80	1.00	0.031	0.040
b	0.15	0.30	0.006	0.012
b1	0.15	0.25	0.006	0.010
c	0.08	0.22	0.003	0.009
c1	0.08	0.20	0.003	0.008
D	1.90	2.10	0.074	0.084
E	2.00	2.20	0.078	0.086
E1	1.15	1.35	0.045	0.055
e	0.65 BSC		0.025 BSC	
e1	1.30 BSC		0.051 BSC	
L	0.26	0.46	0.010	0.018
θ	0°	8°	0°	8°
θ1	4°	10°	4°	10°

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