

PCS3P6200A

rev 0.2

Spread Spectrum EMI reduction IC for HD Display

Features

- Custom Clock Generator for Display Systems
- Wide Operating Frequency Range covering most of the pixel frequencies
- Generates a low EMI 1x Output
- Frequency range: 25 MHz 120 MHz
- 4 Frequency Deviation selection options ± 1.50%, ± 1.25%, ± 0.75%, ± 1.00%
- Supply voltage : 3.3V±0.3V
 - 2.5V±0.125V
- ModRate 85KHz @ 72MHz
- 6 Pin TSOT-26 package
- Commercial and Industrial Temperature range

Product Description

PCS3P6200A is a versatile spread spectrum modulator designed specifically for a wide range of clock frequencies. The device addresses the need of a low EMI clock generator for use in display systems covering wide choice of pixel frequencies.

PCS3P6200A reduces electromagnetic interference

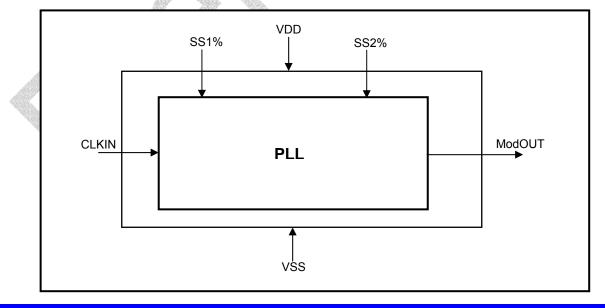
Block Diagram

(EMI) at the clock source, allowing system wide reduction of EMI of all clock dependent signals. PCS3P6200A allows significant system cost savings by reducing the number of circuit board layers, ferrite beads, shielding that are traditionally required to pass EMI regulations.

The Supply Voltage of the Device is 3.3V/2.5V. It has two Spread Selection Pins, SS1% and SS2% to select among the four possible deviation options. The Frequency Deviation across the Frequency range remains within ±10% of the selected deviation. *Refer to the Frequency Deviation Selection Table for details*. The Device is available in a 6 Pin TSOT-26 Package, over Commercial and Industrial temperature range.

Application

PCS3P6200A is targeted for use in a broad range of applications including Liquid Crystal and Plasma Displays



PulseCore Semiconductor Corporation

1715 S. Bascom Ave Suite 200, Campbell, CA 95008 • Tel: 408-879-9077 • Fax: 408-879-9018 www.pulsecoresemi.com

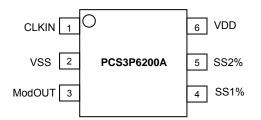
Notice: The information in this document is subject to change without notice.



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rev 0.2

Pin Configuration (6L TSOT- 26 Package)





Pin Description

Pin#	Pin Name	Туре	Description
1	CLKIN	I	External Reference Clock Input.
2	VSS	Р	Ground to entire chip
3	ModOUT	0	Modulated Frequency Output
4	SS1%	I	Frequency Deviation Selection. Refer to "Frequency Deviation Selection Table" for details. Has an Internal pull-up resistor.
5	SS2%	I	Frequency Deviation Selection. Refer to "Frequency Deviation Selection Table" for details. Has an Internal pull-up resistor.
6	VDD	Р	Power to entire chip

Frequency Deviation Selection Table

SS2%	SS1%	Frequency Deviation
L	L	± 1.50%
L	H	± 1.25%
H	L	± 0.75%
Н	Н	± 1.00%



PCS3P6200A

rev 0.2

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit				
$V_{\text{DD}}, V_{\text{IN}}$	Voltage on any pin with respect to Ground	-0.5 to +4.6	V				
T _{STG}	torage temperature -65 to +125 °C						
Ts	Max. Soldering Temperature (10 sec)	260	°C				
TJ	Junction Temperature	150	°C				
T _{DV} Static Discharge Voltage (As per JEDEC STD22- A114-B)							
	Note: These are stress ratings only and are not implied for functional use. Exposure to absolute maximum ratings for prolonged periods of time may affect device reliability.						

Operating Conditions for 2.5V and 3.3V Supply Voltage

Parameter	Description	Min	Max	Unit
V _{DD(2.5)}		2.375	2.625	
V _{DD(3.3)}	Supply Voltage	3.0	3.6	V
T _A	Operating Temperature (Ambient Temperature)	-40	+85	°C
CL	Load Capacitance		15	pF

DC Electrical Characteristics for 2.5V Supply

Symbol	Parameter	Min	Тур	Max	Unit
V _{IL}	Input low voltage	VSS - 0.3		0.7	V
VIH	Input high voltage	1.7		VDD + 0.3	V
IIL	Input low current			-35	μA
IIH	Input high current			35	μA
Vol	Output low voltage (VDD = 2.5V, I _{OL} = 8 mA)			0.6	V
Vон	Output high voltage (VDD = 2.5V, I _{OH} = -8 mA)	1.8			V
IDD	Static supply current*			4	mA
lcc	Dynamic supply current (2.5V and no load)		11		mA
V _{DD}	Operating voltage	2.375	2.5	2.625	V
t _{ON}	Power-up time (first locked cycle after power-up)			5	mS
CIN	Input Capacitance		5		pF
Zout	Output Impedance		40		Ω



PCS3P6200A

rev 0.2

AC Electrical Characteristics for 2.5V Supply

		Тур		Unit
put frequency	25		120	MHz
utput frequency	25		120	MHz
utput rise time (measured from 0.7V to 1.7V)		2.2	A	nS
utput fall time (measured from 1.7V to 0.7V)		1.2		nS
ter (Cycle to cycle)		±250		pS
utput duty cycle	40	50	60	%
	utput frequency utput rise time (measured from 0.7V to 1.7V) utput fall time (measured from 1.7V to 0.7V) ter (Cycle to cycle)	utput frequency 25 utput rise time (measured from 0.7V to 1.7V) 1.7V utput fall time (measured from 1.7V to 0.7V) 1.7V ter (Cycle to cycle) 1.7V	utput frequency 25 utput rise time (measured from 0.7V to 1.7V) 2.2 utput fall time (measured from 1.7V to 0.7V) 1.2 ter (Cycle to cycle) ±250	utput frequency25120utput rise time (measured from 0.7V to 1.7V)2.2utput fall time (measured from 1.7V to 0.7V)1.2ter (Cycle to cycle)±250

DC Electrical Characteristics for 3.3V Supply

Symbol	Parameter	Min	Тур	Max	Unit
VIL	Input low voltage	VSS - 0.3	Charles .	0.8	V
V _{IH}	Input high voltage	2.0		VDD + 0.3	V
IIL	Input low current	and the second second		-35	μA
I _{IH}	Input high current			35	μA
V _{OL}	Output low voltage (VDD = 3.3V, I _{OL} = 8 mA)			0.4	V
V _{OH}	Output high voltage (VDD = 3.3V, I _{OH} = -8 mA)	2.5			V
I _{DD}	Static supply current*			4.5	mA
I _{CC}	Dynamic supply current (3.3V and no load)		14		mA
V _{DD}	Operating voltage	3.0	3.3	3.6	V
t _{ON}	Power-up time (first locked cycle after power-up)			5	mS
CIN	Input Capacitance		5		pF
Z _{OUT}	Output Impedance		40		Ω
* CLKIN pin i	is pulled low	÷	-	<u>. </u>	

AC Electrical Characteristics for 3.3V Supply

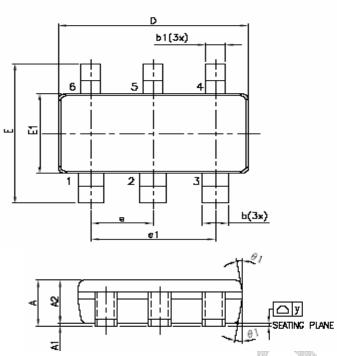
Symbol	Parameter	Min	Тур	Max	Unit
CLKIN	Input frequency	25		120	MHz
ModOUT	Output frequency	25		120	MHz
t∟⊢*	Output rise time (measured from 0.8 to 2.0V)		1.5		nS
t _{HL} *	Output fall time (measured at 2.0V to 0.8V)		1.1		nS
t _{JC}	Jitter (Cycle to cycle)		±225		pS
t _D	Output duty cycle	45	50	55	%

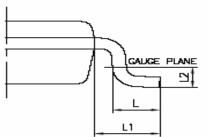


rev 0.2

Package Information

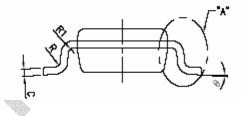
6L TSOT26







PCS3P6200A



	Dimensions				
Symbol	Inch	nes	Millimeters		
	Min	Max	Min	Мах	
A	0.0295	0.035	0.75	0.90	
A1	0.00	0.0039	0.00	0.10	
A2	0.0275	0.0314	0.70	0.80	
b	0.0157	0.0197	0.40	0.50	
b1	0.0118	0.0157	0.30	0.40	
C	0.0031	0.0078	0.08	0.20	
D 0.1141		41	2.90 REF		
E	0.1023	0.1181	2.60	3.00	
E1	0.0590	0.0069	1.50	1.70	
е	0.0374		0.95 BSC		
e1	0.07	'48	1.9	90 BSC	
L	0.0118	0.0236	0.30	0.60	
L1	0.0236	8 REF	0.60 REF		
L2	0.0098	BSC	0.25 BSC		
R	0.0039		0.10		
R1	0.0039	0.0098	0.10	0.25	
θ	0°	8°	0°	8°	
у		0.0039		0.10	





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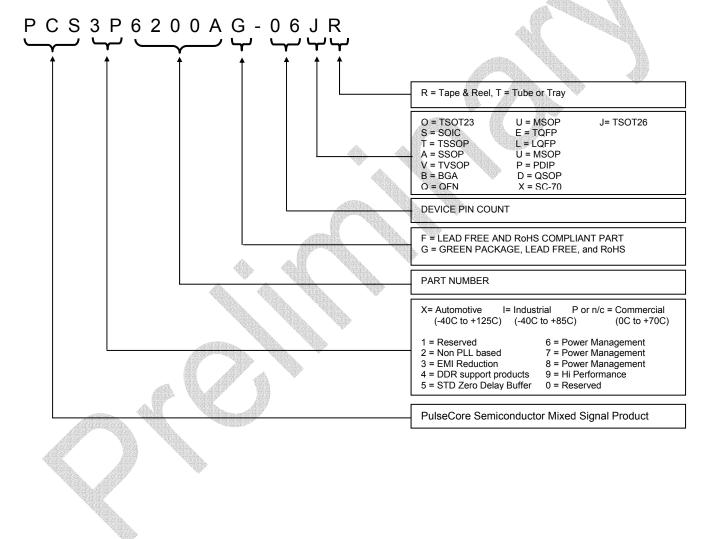
rev 0.2

Ordering Codes

Part Number	Marking	Package Type	Temperature
PCS3P6200AG-06JR	AC4LL	6-Pin TSOT-26, TAPE & REEL, Green	Commercial
PCS3I6200AG-06JR	AC2LL	6-Pin TSOT-26, TAPE & REEL, Green	Industrial

LL = 2 Character LOT #

Device Ordering Information



Licensed under U.S Patent Nos 5,488,627 and 5,631,921

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rev 0.2



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Note: This product utilizes US Patent # 6,646,463 Impedance Emulator Patent issued to PulseCore Semiconductor, dated 11-11-2003 Many PulseCore Semiconductor products are protected by issued patents or by applications for patent

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