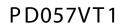
# **O**PRIME VIEW



## Version : <u>3.0</u>

# TECHNICAL SPECIFICATION

### MODEL NO. : PD057VT1

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Customer•s Confirmation

Customer

Date

#### FOR MORE INFORMATION:

AZ DISPLAYS, INC. 75 COLUMBIA, ALISO VIEJO, CA 92656 Http://www.AZDISPLAYS.com

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PVI•s Confirmation

中山圣北朝

Confirmed By

目

Prepared By

# Revision History

Rev.	Issued Date	Revised Contents
1.0	Jun. 07, 2007	New
2.0	Aug. 11, 2008	Add Page 16 13.Handling Cautions
	•	d) items of 13-1
3.0	<b>,</b>	Delete backlight connector pin pitch Before : Connector type : JST BHR-03VS-1, PIN No 3 pins, pitch=8.0mm After :
		Connector type:JST BHR-03VS-1, PIN No 3 pins

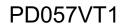
# TECHNICAL SPECIFICATION

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#### 1. Application

This data sheet applies to a color TFT LCD module, PD057VT1.

PD057VT1 module applies to OA product, car TV (must use Analog to Digital driving board), which requires high quality flat panel display. If you must use in severe reliability environment, please don't extend over PVI's reliability test conditions.

#### 2. Features

- . VGA (640\*480 pixels) resolution
- . Amorphous silicon TFT LCD panel with back-light unit
- . Pixel in stripe configuration
- . Thin and light weight
- . Display Colors : 262,144 colors
- . TTL interface

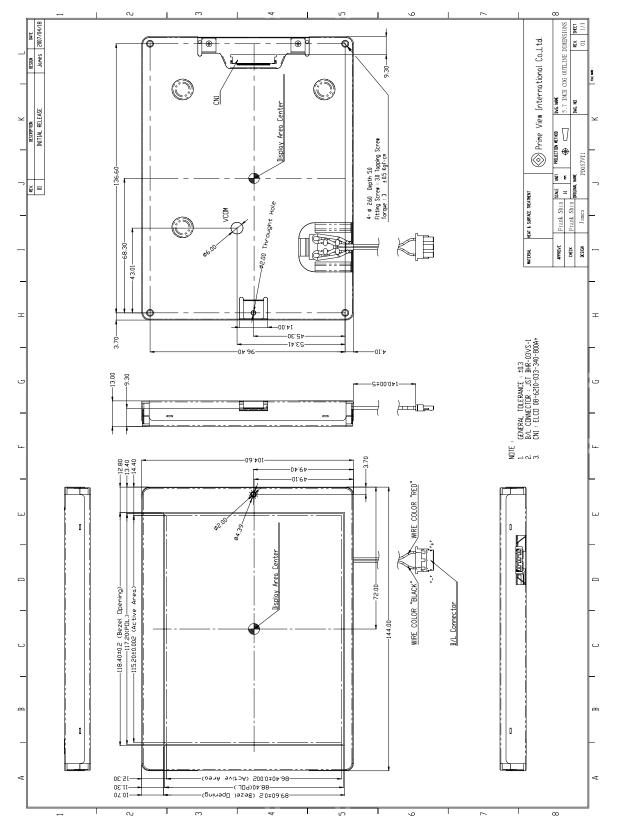
#### 3. Mechanical Specifications

Parameter	Specifications	Unit
Screen Size	5.7 (diagonal)	inch
Display Format	640×(R,G,B)×480	dot
Display Colors	262,144	
Active Area	115.20 (H)×86.4 (V)	mm
Pixel Pitch	0.18(H)×0.18(V)	mm
Pixel Configuration	Stripe	
Outline Dimension	144.0 (W)×104.6(H)×13.0 (D) (typ.)	mm
Weight	211±10	g
Back-light	36-LED	
Surface treatment	Anti-glare + EWV film	
Display mode	Normally white	
Gray scale inversion direction	6 o'clock	
	[ref. to Note 12-1]	

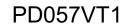
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#### 4. Mechanical Drawing of TFT-LCD Module



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#### 5.Input / Output Terminals

#### 5-1) TFT-LCD Panel Driving

Connector type : ELCO 08-6210-033-340-800A+, PIN No 33 pins

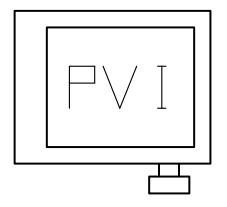
Pin No	Symbol	I/O	Description	Remark
1	GND	-	GND	
2	CK	-	Clock signal for sampling each data signal	
3	Hsync	Ι	Horizontal synchronous signal (neqative)	
4	Vsync	-	Vertical synchronous signal (neqative)	
5	GND	-	GND	
6	R0		RED data signal (LSB)	
7	R1	_	RED data signal	
8	R2		RED data signal	
9	R3	-	RED data signal	
10	R4	-	RED data signal	
11	R5	Ι	RED data signal (MSB)	
12	GND	-	GND	
13	G0	Ι	GREEN data signal (LSB)	
14	G1	Ι	GREEN data signal	
15	G2	Ι	GREEN data signal	
16	G3	Ι	GREEN data signal	
17	G4	Ι	GREEN data signal	
18	G5	Ι	GREEN data signal (MSB)	
19	GND	-	GND	
20	B0	-	Blue data signal (LSB)	
21	B1	-	Blue data signal	
22	B2	-	Blue data signal	
23	B3	T	Blue data signal	
24	B4	Ι	Blue data signal	
25	B5	Ι	Blue data signal (MSB)	
26	GND	-	GND	
27	ENAB	Ι	Signal to settle the horizontal display position(positive)	Note5-1
28	V <sub>cc</sub>	-	+3.3V power supply	
29	$V_{CC}$	-	+3.3V power supply	
30	R/L	I	Horizontal display mode select signal	
30	R/L	1	L : Normal ,H : Left /Right reverse mode	Note5-2
31 U/D I		Ι	Vertical display mode select signal	110160-2
	0,0	1	H : Normal ,L : Up/Down reverse mode	
32	V/Q	I	H : Normal	
33	GND	-	GND	

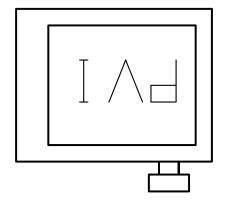
Note5-1 : The horizontal display start timing is settled in accordance with rising of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 10-2. Don't keep ENAB"High" during operation.

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Note 5-2 : The definitions of U/D & R/L





R/L(PIN 30)= High , U/D(PIN 31)= Low

#### 5-2) LED Backlight driving

R/L(PIN 30)= Low, U/D(PIN 31)= High

Connector type : JST BHR-03VS-1, PIN No 3 pins

Pin No	Symbol	Description	Remark
1	+	Input terminal (Anode)	Red
2	NC	No connect	-
3	-	Input terminal (Cathode)	Black

#### 6.Absolute Maximum Ratings:

GND=0V, Ta=25℃

Parameters	Symbol	Condition	MAX.	Unit	Remark
Input Voltage	Vi	<b>Ta=25</b> ℃	-0.3~+4	V	Note6-1
+3.3V supply voltage	V <sub>CC</sub>	<b>Ta=25</b> ℃	0~+3.6	V	

Note6-1 : CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB,R/L,U/D,V/Q

#### **7.Electrical Characteristics**

#### 7-1) Recommended Operating Conditions:

Item Symbol Min. Max. Unit Remark Тур. +3.3V Supply Voltage V<sub>cc</sub> V +3.0 +3.3 +3.6 Permissive input ripple voltage  $V_{\mathsf{RF}}$ \_ \_ 100 mVp-p V<sub>cc</sub>=3.3V Input voltage(Low) 0 0.3 V<sub>cc</sub> VIL -V 0.7 V<sub>cc</sub> VIH V Input voltage(High) \_ V<sub>cc</sub>

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GND=0V, Ta=25℃



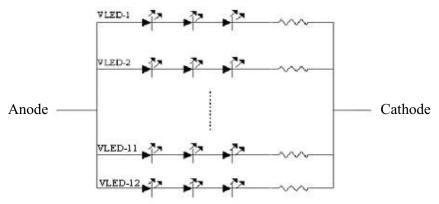
#### 7-2) Recommended Driving Condition for LED Back Light

						Ta=25℃
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
LED voltage	V <sub>LED</sub>	-	-	(11)	V	Note 7-1
LED current	ILED	-	20	-	mA	Note 7-2
Back Light Power Consumption	P <sub>LED</sub>			2640	mW	Note 7-1
Back Light Fower Consumption		-	-	2040	111 VV	Note 7-3

Note 7-1 : The I<sub>LED</sub> = 20 mA (Constant current)

Note 7-2 : The LED driving condition is defined for each LED module. (3 LED Serial) The input current= 20 mA\*12=240 mA.

Note 7-3 :  $P_{LED} = V_{LED-1} * I_{LED-1} + V_{LED-2} * I_{LED-2} + V_{LED-11} * I_{LED-11} + V_{LED-12} * I_{LED-12}$ 



#### 7-3) Power Consumption

Parameters	Symbol	Тур.	Max.	Unit	Remark
+3.3V Current Dissipation	I <sub>cc</sub>	67	100	mA	
LCD Panel Power Consumption	-	0.22	0.33	W	Note 7-4
Total Power Consumption	-	-	2.97	W	

Note 7-4 : The power consumption for back light is not included.

#### 8. Pixel Arrangement

The LCD module pixel arrangement is stripe configuration.

RGBRGBRGB 1 st Line RGBRGB 2 nd Line RGB 3 rd Line	R G B R G B R G B
1 st Pixel 1 Pixel = RGB	640 th pixel
RGBRGBRGB RGBRGBRGB	R G B R G B R G B

#### 9. Display Color and Gray Scale Reference

Color								In	put	Co	olor	Da	ta						
				Re	əd					Gre	een					Bl	ue		
		R5	R4	R3	R2	<b>R1</b>	R0	G5	G4	G3	G2	G1	G0	B5	<b>B4</b>	<b>B</b> 3	<b>B2</b>	B1	<b>B0</b>
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (63)	1	1	1	┺	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red (02)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker																		
Red	↓	↓	$\downarrow$	$\downarrow$	↓	↓	↓	↓	$\downarrow$	$\downarrow$	$\downarrow$	$\downarrow$	↓	↓	$\downarrow$	↓	¥	¥	↓
	Brighter																		
	Red (61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green (02)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
_	Darker																		
Green	↓	↓	¥	$\downarrow$	¥	$\downarrow$	$\downarrow$	↓	$\downarrow$	$\downarrow$	V	V	$\downarrow$	↓	$\downarrow$	$\downarrow$	$\downarrow$	¥	$\downarrow$
	Brighter																		
	Green (61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue (02)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	Darker		,	,		,			,	,	,	,	,				,	,	
Blue	↓	↓	Ŷ	Ŷ	Ŷ	Ŷ	$\downarrow$	↓	Ŷ	$\downarrow$	Ŷ	Ŷ	$\downarrow$	↓	↓	↓	↓	$\downarrow$	$\downarrow$
	Brighter																		
	Blue (61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

#### 10. Interface Timing 10-1) Timing Parameters

Parameter		Symbol	Min.	Тур.	Max.	Unit
	Frequency	1/Teph		25.2	27.8	MHz
Clock	"High" time	Tcwh	18			ns
	"Low" time	Tcwl	18			ns
Data	Setup time	Tdsu	10			ns
Data	Hold time	Tdhđ	10			ns
	Period	TH	30.00	31.78		us
Hsync	Fenod	ш	770	800	900	clock
	Pulse width	$\mathrm{TH}_{\mathrm{WH}}$	5	30		clock
	Period	TV	515	525	560	line
Vsync	Pulse width	Twv	1	3	5	line
Horizontal display period		THd	640	640	640	clock
Vertical display period		TVđ	480	480	480	line

Note 10-1 : In case of low-frequency, the deterioration of display quality, flicker etc., may occur.

#### 10-2) Display Position in horizontal direction

			0	, ,	<u> </u>	•	
	Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
ENAB	ENAB Setup time		10			ns	
signal	Pulse width	Tep	2	640	640	clock	
	ifference between and ENAB signal	THs	112	144	175	clock	

Display position in horizontal direction is designated by rising timing of ENAB signal.

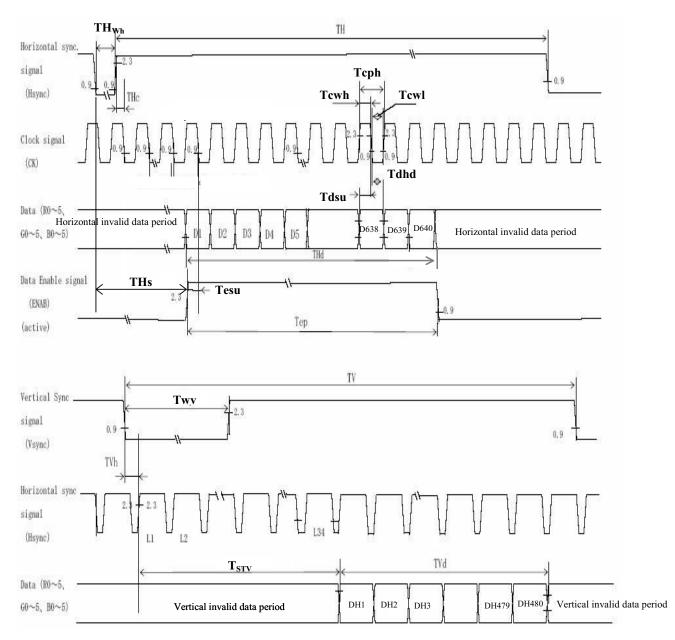
When ENAB is fixed to "Low", the horizontal display will starts from 144<sup>th</sup> clock after HS falling.

#### 10-3) Display Position in vertical direction

Display start position in vertical direction is fixed to the 35<sup>th</sup> line. The ENAB signal does not relate to Vertical display position.

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#### 10-4) Timing Diagram

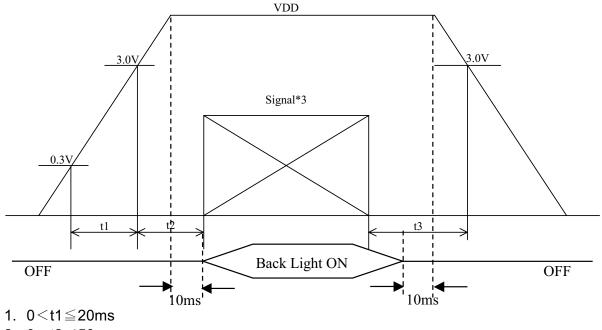


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# **O**PRIME VIEW

# PD057VT1

#### 11. Power On Sequence



2.  $0 < t2 \le 50 ms$ 

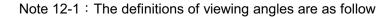
3.  $0 < t3 \le 1s$ 

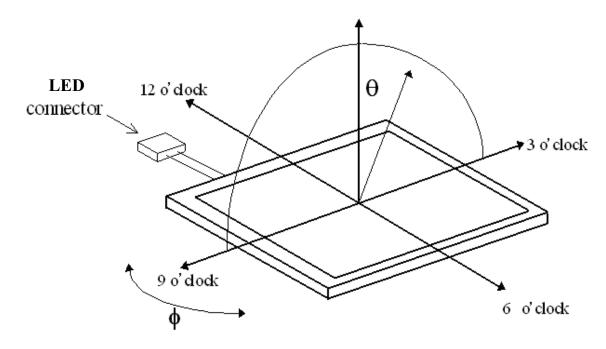
# 12. Optical Characteristics 12-1) Specification:

Ta=25℃										
Parameter		Symbol	Condition	MIN.	TYP.	MAX.	Unit	Remarks		
Viewing Angle	Horizontal	θ	CR≧10	70	75	-	deg	Note 12-1		
	Vertical	$\theta$ (to 6 o'clock)		65	70	-	deg			
		$\theta$ (to 12 o'clock)		45	50	-	deg			
Contrast Ratio		CR	At optimized viewing angle	400	600	-	-	Note 12-2		
Response time	Rise	Tr	<i>θ</i> =0°	-	15	30	ms	Note 12-3		
	Fall	Tf		-	25	50	ms			
Brightness		-	<i>θ</i> =0°/ <i>φ</i> =0	450	500	-	cd/m²	Note 12-4		
Luminance Uniformity		U%		80	85	-	%	Note 12-5		
White Chromaticity		x		0.26	0.30	0.34	-			
		У		0.29	0.33	0.37	-			
Cross Talk		-	<i>θ</i> =0°	-	-	3.5	%	Note 12-7		
LED Life Time		-		20000	-	-	hrs	Note 12-6		

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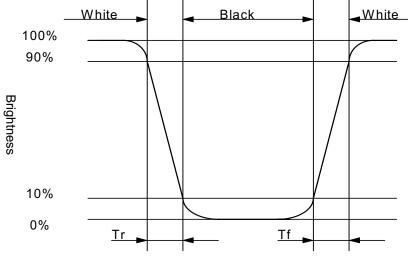






Note 12-2: The definition of contrast ratio  $CR = \frac{Luminance at White Pattern}{Luminance at Black Pattern}$ 

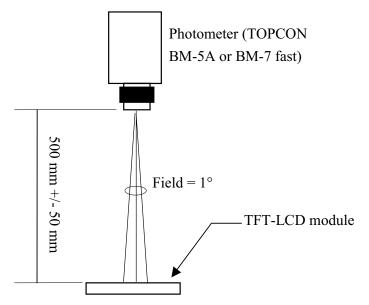
Note 12-3 : Definition of Response Time Tr and Tf

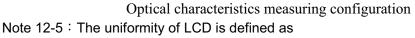


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Note 12-4 : All optical measurements shall be performed after backlight being turned-on for 30 mins. The optical characteristics shall be measured in dark room (ambient illumination on panel surface less than 1 Lux). The measuring configuration shows as following figure.





U =<u>The Minimum Brightness</u> of the 9 testing Points

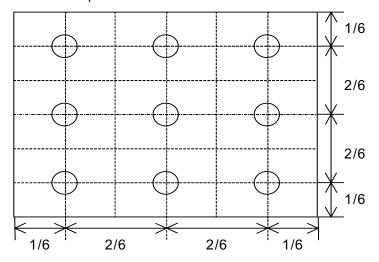
The Maximum Brightness of the 9 testing Points

Luminance meter: BM-5A or BM-7 fast (TOPCON)

Measurement distance: 500 mm +/- 50 mm

Ambient illumination: < 1 Lux

Measuring direction: Perpendicular to the surface of module The test pattern is white.



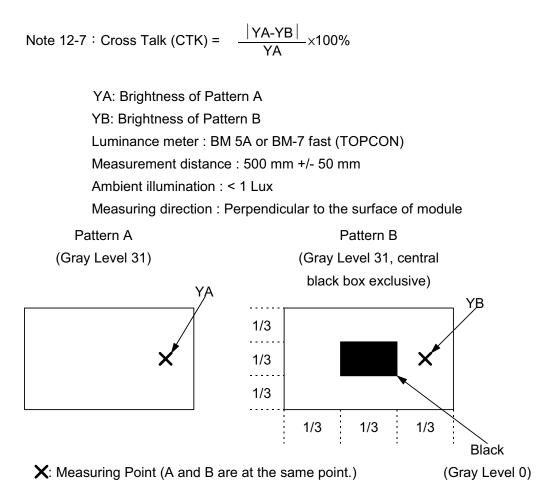
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# PD057VT1

Note 12-6 : The "LED Life time " is defined as the module brightness decrease to 50% original Brightness that the ambient temperature is  $25^{\circ}$ C and  $I_{LED} = 240$ mA.



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#### 13. Handling Cautions

#### 13-1) Mounting of module

- a) Please power off the module when you connect the input/output connector.
- b) Polarizer which is made of soft material and susceptible to flaw must be handled carefully.
- c) Protective film (Laminator) is applied on surface to protect it against scratches and dirt.
- d) Please following the tear off direction as figure 13-1 to remove the protective film as slowly as possible, so that electrostatic charge can be minimized.

#### 13-2) Precautions in mounting

- a) When metal part of the TFT-LCD module (shielding lid and rear case) is soiled, wipe it with soft dry cloth.
- b) Wipe off water drops or finger grease immediately. Long contact with water may cause discoloration or spots.
- c) TFT-LCD module uses glass which breaks or cracks easily if dropped or bumped on hard surface. Please handle with care.
- d) Since CMOS LSI is used in the module. So take care of static electricity and earth yourself when handling.

#### 13-3) Adjusting module

- a) Adjusting volumes on the rear face of the module have been set optimally before shipment.
- b) Therefore, do not change any adjusted values. If adjusted values are changed, the specifications described may not be satisfied.

#### 13-4) Others

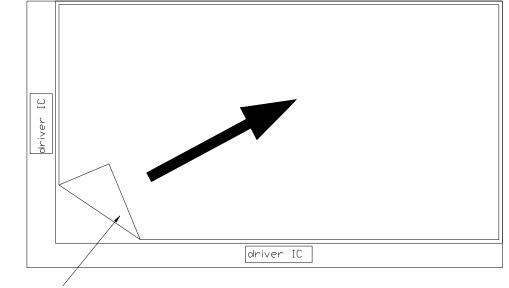
- a) Do not expose the module to direct sunlight or intensive ultraviolet rays for many hours.
- b) Store the module at a room temperature place.
- c) The voltage of beginning electric discharge may over the normal voltage because of leakage current from approach conductor by to draw lump read lead line around.
- d) If LCD panel breaks, it is possibly that the liquid crystal escapes from the panel. Avoid putting it into eyes or mouth. When liquid crystal sticks on hands, clothes or feet. Wash it out immediately with soap.
- e) Observe all other precautionary requirements in handling general electronic components.
- f) Please adjust the voltage of common electrode as material of attachment by 1 module.

#### 13-5) Polarizer mark

The polarizer mark is to describe the direction of wide view angle film how to match up with the rubbing direction.

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Protective film

Figure 13-1 the way to peel off protective film

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#### 14. Reliability Test

No	Test Item	Test Condition	Remark
1	High Temperature Storage Test	Ta = +80℃, 240 hrs	
2	Low Temperature Storage Test	Ta = -40°C, 240 hrs	
3	High Temperature Operation Test	Ta = +70℃, 240 hrs	
4	Low Temperature Operation Test	Ta = -30℃, 240 hrs	
_	High Temperature & High Humidity	Ta = +60℃, 90%RH, 240 hrs	
5	Operation Test	(No Condensation)	
0	Thermal Cycling Test	-20°C →+70°C, 200 Cycles	
6	(non-operating)	30 min 30 min	
7	Vibration Test	Frequency: $10 \sim 57 \text{ H}_2/\text{Vibration Width:}0.075\text{mm}$ 58-500 Hz / Gravity: 9.8m/s <sup>2</sup>	
	(non-operating)	Sweep time: 11 minutes	
		Test period: 3 hrs for each direction of X, Y, Z Gravity: 490m/s <sup>2</sup> * 6ms	
0	Shock Test	Direction: $\pm X$ , $\pm Y$ , $\pm Z$	
	(non-operating)	3 times for each direction	
		150pF,330Ω	
	Electrostatic Discharge Test (non-operating)	Air:±15KV;Contact:±8KV	
		10 times/point <sup>,</sup> 9 points/panel face	

Ta: ambient temperature

Note: The protective film must be removed before temperature test

#### [Criteria]

In the standard conditions, there is not display function NG issue occurred. (Including : line defect, no image) All the cosmetic specification is judged before the reliability stress.

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#### 15. Packing Diagram

