



January 2008

This device is obsolete, January 2008 This is replaced by the LV91xxDV device For new designs use the LV93xxDV device

- Pletronics' LV11B Series is a quartz crystal controlled precision square wave generator with an LVDS output.
- Solder pad compatible with many 9x14 Plastic J-lead packages.
- FR4 base with a mechanical metal cover.
- Tape and Reel or cut tape packaging is available.
- 1 to 700 MHz
- 9.9mm x 13.97mm 'B' package
- Enable/Disable Function on pad 2
- Does NOT permit "wired-OR" of outputs
- Low Jitter

Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2002/95/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 2.18 grams or .82 grams or 1.51 grams Moisture Sensitivity Level: 1 As defined in J-STD-020C Second Level Interconnect code: e4

Absolute Maximum Ratings:

Parameter	Unit
V _{cc} Supply Voltage	-0.5V to +7.0V
Vi Input Voltage	-0.5V to V _{CC} + 0.5V
Vo Output Voltage	-0.5V to V _{CC} + 0.5V

Thermal Characteristics

The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 60 to 100°C/Watt depending on the solder pads, ground plane and construction of the PCB.



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Part Number:

LV11	45	В	Ε	V	-125.0M	-XX		Part Marking:
							Packaging code or blank T250 = 250 per Tape and Reel T500 = 500 per Tape and Reel T1K = 1000 per Tape and Reel	PLE LV11B FFFF.F M • YMDXX
							Frequency in MHz	or LV11B<i>X</i>
							Supply Voltage V _{cc} V = 3.3V <u>+</u> 10%	FFFF.F M PLE XX • YYWWXX
							Optional Enhanced OTR Blank = Temp. range -10 to +70°C E = Temp. range -40 to +85°C	
							Series Model	
							Frequency Stability 45 = ± 50 ppm 44 = ± 25 ppm 20 = ± 20 ppm	
							Series Model	

Legend:

PLE = Pletronics

FFFF.FM = Frequency in MHz

YMD or YYWW = Date of Manufacture (Year - month - day or year and week)

All other marking is internal factory codes

Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD

Code	6	7	8	9	0	1	2
Year	2006	2007	2008	2009	2010	2011	2012

Code	Α	В	С	D	E	F	G	Н	J	K	L	M
Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	Α	В	С
Day	1	2	3	4	5	6	7	8	9	10	11	12
Code	D	E	F	G	Н	J	K	L	М	N	Р	R
Day	13	14	15	16	17	18	19	20	21	22	23	24
Code	Т	U	٧	W	Х	Υ	Z					
Day	25	26	27	28	29	30	31					



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Electrical Specification for 3.30V ±10% over the specified temperature range

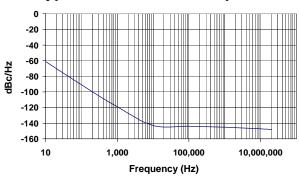
Item	Min	Max	Unit	Condition		
Frequency Range	1	700	MHz			
Frequency Accuracy "45"	-50	+50	ppm		voltages, load changes, aging for 1	
"44"	-25	+25		year, shock, v	ibration and temperatures	
"20"	-20	+20				
Output Waveform		LVDS				
Output High Level		1.60	Volts			
Output Low Level	0.90		Volts			
Differential Output (V _{OD})	250	450	mVolts		See load circuit R1 = 50 ohms	
Output Offset Voltage (Vos)	1.125	1.375	Volts	≥ 80 MHz		
Differential Output Error (dV _{os})	1	50	mVolts			
Output Symmetry	45	55	%	Referenced to	50% of amplitude or crossing point	
Output T _{RISE} and T _{FALL}	300	700	pS	≥ 80 MHz	Vth is 20% and 80% of waveform	
	400	900	pS	< 80 MHz	VIII IS 20% and 60% of waveloring	
Jitter	ı	0.2	pS RMS	Measured from	n 12KHz to 20MHz from Fnominal	
	•	2.8		Measured from	n 10Hz to 1MHz from Fnominal	
Vcc Supply Current	-	90	mA	≥ 80 MHz	Includes current of properly terminated device	
Enable/Disable Internal Pull-up	50	-	Kohm	To Vcc (equiva	alent resistance)	
V disable	-	0.8	Volts	Referenced to	Ground	
V enable	2.0	-	Volts			
Enable	-	100	nS	Time for outpu	ut to reach a logic state	
Disable time	-	100	nS	Time for output to reach a high Z state		
Start up time	-	5	mS	≥ 80 MHz Measured from the time Vcc = 3.0V		
Operating Temperature Range	-10	+70	°C	Standard Tem	perature Range	
	-40	+85	°C	Extended Temperature Range "E" Option		
Storage Temperature Range	-55	+125	°C			

Specifications with Pad 2 E/D open circuit

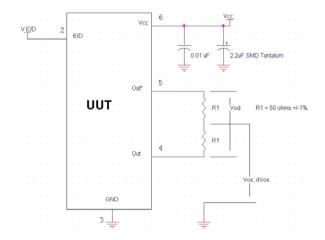


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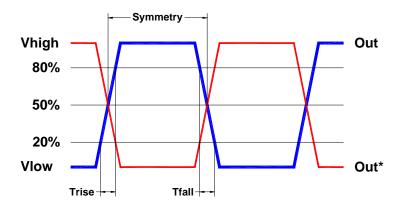
Typical Phase-Noise Response



Load Circuit



Test Waveform





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Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

ESD Rating

Model	Minimum Voltage	Conditions		
Human Body Model	1500	MIL-STD-883 Method 3115		
Charged Device Model	1000	JESD 22-C101		

Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

P/N: LV1145BV-100.0M

Customer P/N: 12345678

Qty: 1000

O627B6

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

2nd LvL Interconnect

Category=e4

Max Safe Temp=245C for 10s 2X Max

Layout and application information

Recommend connecting Pad 1 and Pad 2 together to permit the design to accept Enable/Disable on both input pads

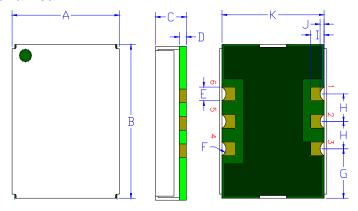
For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.



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Mechanical:



FR4 PCB Base:

Solder masked

All via holes tented on bottom Copper Clad $\frac{1}{2}$ oz. Typical Gold plated 0.02 µinch (0.5 µm) Label:

White Kapton with Black Letters –or--

Blue Epoxy heat cure ink covering top with laser marked lettering

Cover:

Centered on the base 304 Stainless Steel 0.010 inch (0.25µm) Electroless Nickel Plated 1 µinch (25 µm) typical

Pin 3 Ground plane is typical

Not to scale

	Inches	mm
Α	0.390 <u>+</u> 0.010	9.90 <u>+</u> 0.25
В	0.550 <u>+</u> 0.010	13.97 <u>+</u> 0.25
С	0.125 max	3.17 max
D ¹	0.026 typ.	0.66
E ¹	0.050	1.27
F¹	0.028 R	0.72 R
G¹	0.180	4.57
H¹	0.100	2.54
I ¹	0.050	1.27
J ¹	0.015	0.38
K ¹	0.380	9.65

- The package is not hermetically sealed.
- The sides are intentionally left open to permit cleaning material to freely flow in the package, thus minimizing the accumulation of contaminants during cleaning processes.
- The internal part of the package must be thoroughly dry before operating.

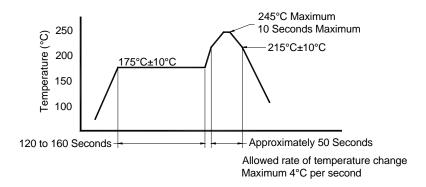
Pad	Function	Note
1	No connect	There is no internal connection to this pad
2	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is <0.30 volts, the output will be inhibited (high impedance state.) Recommend connecting this pad to $V_{\rm CC}$ if the oscillator is to be always on.
3	Ground (GND)	
4	Output	The outputs must be terminated, 100 ohms between the outputs is the ideal
5	Output*	termination. When disabled, the outputs are not in a high impedance state - can not be wire - or connected
6	Supply Voltage (V _{cc})	Recommend connecting appropriate power supply bypass capacitors as close as possible.





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Reflow Cycle (typical for lead free processing)



The part may be reflowed 2 times without degradation.

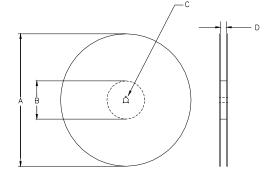
Tape and Reel: available for quantities of 250 to 1000 per reel, cut tape for < 250

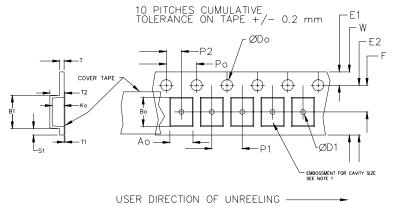
	Constant Dimensions Table 1										
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max			
8mm		1.0			2.0						
12mm	1.5	1.5	1.75	4.0	<u>+</u> 0.05						
16mm	+0.1 -0.0	1.5	<u>+</u> 0.1	<u>+</u> 0.1	2.0	0.6	0.6	0.1			
24mm		1.5			<u>+</u> 0.1						

	Variable Dimensions Table 2										
Tape Size	B1 Max	E2 Min	F	P1	T2 Max	W Max	Ao, Bo & Ko				
24 mm	12.1	14.25	7.5 <u>+</u> 0.1	16.0 <u>+</u> 0.1	8.0	16.3	Note 1				

Note 1: Embossed cavity to conform to EIA-481-B

Dimensions in mm Not to scale





		REEL DIMENSIONS			
Α	inches	7.0	10.0	13.0	
	mm	177.8	254.0	330.2	
В	inches	2.50	4.00	3.75	
	mm	63.5	101.6	95.3	Tape Width
С	mm	13.0 +0.5 / -0.2			widin
D	mm			24.4 +2.0 -0.0	24.0

Reel dimensions may vary from the above



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