High-gain Amplifier Transistor (32V, 0.3A) 2SD1383K

Features

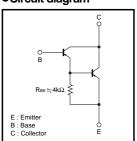
- 1) Darlington connection for high DC current gain.
- 2) Built-in $4k\Omega$ resistor between base and emitter.
- 3) Complements the 2SB852K.

Packaging specifications

Туре	2SD1383K
Package	SMT3
hFE	В
Marking	W*
Code	T146
Basic ordering unit (pieces)	3000

^{*} Denotes hre

●Circuit diagram



● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit		
Collector-base voltage	VcBo 40		V		
Collector-emitter voltage	Vces 32		V *1		
Emitter-base voltage	VEBO	6	V		
Collector current	lc	0.3	A (DC)		
		1.5	A (Pulse) *2		
Collector power dissipation	Pc	0.2	W		
Junction temperature	Tj	150	°C		
Storage temperature	Tstg	-55 to +150	°C		
:4 B 00					

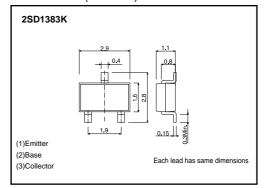
^{*1} R_{BE}=0Ω *2 Single pulse Pw=10ms

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	40	_	-	V	Ic=100μA
Collector-emitter breakdown voltage	BVces	32	-	-	V	Ic=-1mA , R _{BE} =0Ω
Emitter-base breakdown voltage	ВVево	6	-	-	V	Ιε=100μΑ
Collector cutoff current	Ісво	-	_	1	μΑ	Vcb=24V
Emitter cutoff current	ІЕВО	-	-	1	μΑ	V _{EB} =4.5V
DC current transfer ratio	hfe	5000	-	-	-	Vce=5V, Ic=0.1A
Collector-emitter saturation voltage	VCE(sat)	-	_	1.5	V	Ic=200mA, I _B =0.4mA *1
Transition frequency	f⊤	-	250	-	MHz	VcE=5V, IE= -10mA, f=100MHz *2
Output capacitance	Cob	_	3	_	pF	Vcb=10V, Ie=0A, f=1MHz

^{*1} Measured using pulse current.

●Dimensions (Unit:mm)



Electrical characteristic curves

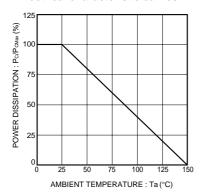


Fig.1 Power dissipation curves

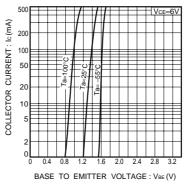


Fig.2 Ground emitter propagation characteristisc

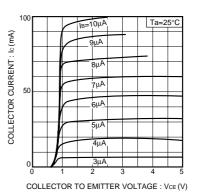


Fig.3 Ground emitter output characteristics

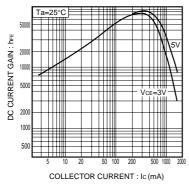


Fig.4 DC current gain vs. collector current ($\rm I$)

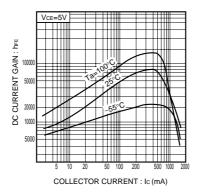


Fig.5 DC current gain vs. collector current ($\rm II$)

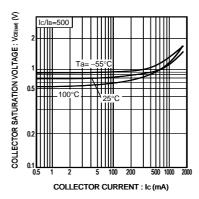


Fig.6 Collector-emitter saturation voltage vs. collector current

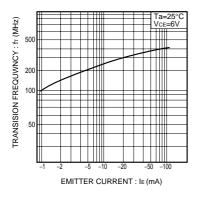


Fig.7 Gain bandwidth product vs. emitter current

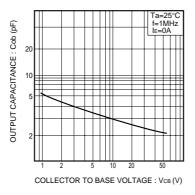


Fig.8 Collector output capacitance vs. collector-base voltage

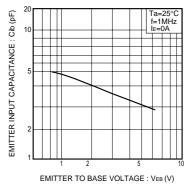


Fig.9 Emitter input capacitance vs. emitter-base voltage

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ROHM CO., LTD. 21 Saiin Mizosaki-cho, Ukyo-ku, Kyoto 615-8585, Japan

an TEL:+81-75-311-2121 FAX:+81-75-315-0172

