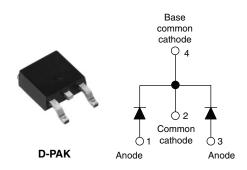


Vishay High Power Products

Schottky Rectifier, 2 x 3.5 A



PRODUCT SUMMARY				
I _{F(AV)}	2 x 3.5 A			
V_{R}	60 V			

FEATURES

- Popular D-PAK outline
- · Center tap configuration
- Small foot print, surface mountable
- · Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for AEC Q101 level

DESCRIPTION

The 6CWQ06FNPbF surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	7	A		
V _{RRM}		60	V		
I _{FSM}	t _p = 5 μs sine	490	A		
V _F	3 Apk, T _J = 25 °C (per leg)	0.61	V		
T _J	Range	- 40 to 150	°C		

VOLTAGE RATINGS			
PARAMETER	SYMBOL	6CWQ06FNPbF	UNITS
Maximum DC reverse voltage	V_{R}	60	V
Maximum working peak reverse voltage	V_{RWM}	00	V

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	per leg		50 % duty cycle at T _C = 133 °C, rectangular waveform		3.5	
See fig. 5	per device	I _{F(AV)}			7	Δ.
Maximum peak one cycle			5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	490	A
non-repetitive surge curren See fig. 7	ıı	I _{FSM}	10 ms sine or 6 ms rect. pulse		70	
Non-repetitive avalanche e	nergy per leg	E _{AS}	E _{AS} T _J = 25 °C, I _{AS} = 1 A, L = 12 mH		6	mJ
Repetitive avalanche curre	nt per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	А

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

Document Number: 94249 Revision: 13-Aug-08

6CWQ06FNPbF

Vishay High Power Products

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		3 A	- T _{.1} = 25 °C	0.61	V
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	6 A	1j=25 C	0.76	
See fig. 1	V FM (*)	3 A	- T _J = 125 °C	0.53	
J		6 A	TJ = 125 C	0.65	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	2	mA
See fig. 2	I IRM (17	T _J = 125 °C		30	
Threshold voltage	V _{F(TO)}	$T_{J} = T_{J} \text{ maximum} $ 0.38 34.31		0.38	V
Forward slope resistance	r _t			mΩ	
Typical junction capacitance per leg	C _T	$V_R = 5 V_{DC}$, (test signal rar	145	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 5.0			nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/µ			V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J ⁽¹⁾ , T _{Stg}		- 40 to 150	°C
Maximum thermal resistance,	per leg	D	DC operation	4.70	°C/W
junction to case	per device	R_{thJC}	See fig. 4	2.35	O/ VV
Approximate weight				0.3	g
Approximate weight				0.01	OZ.
Marking device			Case style D-PAK (similar to TO-252AA)	6CWC	06FN

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

Document Number: 94249 Revision: 13-Aug-08



Schottky Rectifier, 2 x 3.5 A Vishay High Power Products

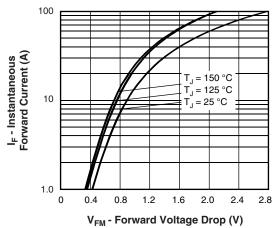


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

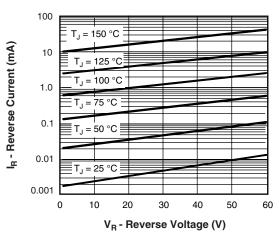


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

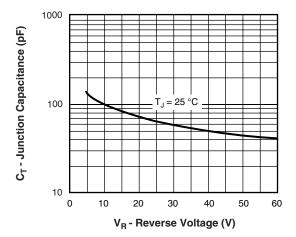


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

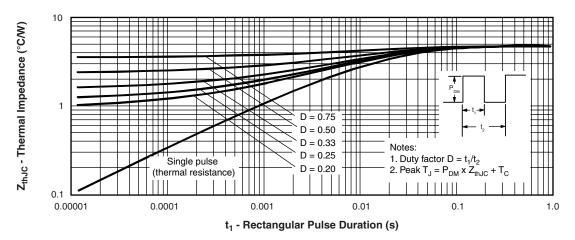
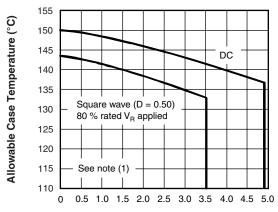


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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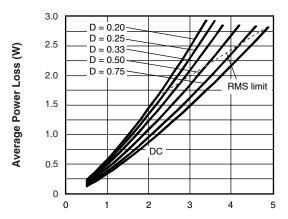
Schottky Rectifier, 2 x 3.5 A





I_{F(AV)} - Average Forward Current (A)

Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)



I_{F(AV)} - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics (Per Leg)

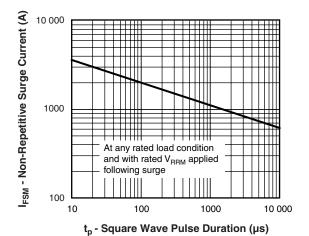


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

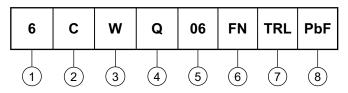
Document Number: 94249 Revision: 13-Aug-08



Schottky Rectifier, 2 x 3.5 A Vishay High Power Products

ORDERING INFORMATION TABLE

Device code



1 - Current rating (7 A)

2 - Center tap configuration

Package identifier:

W = D-PAK

4 - Schottky "Q" series

5 - Voltage rating (06 = 60 V)

6 - FN = TO-252AA (D-PAK)

7 - • None = Tube (50 pieces)

• TR = Tape and reel

• TRL = Tape and reel (left oriented)

• TRR = Tape and reel (right oriented)

None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95016				
Part marking information	http://www.vishay.com/doc?95059			
Packaging information	http://www.vishay.com/doc?95033			



Vishay

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