



## N-Channel 100-V (D-S) MOSFET

## PRODUCT SUMMARY

$V_{DS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A) <sup>a</sup>	$Q_g$ (Typ)
100	0.026 at $V_{GS} = 10$ V	35	31 nC

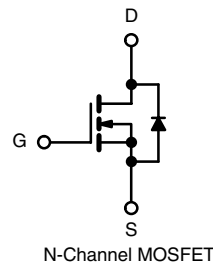
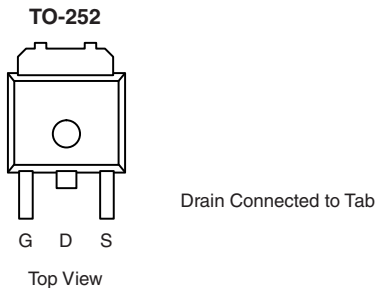
## FEATURES

- TrenchFET<sup>®</sup> Power MOSFET
- 100 % UIS Tested

RoHS  
COMPLIANT

## APPLICATIONS

- Primary Side Switch



Ordering Information: SUD35N10-26P-E3 (Lead (Pb)-free)

ABSOLUTE MAXIMUM RATINGS  $T_A = 25$  °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 175$ °C)	$I_D$	$T_C = 25$ °C	A
		$T_C = 70$ °C	
		$T_A = 25$ °C	
		$T_A = 70$ °C	
Pulsed Drain Current	$I_{DM}$	40	
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25$ °C	50 <sup>e</sup>
		$T_A = 25$ °C	6.9 <sup>b, c</sup>
Avalanche Current Pulse	$I_{AS}$	33	
Single Pulse Avalanche Energy	$E_{AS}$	55	mJ
Maximum Power Dissipation	$P_D$	$T_C = 25$ °C	W
		$T_C = 70$ °C	
		$T_A = 25$ °C	
		$T_A = 70$ °C	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 175	°C

## THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b, d</sup>	$R_{thJA}$	15	18	°C/W
Maximum Junction-to-Case	$R_{thJC}$	1.5	1.8	

Notes:

- Based on  $T_C = 25$  °C.
- Surface Mounted on 1" x 1" FR4 board.
- $t = 10$  s.
- Maximum under Steady State conditions is 50 °C/W.
- Calculated based on maximum junction temperature. Package limitation current is 50 A.

SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	100			V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$		165		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 11		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2.5		4.4	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 100\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 100\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			10	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	40			A
Drain-Source On-State Resistance <sup>a</sup>	$r_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 12\text{ A}$		0.021	0.026	$\Omega$
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 15\text{ V}, I_D = 12\text{ A}$		25		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 12\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		2000		pF
Output Capacitance	$C_{oss}$			180		
Reverse Transfer Capacitance	$C_{rss}$			60		
Total Gate Charge	$Q_g$	$V_{DS} = 50\text{ V}, V_{GS} = 10\text{ V}, I_D = 12\text{ A}$		31	47	nC
Gate-Source Charge	$Q_{gs}$			10		
Gate-Drain Charge	$Q_{gd}$			9		
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		1.5		$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 50\text{ V}, R_L = 5\text{ }\Omega$ $I_D \cong 10\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		10	15	ns
Rise Time	$t_r$			10	15	
Turn-Off Delay Time	$t_{d(off)}$			15	25	
Fall Time	$t_f$			10	15	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$			50	A
Pulse Diode Forward Current <sup>a</sup>	$I_{SM}$				40	
Body Diode Voltage	$V_{SD}$	$I_S = 10\text{ A}$		0.8	1.2	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 10\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		50	75	ns
Body Diode Reverse Recovery Charge	$Q_{rr}$			100	150	nC
Reverse Recovery Fall Time	$t_a$			38		ns
Reverse Recovery Rise Time	$t_b$			12		

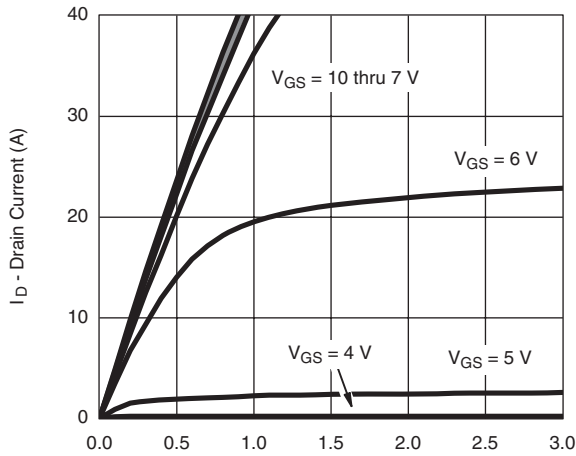
## Notes:

- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
b. Guaranteed by design, not subject to production testing.

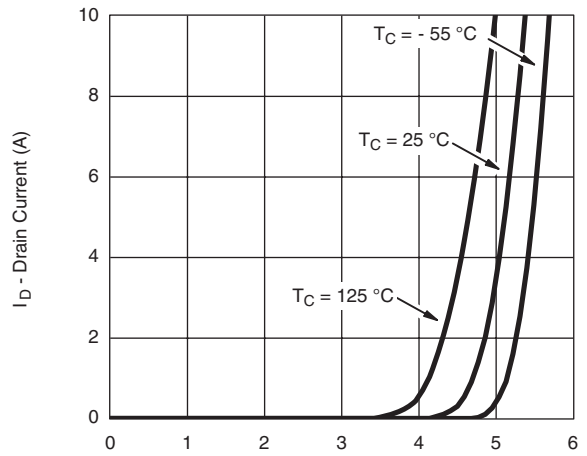
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



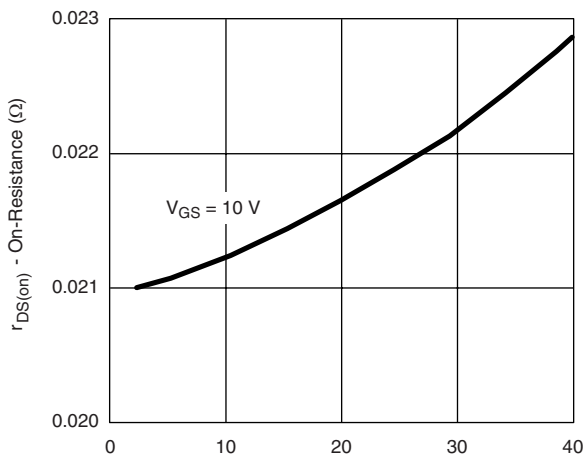
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



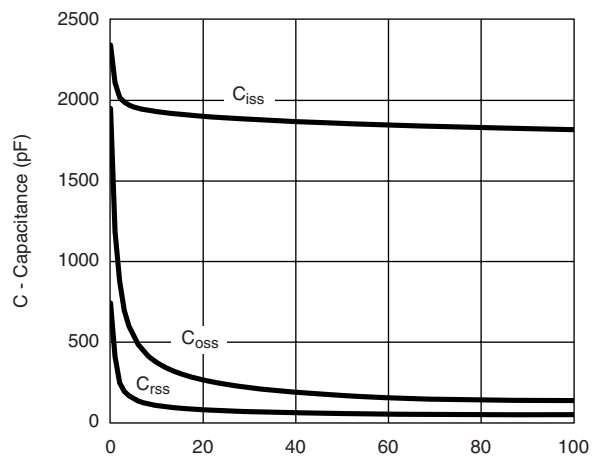
**Output Characteristics**



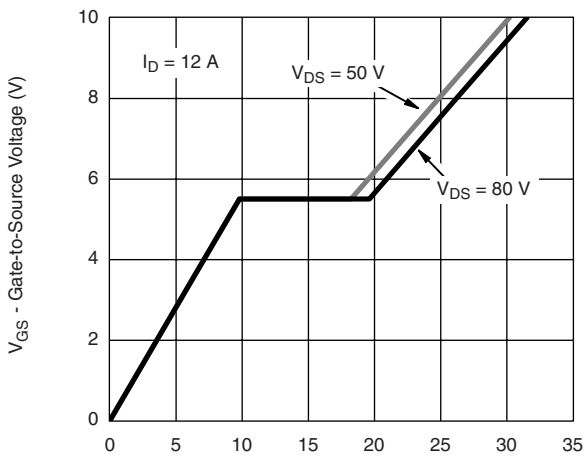
**Transfer Characteristics**



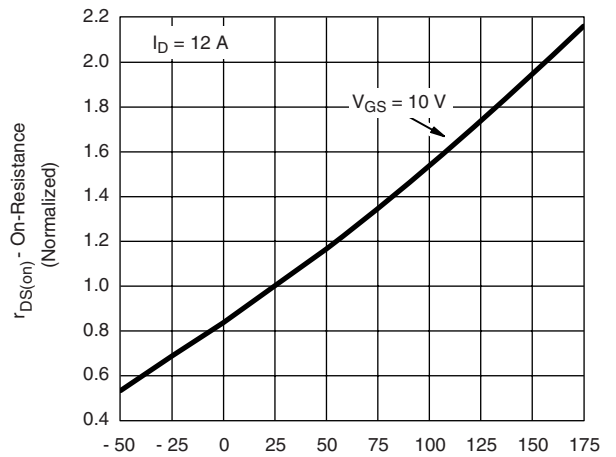
**On-Resistance vs. Drain Current**



**Capacitance**



**Gate Charge**



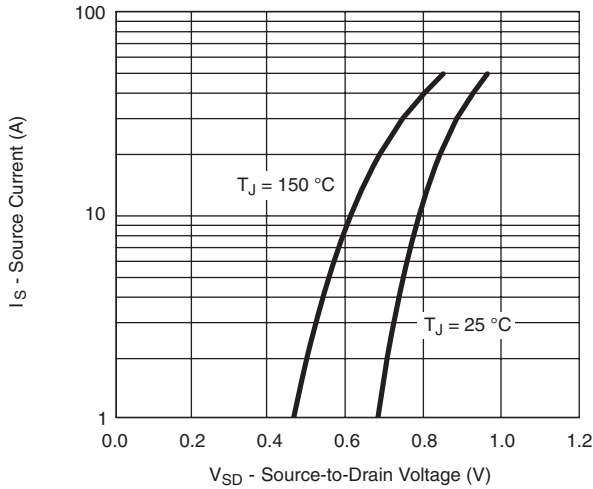
**On-Resistance vs. Junction Temperature**

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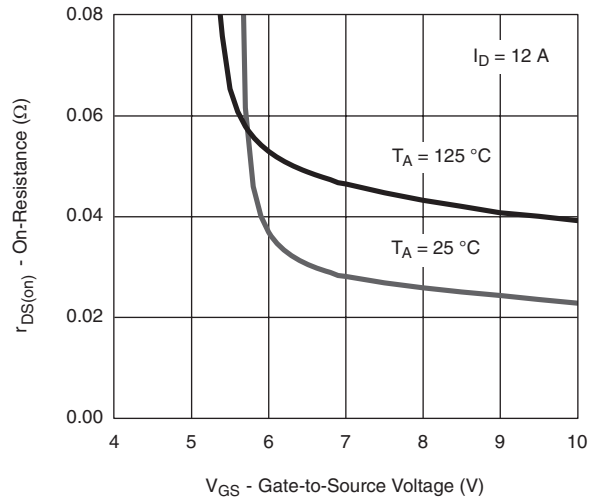


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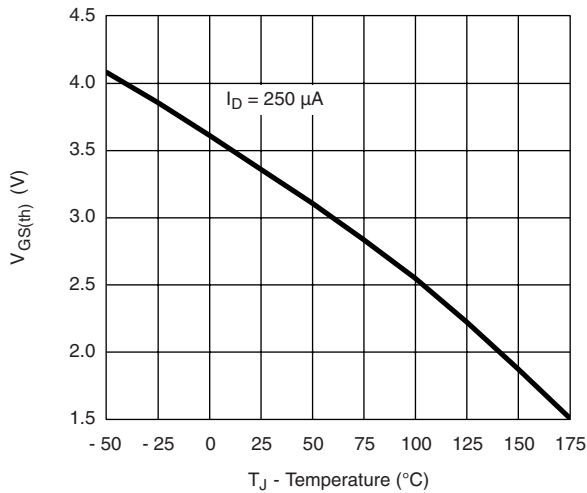
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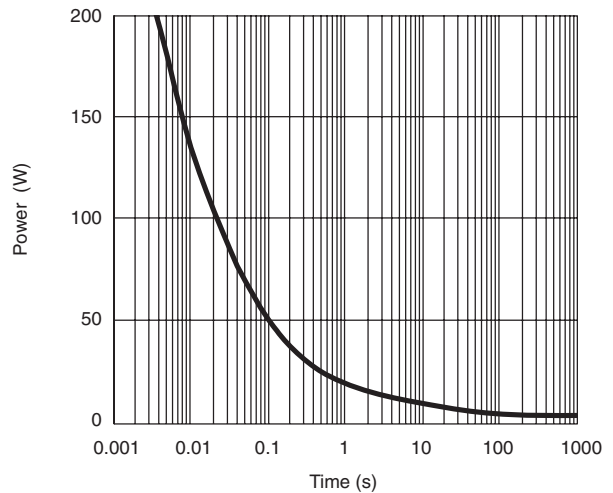
Source-Drain Diode Forward Voltage



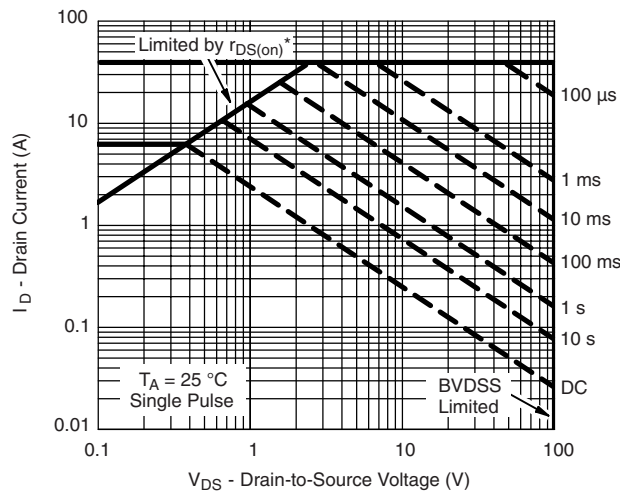
$r_{DS(on)}$  vs.  $V_{GS}$  vs. Temperature



Threshold Voltage



Single Pulse Power, Junction-to-Ambient

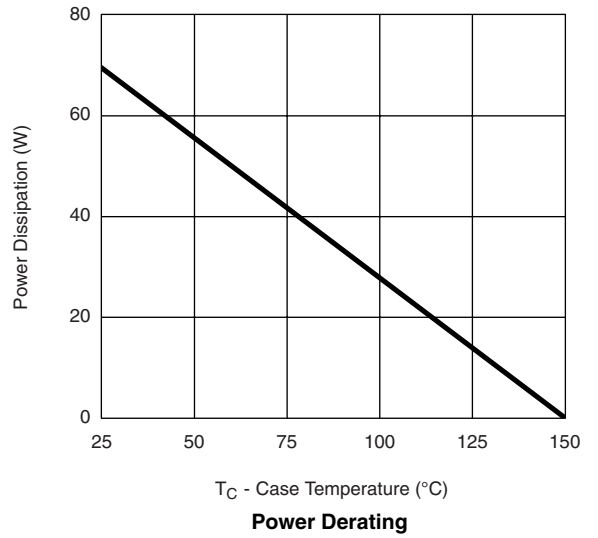
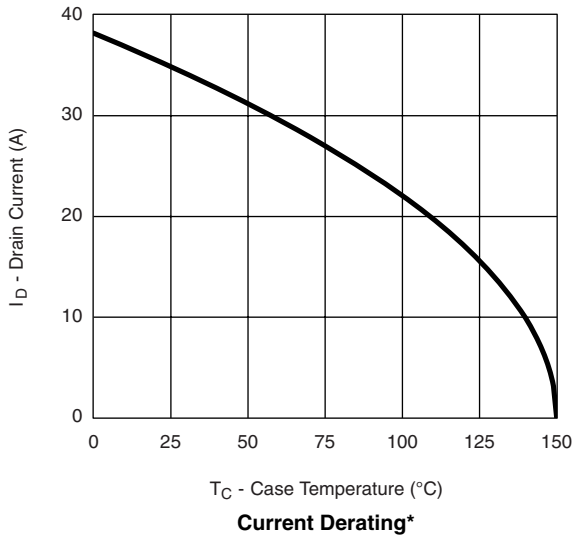


\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $r_{DS(on)}$  is specified

Safe Operating Area



**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



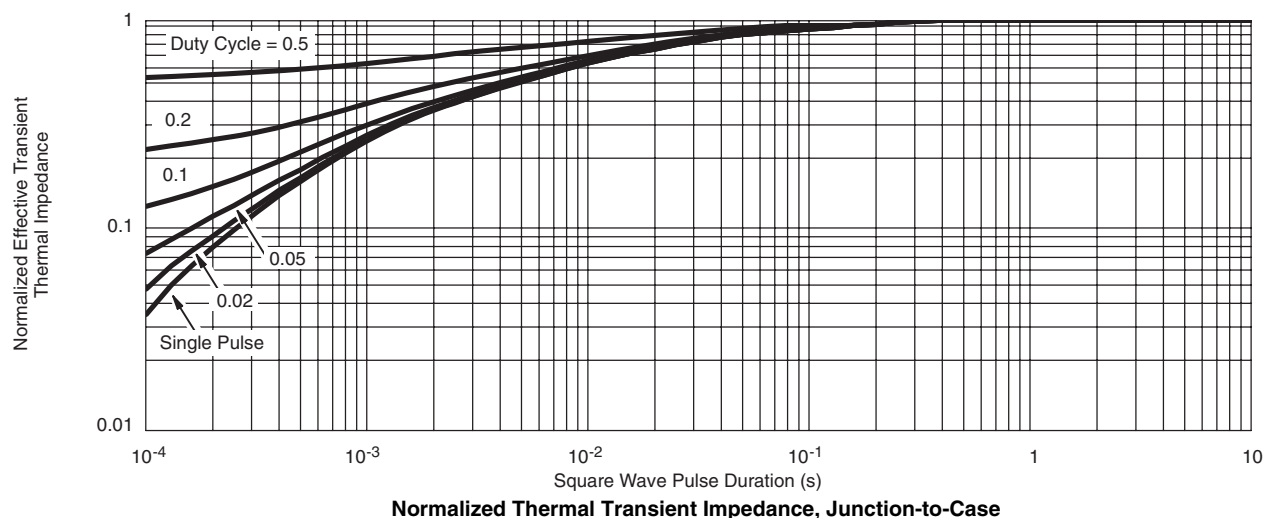
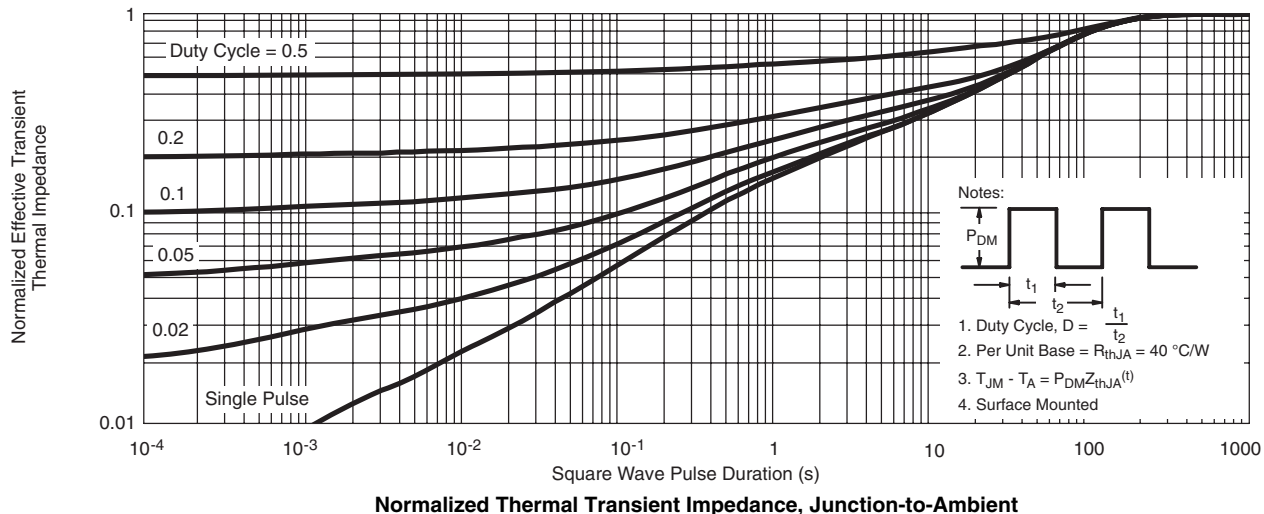
\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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