TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOS V-H)

TPCP8005-H

High-Efficiency DC/DC Converter Applications Notebook PC Applications Portable Equipment Applications

- Small footprint due to a small and thin package
- High-speed switching
- Small gate charge: QSW = 5.0 nC (typ.)
- Low drain-source ON-resistance: RDS (ON) = 9.8 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 30 \text{ S (typ.)}$
- Low leakage current: $IDSS = 10 \mu A \text{ (max) (V}_{DS} = 30 \text{V)}$
- Enhancement mode: $V_{th} = 1.5 \text{ to } 2.5 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

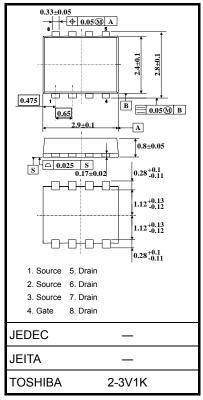
Characte	eristic	Symbol	Rating	Unit	
Drain-source voltage	e voltage		30	V	
Drain-gate voltage (F	$R_{GS} = 20 \text{ k}\Omega$	V_{DGR}	30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	ID	11	Α	
Diam current	Pulsed (Note 1)	I_{DP}	44	A .	
Drain power dissipati	on $(t = 5 s)$ (Note 2a)	P_{D}	1.68	W	
Drain power dissipati	on (t = 5 s) (Note 2b)	P _D	0.84	W	
Single-pulse avalanc	he energy (Note 3)	E _{AS}	78.7	mJ	
Avalanche current		I _{AR}	11	Α	
Repetitive avalanche	energy Note 2a) (Note 4)	E _{AR}	0.137	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature	range	T _{stg}	-55 to 150	°C	

Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

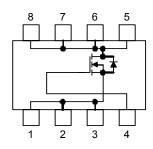
This transistor is an electrostatic-sensitive device. Handle with care.

Unit: mm

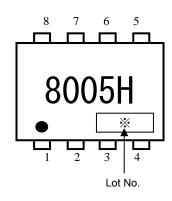


Weight: 0.017 g (typ.)

Circuit Configuration



Marking (Note 5)



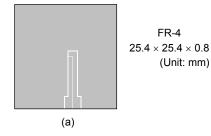
Thermal Characteristics

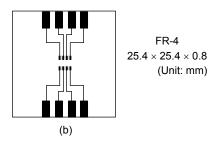
Characteristic	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t=5\;\text{s}) \tag{Note 2a}$	R _{th (ch-a)}	74.4	°C/W
Thermal resistance, channel to ambient $(t = 5 \text{ s})$ (Note 2b)	R _{th (ch-a)}	148.8	°C/W

Note 1: The channel temperature should not exceed 150°C during use.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)

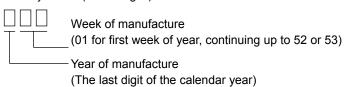




Note 3: $V_{DD} = 24 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 0.5 mH, $R_G = 25 \Omega$, $I_{AR} = 11 \text{A}$

Note 4: Repetitive rating: pulse width limited by max channel temperature

Note 5: * Weekly code: (Three digits)



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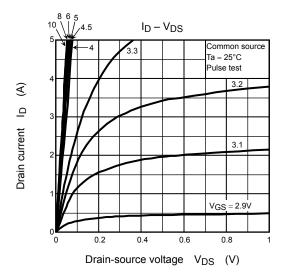
Electrical Characteristics (Ta = 25°C)

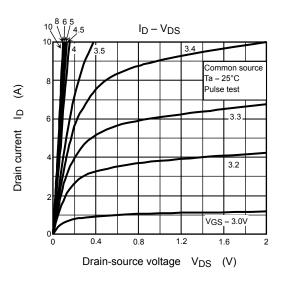
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage current		I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA	
Drain cutoff curre	rain cutoff current		V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μΑ	
Drain aguras bra	akdayın yaltasa	V _{(BR) DSS}	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V	
Drain-source breakdown voltage		V _{(BR) DSX}	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_	٧	
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.5	_	2.5	V	
Drain aguras ON	rociotanos	D= - (-)	V _{GS} = 4.5 V, I _D = 5.5 A	_	12.1	15.7	0	
Drain-source ON-resistance		R _{DS} (ON)	V _{GS} = 10 V, I _D = 5.5 A	_	9.8	12.9	mΩ	
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 5.5 A	15	30	_	S	
Input capacitance	;	C _{iss}		_	1433	2150	pF	
Reverse transfer	capacitance	C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	83	125		
Output capacitan	ce	C _{oss}			303			
Gate resistance		Rg	V _{DS} = 10 V, V _{GS} = 0 V, f = 5 MHz	_	1.0	1.5	Ω	
	Rise time	t _r	V _{GS} 10 V I _D = 5.5 A C _C V _{OUT} C _C C _C	_	3.0	_	ns	
	Turn-on time	t _{on}		_	10	_		
Switching time	Fall time	t _f		_	4.0	_		
	Turn-off time	t _{off}	V _{DD} ≃ 15 V Duty ≦ 1%, t _W = 10 μs	_	22	_		
Total gate charge		Qq	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 11 \text{ A}$	_	20	_		
(gate-source plus	gate-source plus gate-drain)		$V_{DD} \simeq 24 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 11 \text{ A}$	_	11	_	nC	
Gate-source charge 1		Q _{gs1}			4.8	_		
Gate-drain ("Miller") charge		Q _{gd}	$V_{DD} \simeq 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 11 \text{ A}$		3.0	_		
Gate switch charg	ge	Q _{SW}		_	5.0	_		

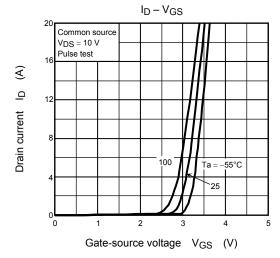
Source-Drain Ratings and Characteristics (Ta = 25°C)

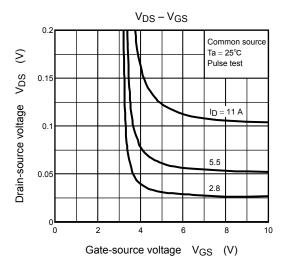
Characteri	stic		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse	(Note 1)	I _{DRP}	_	_	_	44	Α
Forward voltage (diode)			V_{DSF}	I _{DR} = 11 A, V _{GS} = 0 V	_	_	-1.2	V

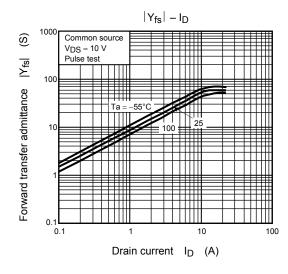
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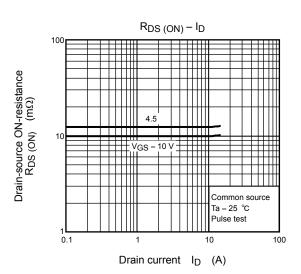


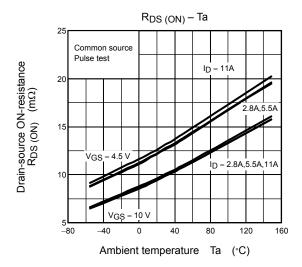


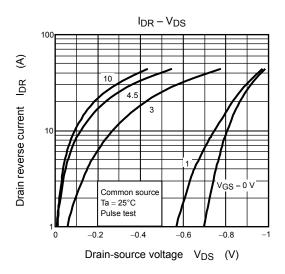


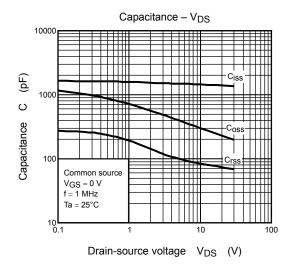


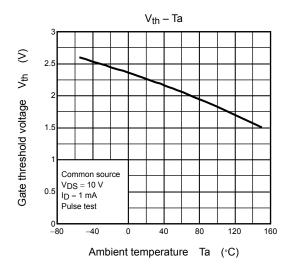


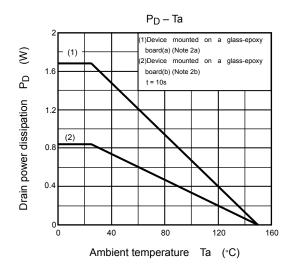


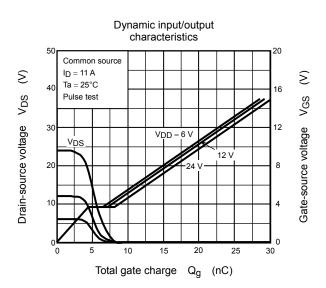




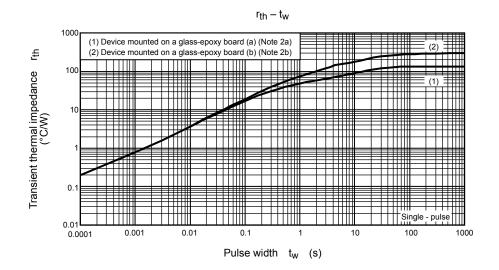


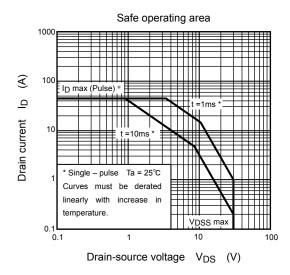






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