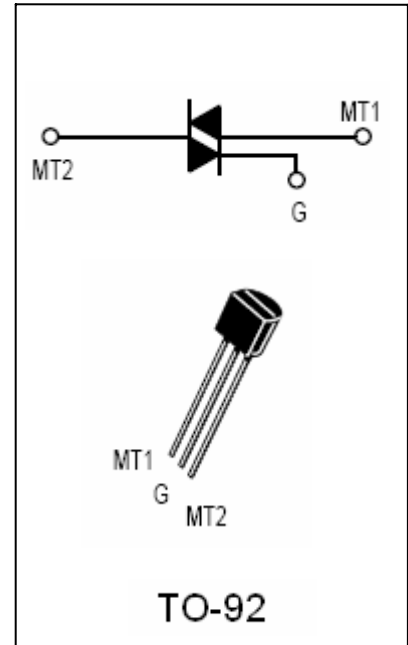


**Triacs**

**MAC97A8**

**FEATURES**

- With TO-92 package
- Designed for use in solid state relays, MPU interface, TTL logic and any other light industrial or consumer application.
- Sensitive gate triggering in four trigger modes for all possible combinations of trigger sources, and especially for circuits that source gate drives.



**ABSOLUTE MAXIMUM RATINGS(Ta=25°C)**

SYMBOL	PARAMETER	MIN	UNIT
V <sub>DRM</sub>	Repetitive peak off-state voltage	600	V
V <sub>RRM</sub>	Repetitive peak off-state voltage	600	V
I <sub>T(AV)</sub>	Average on-state current	0.8	A
I <sub>TSM</sub>	Non-repetitive peak on-state current	8	A
T <sub>j</sub>	Operating junction temperature	110	°C
T <sub>stg</sub>	Storage temperature	-45~150	°C

**CHARACTERISTICS**

T<sub>j</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V <sub>DRM</sub>	Repetitive peak off-state voltage	I <sub>D</sub> =0.1mA	600		V
V <sub>RRM</sub>	Repetitive peak reverse voltage	I <sub>D</sub> =0.2 mA	600		V
I <sub>RRM</sub>	Repetitive peak reverse current	V <sub>RRM</sub> =520V		10	μ A
I <sub>DRM</sub>	Repetitive peak off-state current	V <sub>DRM</sub> =520V		10	μ A
I <sub>GT</sub>	Gate trigger current	V <sub>D</sub> =12V; R <sub>L</sub> =100 Ω	I	6	mA
			II	6	
			III	6	
			IV	35	
V <sub>TM</sub>	On-state voltage	I <sub>T</sub> =1.6A		1.7	V
I <sub>H</sub>	Holding current	I <sub>T</sub> =0.1A ,I <sub>GT</sub> =20mA		8	mA
V <sub>GT</sub>	Gate trigger voltage	V <sub>D</sub> =12V ; R <sub>L</sub> =100 Ω	I	1.5	V
			II	1.5	
			III	1.5	
			IV	1.8	