



**CHENMKO ENTERPRISE CO.,LTD**

**SURFACE MOUNT**

**N-Channel Enhancement Mode Field Effect Transistor**

**VOLTAGE 250 Volts CURRENT 6.7 Ampere**

**CHM634PAPT**

*Lead free devices*

#### APPLICATION

- \* Servo motor control.
- \* Power MOSFET gate drivers.
- \* Other switching applications.

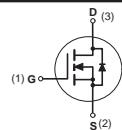
#### FEATURE

- \* Small package. (TO-252A)
- \* Super high dense cell design for extremely low R<sub>DSON</sub>.
- \* High power and current handing capability.

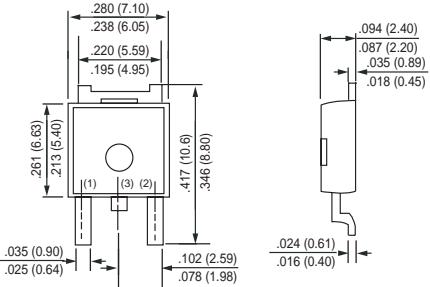
#### CONSTRUCTION

- \* N-Channel Enhancement

#### CIRCUIT



**TO-252A**



1 Gate  
2 Source  
3 Drain (Heat Sink )

Dimensions in inches and (millimeters)

**TO-252A**

#### Absolute Maximum Ratings

T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	CHM634PAPT	Units
V <sub>DSS</sub>	Drain-Source Voltage	250	V
V <sub>GSS</sub>	Gate-Source Voltage	±30	V
I <sub>D</sub>	Maximum Drain Current - Continuous	6.7	A
	- Pulsed (Note 3)	26	
P <sub>D</sub>	Maximum Power Dissipation at T <sub>c</sub> = 25°C	46	W
T <sub>J</sub>	Operating Temperature Range	-55 to 150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C

Note : 1. Surface Mounted on FR4 Board , t <=10sec

2. Pulse Test , Pulse width <= 300us , Duty Cycle <= 2%

3. Repetitive Rating , Pulse width limited by maximum junction temperature

4. Guaranteed by design , not subject to production testing

#### Thermal characteristics

R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient (Note 1)	50	°C/W
2006-02			

## RATING CHARACTERISTIC CURVES ( CHM634PAPT )

**Electrical Characteristics**  $T_A = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units
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### OFF CHARACTERISTICS

$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	250			V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 250 \text{ V}, V_{GS} = 0 \text{ V}$			25	$\mu\text{A}$
$I_{GSSF}$	Gate-Body Leakage	$V_{GS} = 30\text{V}, V_{DS} = 0 \text{ V}$			+100	nA
$I_{GSSR}$	Gate-Body Leakage	$V_{GS} = -30\text{V}, V_{DS} = 0 \text{ V}$			-100	nA

### ON CHARACTERISTICS (Note 2)

$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	2		4	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}, I_D=5.1\text{A}$			450	$\text{m}\Omega$
$g_{FS}$	Forward Transconductance	$V_{DS} = 10\text{V}, I_D = 5\text{A}$		4.4		S

### SWITCHING CHARACTERISTICS (Note 4)

$Q_g$	Total Gate Charge	$V_{DS}=160\text{V}, I_D=5.9\text{A}$ $V_{GS}=10\text{V}$		26	33	nC
$Q_{gs}$	Gate-Source Charge			5		
$Q_{gd}$	Gate-Drain Charge			11		
$t_{on}$	Turn-On Time	$V_{DD}= 100\text{V}$ $I_D=5\text{A}, V_{GS} = 10 \text{ V}$ $R_{GEN}= 50 \Omega$		19	40	nS
$t_r$	Rise Time			11	30	
$t_{off}$	Turn-Off Time			46	90	
$t_f$	Fall Time			10	30	

### DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

$I_s$	Drain-Source Diode Forward Current			8.1	A
$V_{SD}$	Drain-Source Diode Forward Voltage	$I_s = 8.1\text{A}, V_{GS} = 0 \text{ V}$		0.9	1.5