

Silicon PNP Power Transistors

2SA714

DESCRIPTION

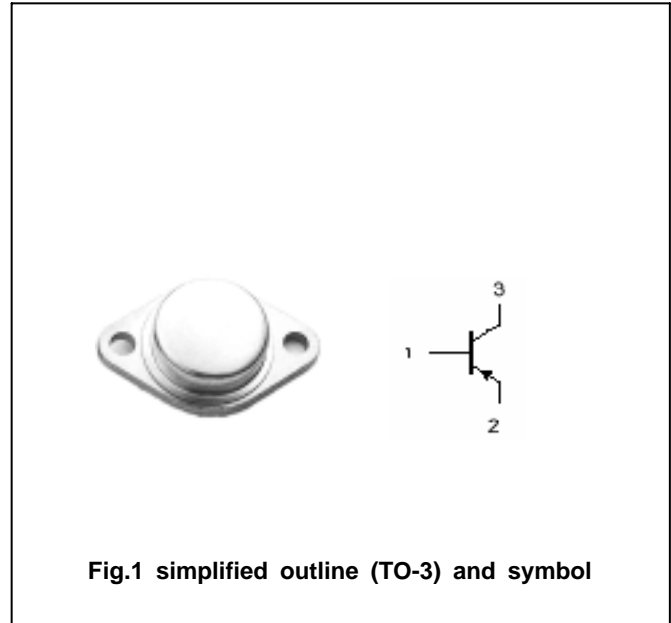
- With TO-3 package
- Wide area of safe operation

APPLICATIONS

- For power amplifier and power switching applications

PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

Absolute maximum ratings($T_a =$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	-150	V
V_{CEO}	Collector-emitter voltage	Open base	-150	V
V_{EBO}	Emitter-base voltage	Open collector	-6	V
I_C	Collector current		-7	A
I_{CM}	Collector current-peak		-11	A
P_C	Collector power dissipation	$T_C=25$	60	W
T_j	Junction temperature		150	
T_{stg}	Storage temperature		-55~150	

Silicon PNP Power Transistors

2SA714

CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-25mA ; I _B =0	-150			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =-1mA ; I _E =0	-150			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-1mA ; I _C =0	-6			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-5A; I _B =-0.5A			-2.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =-5A; I _B =-0.5A			-2.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-150V; I _E =0			-0.1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =-6V; I _C =0			-0.1	mA
h _{FE}	DC current gain	I _C =-1A ; V _{CE} =-5V	60		320	
f _T	Transition frequency	I _C =-1A ; V _{CE} =-5V		8		MHz

Silicon PNP Power Transistors

2SA714

PACKAGE OUTLINE

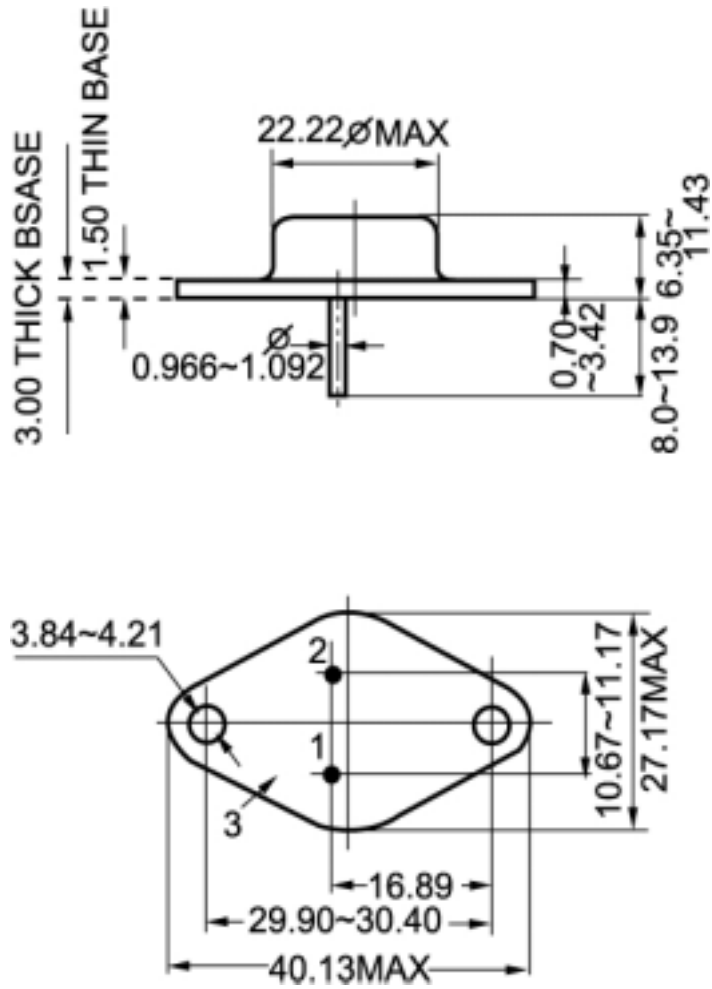


Fig.2 outline dimensions (unindicated tolerance: ± 0.1 mm)