



# SPP1305

## P-Channel Enhancement Mode MOSFET

### DESCRIPTION

The SPP1305 is the P-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and other battery powered circuits where high-side switching , and low in-line power loss are needed in a very small outline surface mount package.

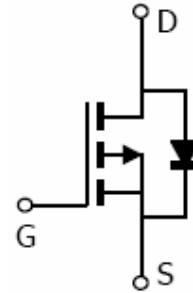
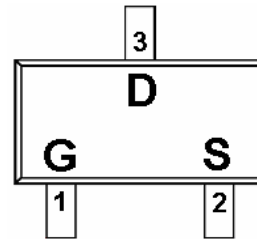
### FEATURES

- ◆ -20V/-0.95A, $R_{DS(ON)}= 280m\Omega@V_{GS}=-4.5V$
- ◆ -20V/-0.80A, $R_{DS(ON)}= 380m\Omega@V_{GS}=-2.5V$
- ◆ -20V/-0.70A, $R_{DS(ON)}= 530m\Omega@V_{GS}=-1.8V$
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOT-323 ( SC-70 ) package design

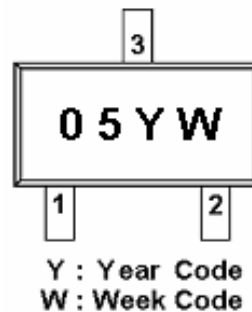
### APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

### PIN CONFIGURATION ( SOT-323 ; SC-70 )



### PART MARKING





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### PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

### ORDERING INFORMATION

Part Number	Package	Part Marking
SPP1305S32RG	SOT-323	05YW

※ Week Code : A ~ Z ( 1 ~ 26 ) ; a ~ z ( 27 ~ 52 )

※ SPP1305S32RG : Tape Reel ; Pb – Free

### ABSOLUTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	-20	V	
Gate –Source Voltage	V <sub>GSS</sub>	±12	V	
Continuous Drain Current(T <sub>J</sub> =150°C)	I <sub>D</sub>	TA=25°C	-1.0	A
		TA=70°C	-0.7	
Pulsed Drain Current	I <sub>DM</sub>	-3	A	
Continuous Source Current(Diode Conduction)	I <sub>S</sub>	-0.28	A	
Power Dissipation	P <sub>D</sub>	TA=25°C	0.33	W
		TA=70°C	0.21	
Operating Junction Temperature	T <sub>J</sub>	-55/150	°C	
Storage Temperature Range	T <sub>STG</sub>	-55/150	°C	
Thermal Resistance-Junction to Ambient	R <sub>θJA</sub>	105	°C/W	



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### ELECTRICAL CHARACTERISTICS

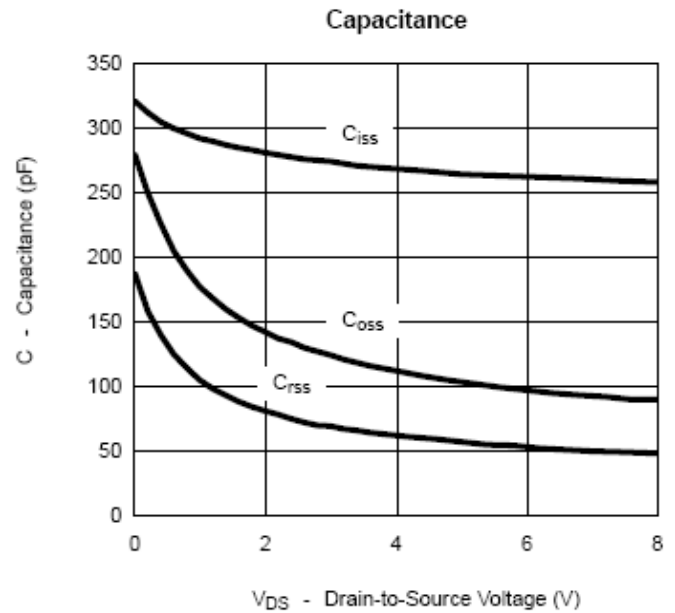
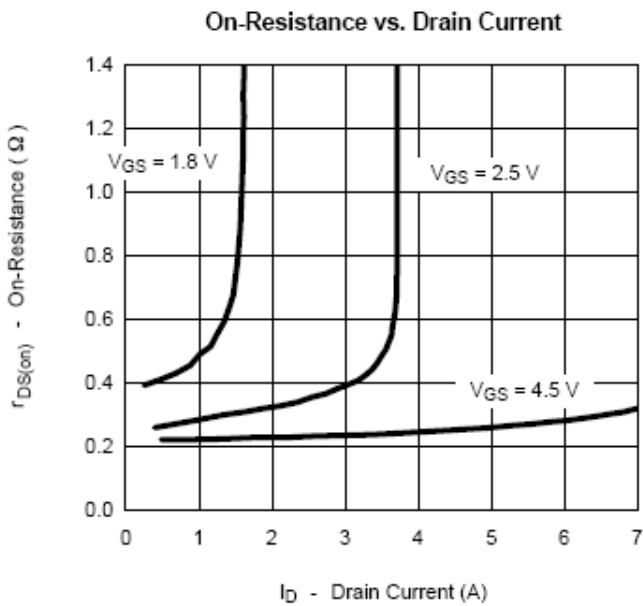
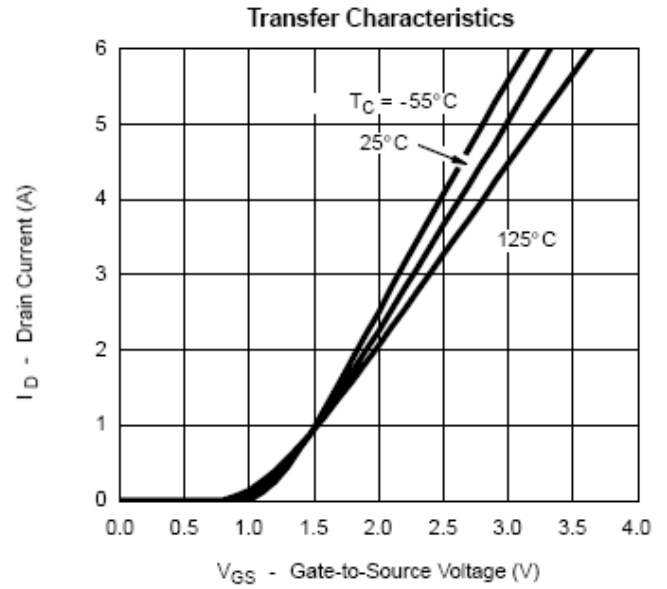
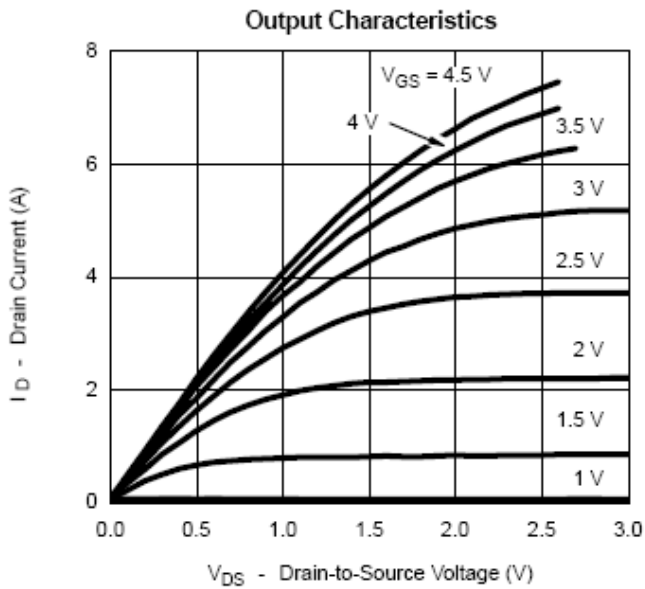
(T<sub>A</sub>=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-20			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-0.5		-1.2	
Gate Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V			-1	uA
		V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V T <sub>J</sub> =55°C			-5	
On-State Drain Current	I <sub>D(on)</sub>	V <sub>DS</sub> ≤ -5V, V <sub>GS</sub> =-4.5V	-6			A
Drain-Source On-Resistance	R <sub>DSS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-0.95A		0.22	0.28	Ω
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-0.80A		0.30	0.38	
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-0.70A		0.42	0.53	
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-1.0A		3.5		S
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-0.5A, V <sub>GS</sub> =0V		-0.8	-1.2	V
<b>Dynamic</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-4V, V <sub>GS</sub> =-4.5V I <sub>D</sub> =-1.0A		3.0	4.2	nC
Gate-Source Charge	Q <sub>gs</sub>			0.6		
Gate-Drain Charge	Q <sub>gd</sub>			0.5		
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-4V, V <sub>GS</sub> =0V f=1MHz		320		pF
Output Capacitance	C <sub>oss</sub>			55		
Reverse Transfer Capacitance	C <sub>rss</sub>			25		
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-4V, R <sub>L</sub> =4Ω I <sub>D</sub> =-1.0A, V <sub>GEN</sub> =-4.5V R <sub>G</sub> =6Ω		10	16	ns
	t <sub>r</sub>			40	60	
Turn-Off Time	t <sub>d(off)</sub>			18	25	
	t <sub>f</sub>			15	20	



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## TYPICAL CHARACTERISTICS

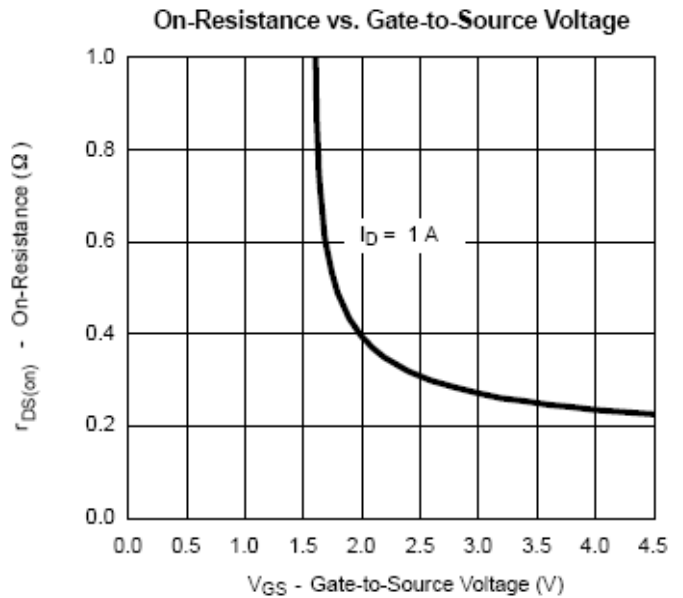
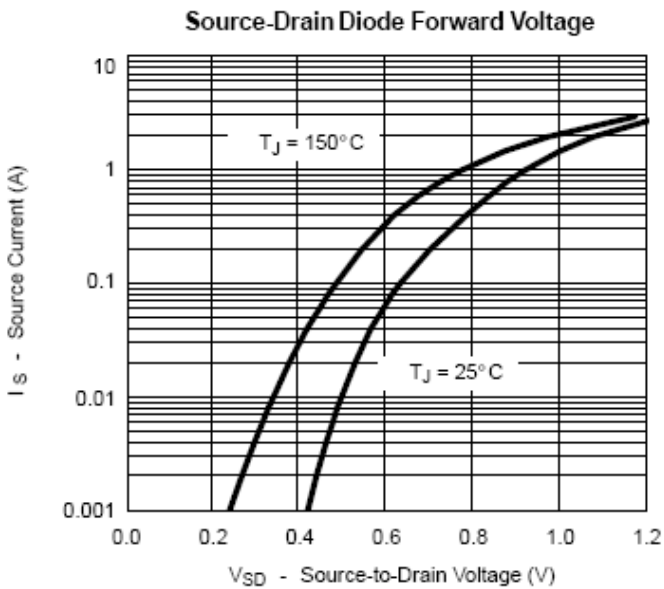
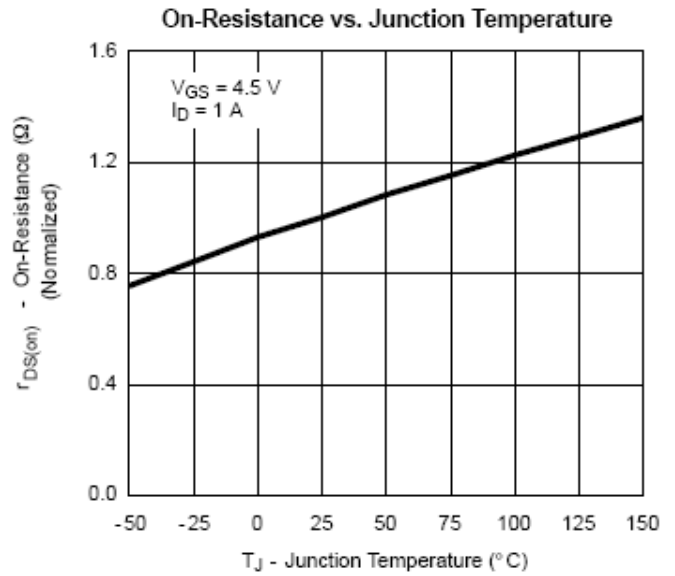
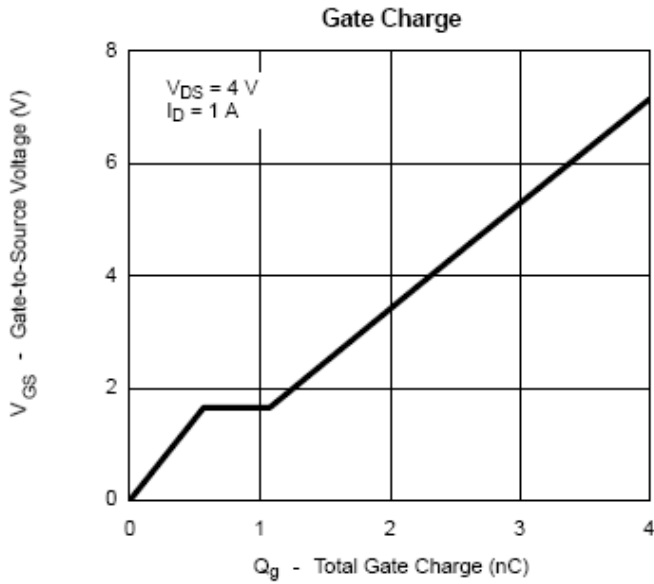




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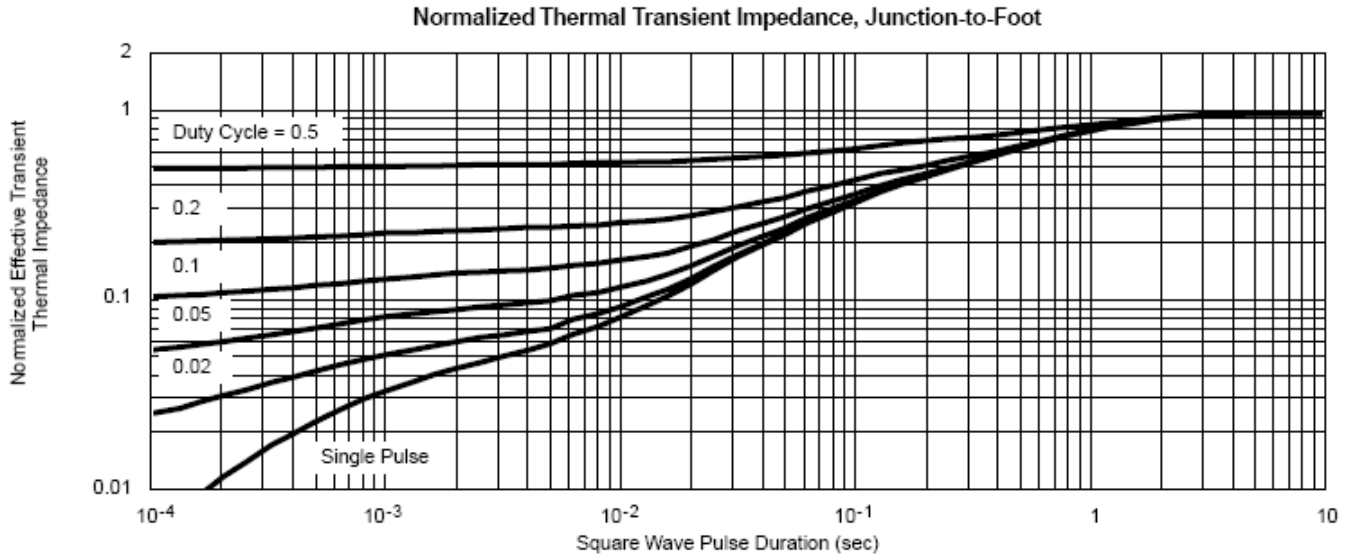
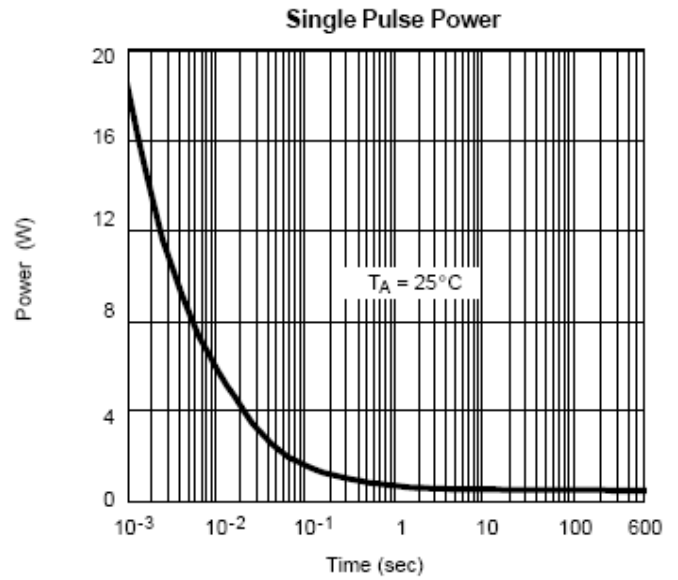
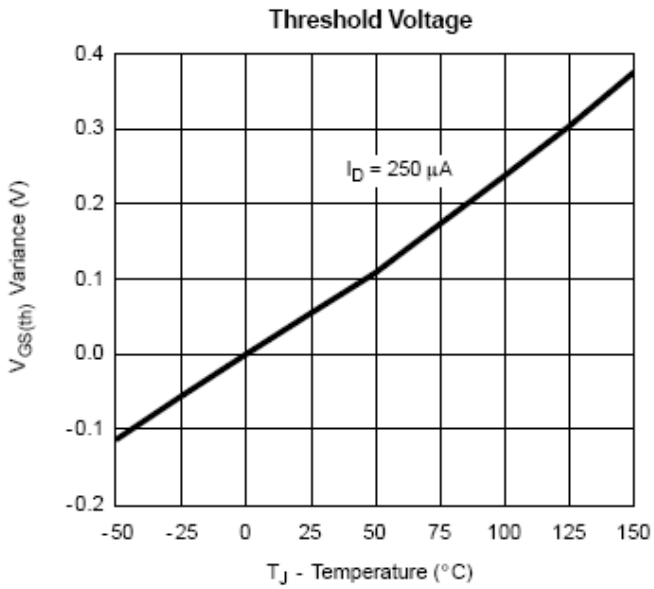
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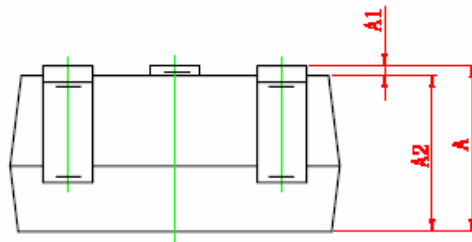
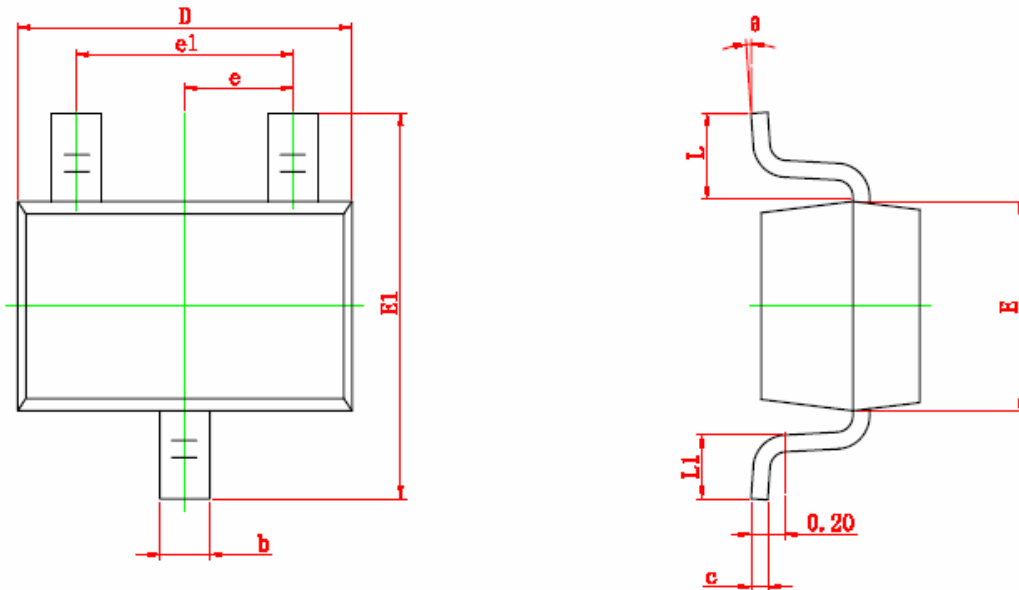




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### SOT-323 PACKAGE OUTLINE



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.200	0.400	0.008	0.016
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
$\theta$	0°	8°	0°	8°



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