## Control IC for Switched Mode Power Supplies using MOS Transistors

The IC KKA4605-2 controls the MOS - power transistor and performs all necessary regulation and monitoring function in free running flyback converters. Because of the fact that a wide load range is achieved, this IC is applicable for consumer as well as industrial power supplies.
The serial circuit and primary winding of the flyback transformer are connected in series to the input voltage. During the switch - on period of the transistor, energy is stored in the transformer. During the switch - off period the energy is fed to the load via the secondary winding. By varying switch - on time of the Power transistor, the IC controls each portion of energy transferred to the secondary side such that the output voltage remains nearly independent of load variations. The required control information is taken from the input voltage during the switch - on period and from a regulation winding during the switch - off period. A new cycle will start if the transformer has transferred the stored energy completely into the load.

## FEATURES:

- Fold - back characteristics provides overload protection for external components
- Burst operation under secondary short - circuit condition implemented
- Protection against open or short of the control loop
- Switch - off if line voltage is too low (undervoltage switch - off)
- Line voltage depending compensation of fold - back point
- Soft start for quiet start - up without noise generated by the transformer

- Chip - over temperature protection (thermal shutdown)
- On - chip ringing suppression circuit against parasitic oscillations of the transformer

In the different voltage ranges the switched - mode Power supply (SMPS) behaves as follow:
No load operation. The Power supply is operating in the burst mode at typical 20 to 40 kHz . The output voltage can be a little bit higher or lower than the nominal value depending on the design of the transformer and the resistors of the control voltage divider.
Nominal operation. The switching frequency is reduced with increasing load and decreasing AC voltage. The duty factor primarily depends on the AC voltage. The output voltage is only depends on the load.
Overload point. Maximum output Power is available at this point of the output characteristic.
Overload. The energy transferred per operation cycle is limited at the top. Therefore the output voltages declines by secondary overloading.

PIN DEFINITION

| Pin \# | Function |
| :---: | :--- |
| 1. | Information Input Concerning Secondary Voltage |
| 2. | Information Input Regarding the Primary Current |
| 3. | Input for Primary Voltage Monitoring |
| 4. | Ground |
| 5. | Output |
| 6. | Supply Voltage Input |
| 7. | Input for Soft Start and Integrator Circuit |
| 8. | Input for the Feedback of the Oscillator |

## Block Diagram



## ABSOLUTE MAXIMUM RATINGS

$\mathrm{Ta}=-20$ to $85^{\circ} \mathrm{C}$; all voltages relatives to $\mathrm{V}_{\mathrm{pp}}$

| Parameter | Symbol | Limit Values |  | Unit | Notes |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | min | max |  |  |
| Voltages $\begin{array}{ll}\text { pin 1 } \\ & \text { pin 2 } \\ & \text { pin 3 } \\ & \text { pin 4 } \\ & \text { pin 5 } \\ & \text { pin 6 } \\ & \text { pin 7 }\end{array}$ | $\mathrm{V}_{1}$ | -0.3 | 3 | V | Supply voltage |
|  | $V_{2}$ | -0.3 |  | V |  |
|  | $V_{3}$ | -0.3 |  | V |  |
|  | $V_{4}$ | -0.3 |  | V |  |
|  | $V_{5}$ | -0.3 | $\mathrm{V}_{6}$ | V |  |
|  | $\mathrm{V}_{6}$ | -0.3 | 20 | V |  |
|  | $\mathrm{V}_{7}$ | -0.3 |  | V |  |
| Current pin 1 | $\mathrm{I}_{1}$ |  | 3 | mA |  |
| pin 2 | $\mathrm{I}_{2}$ |  | 3 | mA |  |
| pin 3 | $\mathrm{I}_{3}$ |  | 3 | mA |  |
| pin 4 | $\mathrm{I}_{4}$ | -1.5 |  | A | $\mathrm{t}_{\mathrm{p}} \leq 50 \mu \mathrm{~s} ; \mathrm{v} \leq 0.1$ * |
| pin 5 | $\mathrm{I}_{5}$ | -0.5 | 1.5 | A | $\mathrm{t}_{\mathrm{p}} \leq 50 \mu \mathrm{~s} ; \mathrm{v} \leq 0.1$ |
| pin 6 | $I_{6}$ |  | 0.5 | A | $\mathrm{t}_{\mathrm{p}} \leq 50 \mu \mathrm{~s} ; \mathrm{v} \leq 0.1$ |
| pin 7 | $\mathrm{I}_{7}$ |  | 3 | mA |  |
| pin 8 | $\mathrm{I}_{8}$ | -5 | 3 | mA |  |
| Junction temperature | Tj |  | 125 | ${ }^{\circ} \mathrm{C}$ |  |
| Storage temperature | $\mathrm{T}_{\text {stg }}$ | -40 | 125 | ${ }^{\circ} \mathrm{C}$ |  |

## Operating Range

| Supply voltage | $\mathrm{V}_{6}$ | 7.5 | 15.5 | V | IC is on |
| :--- | :---: | :---: | :---: | :---: | :--- |
| Ambient temperature | Ta | -20 | 85 | ${ }^{\circ} \mathrm{C}$ |  |

[^0]
## CHARACTERISTICS

$\mathrm{Ta}=25^{\circ} \mathrm{C} ; \mathrm{V}_{\mathrm{S}}=10 \mathrm{~V}$

| Parameter | Symbol | Limit Values |  | Units | Test Condition |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | min. | max. |  |  |
| Start - Up Hysteresis |  |  |  |  |  |
| Start-up current drain | $\mathrm{I}_{6 \mathrm{EO}}$ |  | 1.6 | mA | $\mathrm{V}_{6}=\mathrm{V}_{6 \mathrm{E}}$ |
| Switch-on voltage | $\mathrm{V}_{6 \mathrm{E}}$ | 11 | 13 | V |  |
| Switch-on current | $\mathrm{I}_{6 \mathrm{E} 1}$ |  | 16 | mA | $\mathrm{V}_{6}=\mathrm{V}_{6 \mathrm{E}}$ |
| Voltage Clamp ( $\mathbf{V}_{6}=10 \mathrm{~V}$; IC is off) |  |  |  |  |  |
| At pins 2,3 ( $\left.\mathrm{V}_{6} \leq \mathrm{V}_{6 \mathrm{E}}\right)$ | $\mathrm{V}_{\text {max }}$ | 5.6 | 9.0 | V | $\mathrm{I}_{2,3}=1 \mathrm{~mA}$ |
| Control Range |  |  |  |  |  |
| Control input voltage | $\mathrm{V}_{1 \mathrm{R}}$ | 370 | 430 | mV |  |
| Voltage gain of the control circuit in the control value | - $\mathrm{V}_{\mathrm{R}}$ | 150 |  |  |  |
| Push-Pull Output Stage |  |  |  |  |  |
| Saturation voltages Pin 5 sourcing Pin5 sinking | $\begin{aligned} & V_{\text {sat0 }} \\ & V_{\text {satV }} \\ & \hline \end{aligned}$ |  | $\begin{aligned} & 2.2 \\ & 1.4 \\ & \hline \end{aligned}$ | $\begin{aligned} & \mathrm{V} \\ & \mathrm{~V} \\ & \hline \end{aligned}$ | $\begin{aligned} & I_{5}=-0.1 \mathrm{~A} \\ & \mathrm{I}_{5}=+0.1 \mathrm{~A} \end{aligned}$ |
| Protection Circuit |  |  |  |  |  |
| Undervoltage protection for $\mathrm{V}_{6}$ : voltage at pin $5=\mathrm{V}_{5 \text { min }}$ if $V_{6}<V_{6 \text { min }}$ | $V_{6 \text { min }}$ | 6 | 8 | V |  |
| Undervoltage protection for $\mathrm{V}_{6}$ : voltage at pin $5=\mathrm{V}_{5 \text { min }}$ if $V_{6}>V_{6 \text { max }}$ | $V_{6 \text { max }}$ |  | 16.7 | V |  |

## APPLICATION DIAGRAM



## N SUFFIX PLASTIC DIP <br> (MS - 001BA)



| $\Phi$ | $0.25(0.010)(1 /)$ | T |
| :--- | :--- | :--- |

## NOTES:

1. Dimensions "A", "B" do not include mold flash or protrusions. Maximum mold flash or protrusions $0.25 \mathrm{~mm}(0.010)$ per side.


|  | Dimension, mm |  |
| :---: | :---: | :---: |
| Symbol | MIN | MAX |
| $\mathbf{A}$ | 8.51 | 10.16 |
| $\mathbf{B}$ | 6.1 | 7.11 |
| $\mathbf{C}$ |  | 5.33 |
| $\mathbf{D}$ | 0.36 | 0.56 |
| $\mathbf{F}$ | 1.14 | 1.78 |
| $\mathbf{G}$ | 2.54 |  |
| $\mathbf{H}$ | 7.62 |  |
| $\mathbf{J}$ | $0^{\circ}$ | $10^{\circ}$ |
| $\mathbf{K}$ | 2.92 | 3.81 |
| $\mathbf{L}$ | 7.62 | 8.26 |
| $\mathbf{M}$ | 0.2 | 0.36 |
| $\mathbf{N}$ | 0.38 |  |


[^0]:    ${ }^{*} t_{p}=$ pulse width
    $v=$ duty circle

