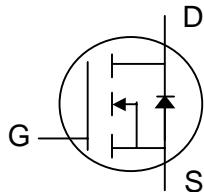


## N-channel Enhancement-mode Power MOSFET

Low gate-charge  
 Simple drive requirement  
 Fast switching

 Pb-free, RoHS compliant.

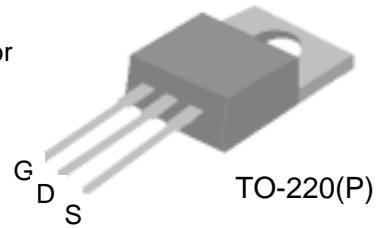
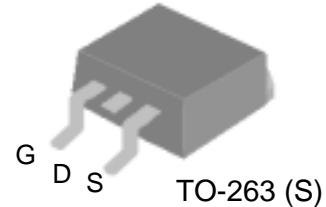


$BV_{DSS}$  30V  
 $R_{DS(ON)}$  25mΩ  
 $I_D$  28A

### DESCRIPTION

The SSM40T03GS is in a TO-263 package, which is widely used for commercial and industrial surface-mount applications. This device is suitable for low-voltage applications such as DC/DC converters.

The through-hole version, the SSM40T03GP in TO-220, is available for vertical-mounting, where a small footprint is required on the board, and/or an external heatsink is to be attached.



### ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 25$	V
$I_D @ T_C=25^\circ\text{C}$	Continuous Drain Current	28	A
$I_D @ T_C=100^\circ\text{C}$	Continuous Drain Current	24	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	95	A
$P_D @ T_C=25^\circ\text{C}$	Total Power Dissipation	31	W
	Linear Derating Factor	0.25	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### THERMAL DATA

Symbol	Parameter	Value	Units
$R_{\Theta JC}$	Maximum Thermal Resistance Junction-case	4	°C/W
$R_{\Theta JA}$	Maximum Thermal Resistance Junction-ambient	62	°C/W

**ELECTRICAL CHARACTERISTICS (at  $T_j=25^\circ\text{C}$ , unless otherwise specified)**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_{\text{D}}=250\mu\text{A}$	30	-	-	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_{\text{D}}=1\text{mA}$	-	0.032	-	$\text{V}/^\circ\text{C}$
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_{\text{D}}=18\text{A}$	-	-	25	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_{\text{D}}=14\text{A}$	-	-	45	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$ , $I_{\text{D}}=250\mu\text{A}$	1	-	3	V
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_{\text{D}}=18\text{A}$	-	15	-	S
$I_{\text{DSS}}$	Drain-Source Leakage Current ( $T_j=25^\circ\text{C}$ )	$V_{\text{DS}}=30\text{V}$ , $V_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
	Drain-Source Leakage Current ( $T_j=150^\circ\text{C}$ )	$V_{\text{DS}}=24\text{V}$ , $V_{\text{GS}}=0\text{V}$	-	-	25	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage	$V_{\text{GS}}= \pm 25\text{V}$	-	-	$\pm 100$	$\text{nA}$
$Q_g$	Total Gate Charge <sup>2</sup>	$I_{\text{D}}=18\text{A}$	-	8.8	-	nC
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{DS}}=20\text{V}$	-	2.5	-	nC
$Q_{\text{gd}}$	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=4.5\text{V}$	-	5.8	-	nC
$t_{\text{d}(\text{on})}$	Turn-on Delay Time <sup>2</sup>	$V_{\text{DS}}=15\text{V}$	-	6	-	ns
$t_r$	Rise Time	$I_{\text{D}}=18\text{A}$	-	62	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time	$R_G=3.3\Omega$ , $V_{\text{GS}}=10\text{V}$	-	16	-	ns
$t_f$	Fall Time	$R_D=0.83\Omega$	-	4.4	-	ns
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	655	-	pF
$C_{\text{oss}}$	Output Capacitance	$V_{\text{DS}}=25\text{V}$	-	145	-	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance	f=1.0MHz	-	95	-	pF

**Source-Drain Diode**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$I_S$	Continuous Source Current ( Body Diode )	$V_D=V_G=0\text{V}$ , $V_S=1.3\text{V}$	-	-	28	A
$I_{\text{SM}}$	Pulsed Source Current ( Body Diode ) <sup>1</sup>		-	-	95	A
$V_{\text{SD}}$	Forward On Voltage <sup>2</sup>	$T_j=25^\circ\text{C}$ , $I_S=28\text{A}$ , $V_{\text{GS}}=0\text{V}$	-	-	1.3	V

**Notes:**

- 1.Pulse width limited by safe operating area.
- 2.Pulse width  $\leq 300\text{us}$ , duty cycle  $\leq 2\%$ .

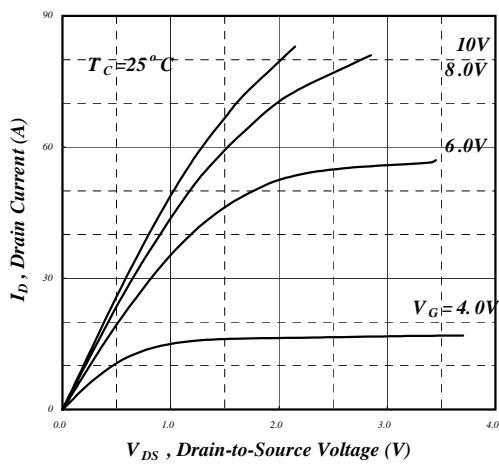


Fig 1. Typical Output Characteristics

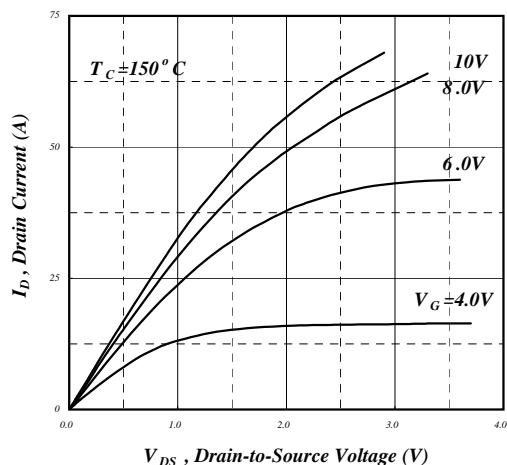


Fig 2. Typical Output Characteristics

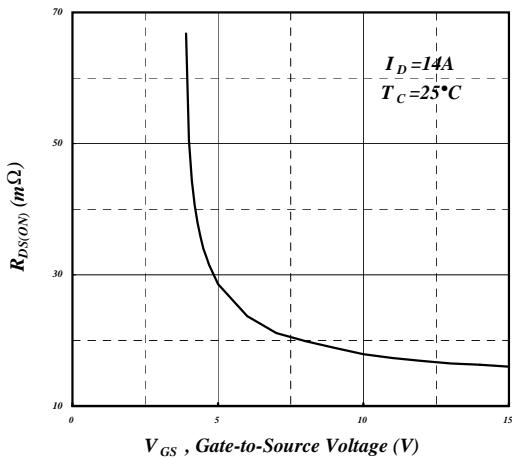


Fig 3. On-Resistance vs. Gate Voltage

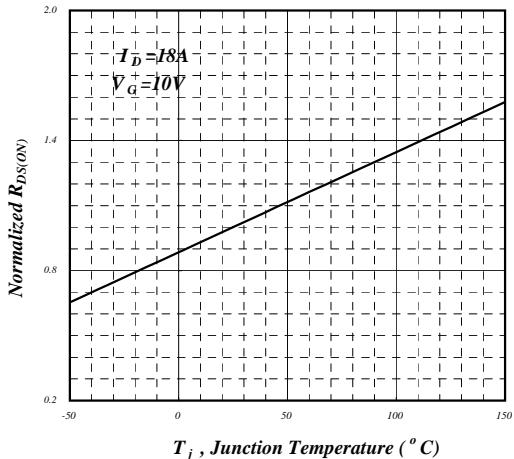


Fig 4. Normalized On-Resistance  
vs. Junction Temperature

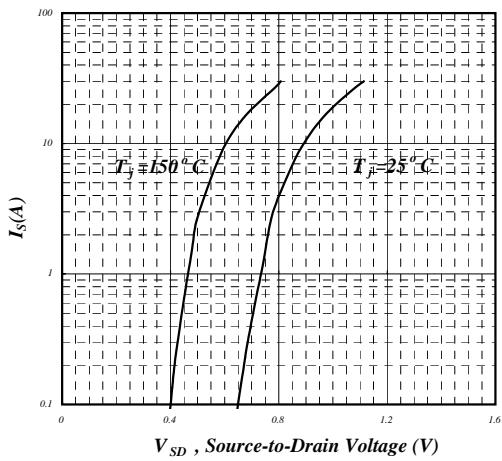


Fig 5. Forward Characteristic of  
Reverse Diode

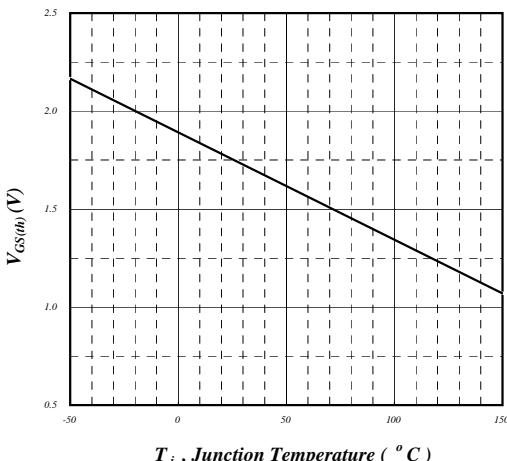


Fig 6. Gate Threshold Voltage vs.  
Junction Temperature

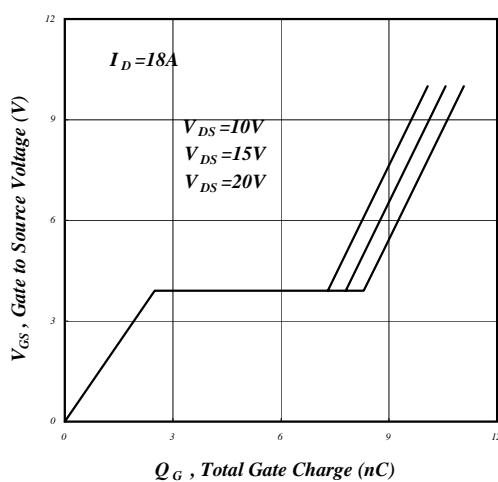


Fig 7. Gate Charge Characteristics

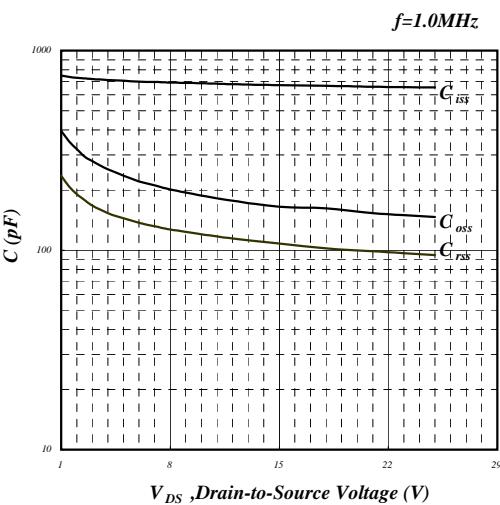


Fig 8. Typical Capacitance Characteristics

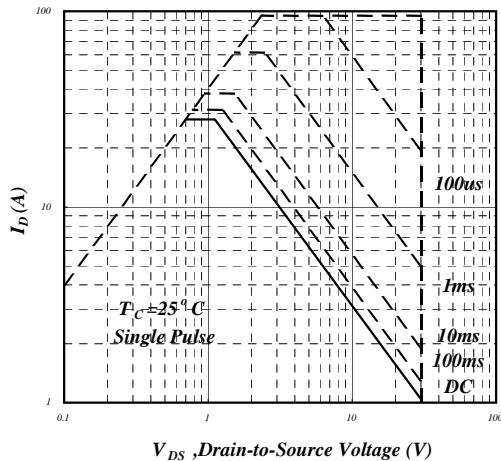


Fig 9. Maximum Safe Operating Area

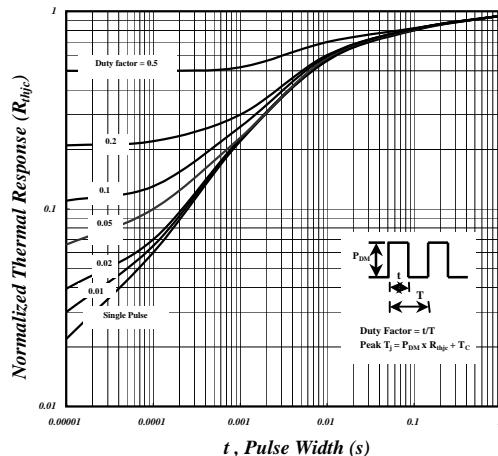


Fig 10. Effective Transient Thermal Impedance

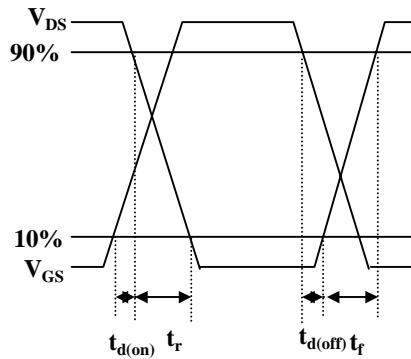


Fig 11. Switching Time Waveform

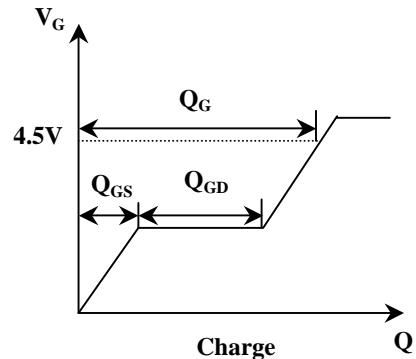


Fig 12. Gate Charge Waveform

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