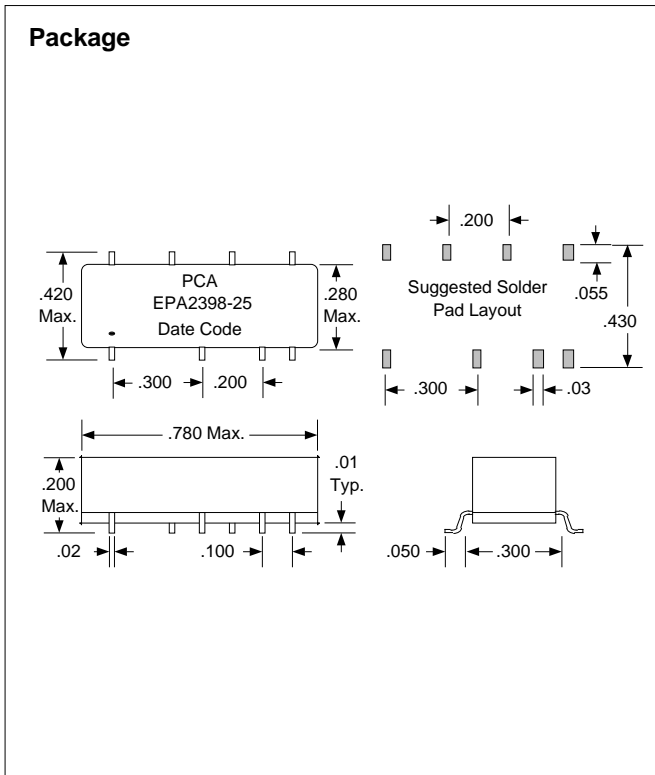
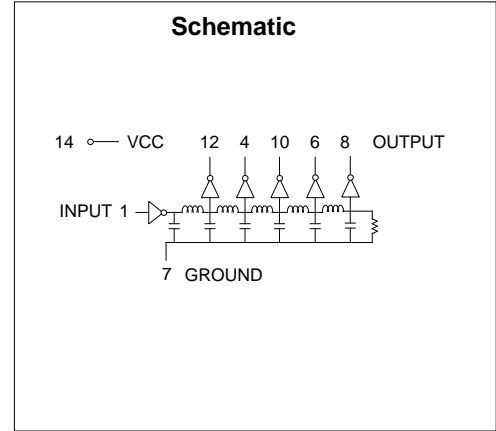


# SMD 14 Pin 5 Tap TTL Compatible Active Delay Lines

Delays are $\pm 5\%$ or $\pm 2$ nS Tap		SMD Part Number	Delays are $\pm 5\%$ or $\pm 2$ nS Total		SMD Part Number
Tap	Total		Tap	Total	
5, 10, 15, 20	25	EPA2398-25	80, 160, 240, 320	400	EPA2398-400
6, 12, 18, 24	30	EPA2398-30	84, 168, 252, 336	420	EPA2398-420
7, 14, 21, 28	35	EPA2398-35	88, 176, 264, 352	440	EPA2398-440
8, 16, 24, 32	40	EPA2398-40	90, 180, 270, 360	450	EPA2398-450
9, 18, 27, 36	45	EPA2398-45	94, 188, 282, 376	470	EPA2398-470
10, 20, 30, 40	50	EPA2398-50	100, 200, 300, 400	500	EPA2398-500
12, 24, 36, 48	60	EPA2398-60	110, 220, 330, 440	550	EPA2398-550
15, 30, 45, 60	75	EPA2398-75	120, 240, 360, 480	600	EPA2398-600
20, 40, 60, 80	100	EPA2398-100	130, 260, 390, 520	650	EPA2398-650
25, 50, 75, 100	125	EPA2398-125	140, 280, 420, 560	700	EPA2398-700
30, 60, 90, 120	150	EPA2398-150	150, 300, 450, 600	750	EPA2398-750
35, 70, 105, 140	175	EPA2398-175	160, 320, 480, 640	800	EPA2398-800
40, 80, 120, 160	200	EPA2398-200	170, 340, 510, 680	850	EPA2398-850
45, 90, 135, 180	225	EPA2398-225	180, 360, 540, 720	900	EPA2398-900
50, 100, 150, 200	250	EPA2398-250	190, 380, 570, 760	950	EPA2398-950
60, 120, 180, 240	300	EPA2398-300	200, 400, 600, 800	1000	EPA2398-1000
70, 140, 210, 280	350	EPA2398-350			

Delay times referenced from input to leading edges at 25°C, 5.0V, with no load.

DC Electrical Characteristics			Min	Max	Unit
Parameter	Test Conditions				
V <sub>OH</sub>	High-Level Output Voltage	V <sub>CC</sub> = min. V <sub>IL</sub> = max. I <sub>OH</sub> = max	2.7		V
V <sub>OL</sub>	Low-Level Output Voltage	V <sub>CC</sub> = min. V <sub>IH</sub> = min. I <sub>OL</sub> = max		0.5	V
V <sub>IK</sub>	Input Clamp Voltage	V <sub>CC</sub> = min. I <sub>I</sub> = I <sub>IK</sub>		-1.2	V
I <sub>IH</sub>	High-Level Input Current	V <sub>CC</sub> = max. V <sub>IN</sub> = 2.7V		50	μA
		V <sub>CC</sub> = max. V <sub>IN</sub> = 5.25V		1.0	mA
I <sub>IL</sub>	Low-Level Input Current	V <sub>CC</sub> = max. V <sub>IN</sub> = 0.5V		-2	mA
I <sub>OS</sub>	Short Circuit Output Current	V <sub>CC</sub> = max. V <sub>OUT</sub> = 0.	-40	-100	mA
		(One output at a time)			
I <sub>CCH</sub>	High-Level Supply Current	V <sub>CC</sub> = max. V <sub>IN</sub> = OPEN		75	mA
I <sub>CCL</sub>	Low-Level Supply Current	V <sub>CC</sub> = max. V <sub>IN</sub> = 0		75	mA
T <sub>RO</sub>	Output Rise Time	T <sub>d</sub> 500 nS (0.75 to 2.4 Volts)		4	nS
		T <sub>d</sub> > 500 nS		5	nS
N <sub>H</sub>	Fanout High-Level Output	V <sub>CC</sub> = max. V <sub>OH</sub> = 2.7V		20 TTL LOAD	
N <sub>L</sub>	Fanout Low-Level Output	V <sub>CC</sub> = max. V <sub>OL</sub> = 0.5V		10 TTL LOAD	



Recommended Operating Conditions		Min	Max	Unit
V <sub>CC</sub>	Supply Voltage	4.75	5.25	V
V <sub>IH</sub>	High-Level Input Voltage	2.0		V
V <sub>IL</sub>	Low-Level Input Voltage		0.8	V
I <sub>IK</sub>	Input Clamp Current		-18	mA
I <sub>OH</sub>	High-Level Output Current		-1.0	mA
I <sub>OL</sub>	Low-Level Output Current		20	mA
PW*	Pulse Width of Total Delay	40		%
d*	Duty Cycle		40	%
T <sub>A</sub>	Operating Free-Air Temperature	0	+70	°C

\*These two values are inter-dependent.

Input Pulse Test Conditions @ 25° C			Unit
E <sub>IN</sub>	Pulse Input Voltage	3.2	Volts
PW	Pulse Width % of Total Delay	50	%
T <sub>RI</sub>	Pulse Rise Time (0.75 - 2.4 Volts)	2.0	nS
PRR	Pulse Repetition Rate @ T <sub>d</sub> 200 nS	5.0	MHz
	Pulse Repetition Rate @ T <sub>d</sub> > 200 nS	100	KHz
V <sub>CC</sub>	Supply Voltage	5.0	Volts



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