

Single P-channel MOSFET

ELM14427AA-N

General description

ELM14427AA-N uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance. Internal ESD protection is included.

Features

- $V_{ds} = -30V$
- $I_d = -12.5A$ ($V_{gs} = -20V$)
- $R_{ds(on)} < 12m\Omega$ ($V_{gs} = -20V$)
- $R_{ds(on)} < 14m\Omega$ ($V_{gs} = -10V$)
- ESD Rating : 2000V HBM

Maximum absolute ratings

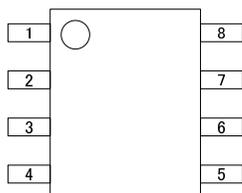
Parameter	Symbol	Limit	Unit	Note	
Drain-source voltage	V_{ds}	-30	V		
Gate-source voltage	V_{gs}	± 25	V		
Continuous drain current	I_d	$T_a = 25^\circ C$	-12.5	A	1
		$T_a = 70^\circ C$	-10.5		
Pulsed drain current	I_{dm}	-60	A	2	
Power dissipation	P_d	$T_a = 25^\circ C$	3.0	W	1
		$T_a = 70^\circ C$	2.1		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	$^\circ C$		

Thermal characteristics

Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$t \leq 10s$	$R\theta_{ja}$	28	40	$^\circ C/W$	1
Maximum junction-to-ambient	Steady-state		54	75	$^\circ C/W$	
Maximum junction-to-lead	Steady-state	$R\theta_{jl}$	21	30	$^\circ C/W$	3

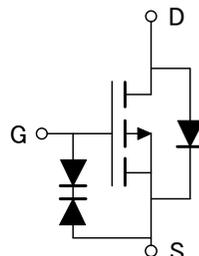
Pin configuration

SOP-8 (TOP VIEW)



Pin No.	Pin name
1	SOURCE
2	SOURCE
3	SOURCE
4	GATE
5	DRAIN
6	DRAIN
7	DRAIN
8	DRAIN

Circuit



Single P-channel MOSFET

ELM14427AA-N

Electrical characteristics

Ta=25°C

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	BVdss	Id=-250 μA, Vgs=0V	-30			V
Zero gate voltage drain current	Idss	Vds=-24V			-1	μA
		Vgs=0V		Tj=55°C	-5	
Gate-body leakage current	Igss	Vds=0V, Vgs=±25V			±1	μA
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=-250 μA	-1.7	-2.5	-3.0	V
On state drain current	Id(on)	Vgs=-10V, Vds=-5V	-60			A
Static drain-source on-resistance	Rds(on)	Vgs=-20V		9.4	12.0	mΩ
		Id=-12.5A		12.2	15.0	
			Tj=125°C			
		Vgs=-10V, Id=-10A		11.5	14.0	mΩ
		Vgs=-4.5V, Id=-5A		32.0		mΩ
Forward transconductance	Gfs	Vds=-5V, Id=-12.5A		24		S
Diode forward voltage	Vsd	Is=-1A, Vgs=0V			-1	V
Max. body-diode continuous current	Is				-4.2	A
DYNAMIC PARAMETERS						
Input capacitance	Ciss			2330	2900	pF
Output capacitance	Coss	Vgs=0V, Vds=-15V, f=1MHz		480		pF
Reverse transfer capacitance	Crss			320		pF
Gate resistance	Rg	Vgs=0V, Vds=0V, f=1MHz		6.8	10.0	Ω
SWITCHING PARAMETERS						
Total gate charge	Qg	Vgs=-10V, Vds=-15V Id=-12.5A		41	52	nC
Gate-source charge	Qgs			10		nC
Gate-drain charge	Qgd			12		nC
Turn-on delay time	td(on)			12.8		ns
Turn-on rise time	tr	Vgs=-10V, Vds=-15V		10.3		ns
Turn-off delay time	td(off)	RI=1.2 Ω, Rgen=3 Ω		49.5		ns
Turn-off fall time	tf			29.0		ns
Body diode reverse recovery time	trr	If=-12.5A, di/dt=100A/μs		28	35	ns
Body diode reverse recovery charge	Qrr	If=-12.5A, di/dt=100A/μs		20		nC

NOTE :

1. The value of Rθja is measured with the device mounted on 1in² FR-4 board of 2oz. Copper, in still air environment with Ta=25°C. The value in any given applications depends on the user's specific board design, The current rating is based on the t ≤ 10s thermal resistance rating.
2. Repetitive rating, pulse width limited by junction temperature.
3. The Rθja is the sum of the thermal impedance from junction to lead Rθjl and lead to ambient.
4. The static characteristics in Figures 1 to 6 are obtained using 80μs pulses, duty cycle 0.5%max.
5. These tests are performed with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with Ta=25°C. The SOA curve provides a single pulse rating.

Single P-channel MOSFET

ELM14427AA-N

Typical electrical and thermal characteristics

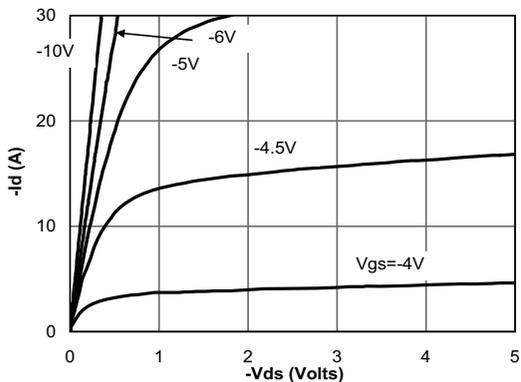


Fig 1: On-Region Characteristics

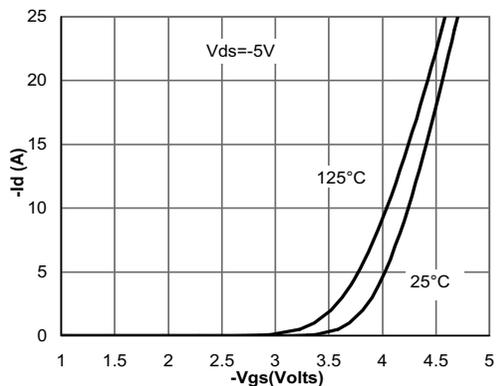


Figure 2: Transfer Characteristics

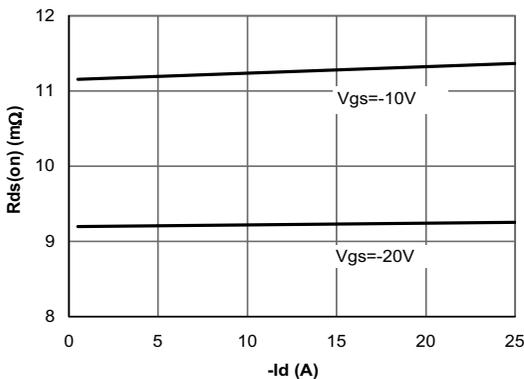


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

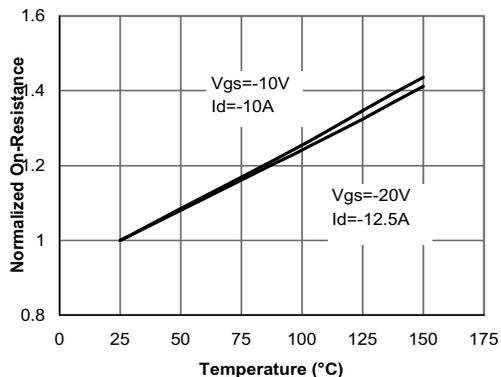


Figure 4: On-Resistance vs. Junction Temperature

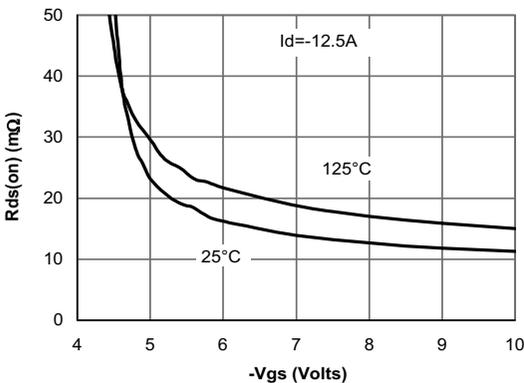


Figure 5: On-Resistance vs. Gate-Source Voltage

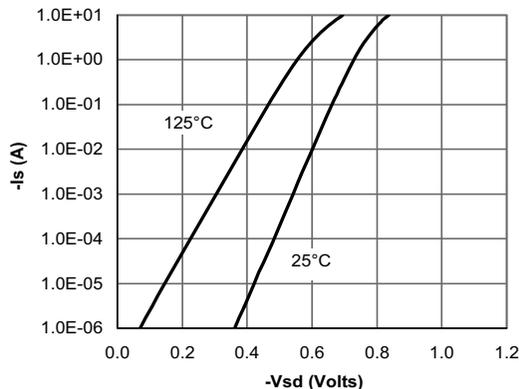


Figure 6: Body-Diode Characteristics

Single P-channel MOSFET

ELM14427AA-N

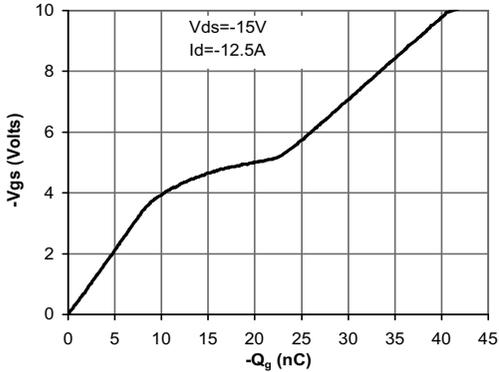


Figure 7: Gate-Charge Characteristics

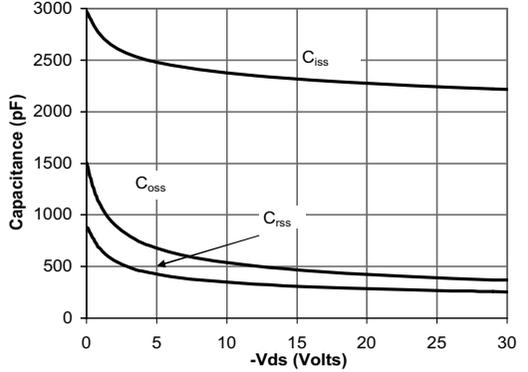


Figure 8: Capacitance Characteristics

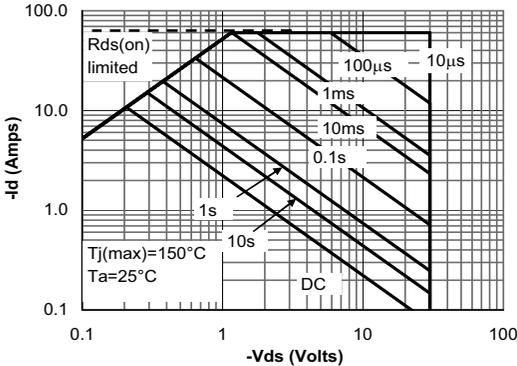


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

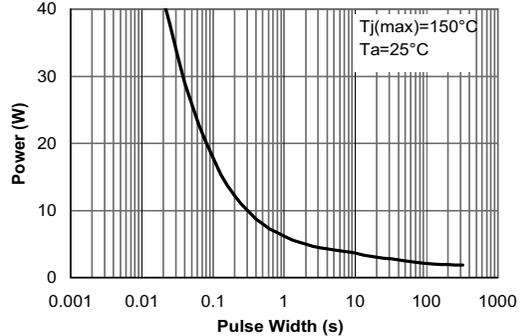


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

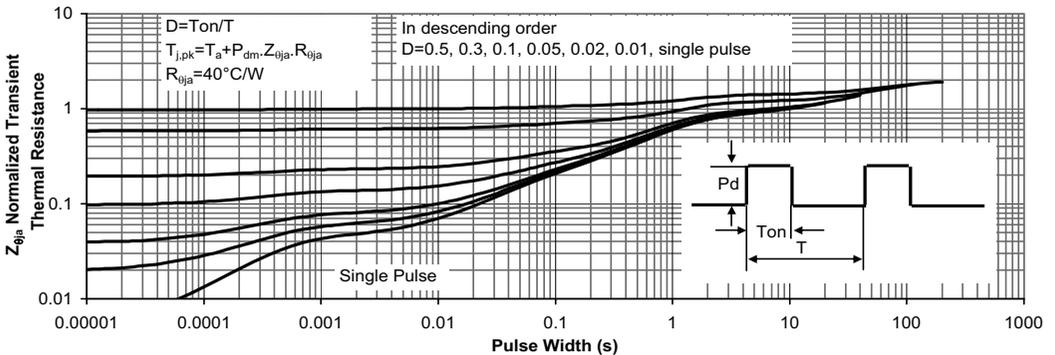


Figure 11: Normalized Maximum Transient Thermal Impedance