



CHENMKO ENTERPRISE CO.,LTD

CHIMD14PT

**SURFACE MOUNT
Dual Digital Transistor**

VOLTAGE 50 Volts CURRENT 500 mAmpere

Lead free devices

APPLICATION

* Switching circuit, Inverter, Interface circuit, Driver circuit.

FEATURE

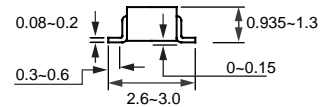
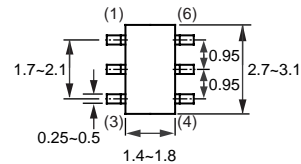
- * Small surface mounting type. (SC-74/SOT-457)
- * High current gain.
- * Suitable for high packing density.
- * Low collector-emitter saturation.
- * High saturation current capability.
- * Two 500mA digital transistor chips in one package.
- * Built in bias resistor(R1=220Ω, Typ.)

MARKING

*ID14



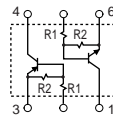
SC-74/SOT-457



Dimensions in millimeters

SC-74/SOT-457

CIRCUIT



PNP DIGITAL TRANSISTOR LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CC}	Supply voltage		-	-50	V
V _{IN}	Input voltage		-5	+5	V
I _O	DC Output current		-	-500	mA
I _{C(Max.)}			-	-500	
P _{TOT}	Total power dissipation	T _{amb} ≤ 25 °C, Note 1	-	150	mW
T _{STG}	Storage temperature		-55	+150	°C

Note

Transistor mounted on an FR4 printed-circuit board.

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Note

Transistor mounted on an FR4 printed-circuit board.

PNP DIGITAL TRANSISTOR CHARACTERISTICST_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{I(off)}	Input off voltage	I _O =-100mA; V _{CC} =-5.0V	-0.3	–	–	V
V _{I(on)}	Input on voltage	I _O =-50mA; V _O =-0.3V	–	–	-1.1	V
V _{O(on)}	Output voltage	I _O =-100mA; I _I =-5mA	–	–	-0.3	V
I _I	Input current	V _I =-3.0V	–	–	-17	mA
I _{C(off)}	Output current	V _I =0V; V _{CC} =-50V	–	–	-0.5	uA
h _{FE}	DC current gain	I _O =-5.0mA; V _O =-100V	82	–	–	
R ₁	Input resistor		154	220	286	Ω
R _{2/R₁}	Resistor ratio		36.3	45.5	54.6	

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V _{I(off)}	Input off voltage	I _O =100mA; V _{CC} =5.0V	0.3	–	–	V
V _{I(on)}	Input on voltage	I _O =50mA; V _O =0.3V	–	–	1.1	V
V _{O(on)}	Output voltage	I _O =100mA; I _I =5mA	–	–	0.3	V
I _I	Input current	V _I =3.0V	–	–	17	mA
I _{C(off)}	Output current	V _I =0V; V _{CC} =50V	–	–	0.5	mA
h _{FE}	DC current gain	I _O =5mA; V _O =100V	82	–	–	
R ₁	Input resistor		154	220	286	Ω
R _{2/R₁}	Resistor ratio		36.3	45.5	54.6	