

GW8 CMOS VCXO

8 pin Dual-in-Line

- Frequency range 50.01MHz to 800MHz
- LVCMOS Output
- Supply Voltage 3.3 VDC
- High Q fundamental mode crystal
- Low jitter multiplier circuit

DESCRIPTION

GW8 VCXOs, are packaged in an industry-standard, 8 pin Dual-in-Line package. GW8 VCXOs incorporate a high Q fundamental crystal and a low jitter multiplier circuit.

SPECIFICATION

Frequency Range:	50.01MHz to 800.0MHz	
Supply Voltage:	3.3 VDC ±5%	
Output Logic:	LVCMOS	
Integrated Phase Jitter:	2.6ps typical, 4.0ps maximum (for 155.250MHz)	
Period Jitter RMS:	4.3ps typical (for 155.250MHz)	
Period Jitter Peak to peak:	27.0ps typical (for 155.250MHz)	
Phase Noise:	See table below	
Initial Frequency Accuracy:	Tune to the nominal frequency with Vc= 1.65 ±0.2VDC	
Output Voltage HIGH (1):	90% Vdd minimum	
Output Voltage LOW (0):	10% Vdd maximum	
Pulling Range:	From ±30ppm to ±150ppm	
Temperature Stability:	See table	
Output Load:	15pF	
Start-up Time:	10ms maximum, 5ms typical	
Duty Cycle:	50% ±5% measured at 50% Vdd	
Rise/Fall Times:	1.2ns typical (15pF load)	
Current Consumption		
<96MHz:	30mA maximum (15pF load)	
>96MHz:	40mA maximum (15pF load)	
Linearity:	10% maximum, 6% typical	
Modulation Bandwidth:	25kHz minimum	
Input Impedance:	2 M Ω minimum	
Slope Polarity:	Monotonic and Positive. (An	
(Transfer function)	increase of control voltage	
	always increases output	
	frequency.)	
Storage Temperature:	-50° to +100°C	
Ageing:	±5ppm per year maximum	
Enable/Disable (Tristate):	isable (Tristate): Not available (4 pad package)	
RoHS Status:	Fully compliant	







PHASE NOISE

Offset	Frequency 155.25MHz	
10Hz	-65dBc/Hz	
100Hz	-95dBc/Hz	
1kHz	-120dBc/Hz	
10kHz	-125dBc/Hz	
100kHz	-121dBc/Hz	
1MHz	-120dBc/Hz	
10MHz	-140dBc/Hz	

FREQUENCY STABILITY

Stability Code	Stability ±ppm	Temp. Range
А	25	0°~+70°C
В	50	0°~+70°C
С	100	0°~+70°C
D	25	-40°~+85°C
E	50	-40°~+85°C
F	100	-40°~+85°C
If non-standard frequency stability is required		

Use 'l' followed by stability, i.e. 120 for ±20ppm

