

Low Power Peak EMI Reducing Solution

Features

- Generates an EMI optimized clock signal at the output.
- Integrated loop filter components.
- Operates with a 3.3V / 2.5V supply.
- Operating current less than 4mA.
- Low power CMOS design.
- Input frequency range: 6MHz to 12MHz for 2.5V
 : 6MHz to 13MHz for 3.3V
- Generates a 1X low EMI spread spectrum clock of the input frequency.
- Frequency deviation: ±1% @ 10MHz
- Available in 6 pin TSOT-23, 8 pin SOIC and 8 pin TSSOP Packages.

Product Description

The ASM3P2969A is a versatile spread spectrum frequency modulator designed specifically for a wide range of clock frequencies. The ASM3P2969A reduces electromagnetic interference (EMI) at the clock source, allowing system wide reduction of EMI of all clock dependent signals. The ASM3P2969A allows significant system cost savings by reducing the number of circuit board layers ferrite beads, shielding that are traditionally required to pass EMI regulations.

The ASM3P2969A uses the most efficient and optimized modulation profile approved by the FCC and is implemented by using a proprietary all digital method.

The ASM3P2969A modulates the output of a single PLL in order to "spread" the bandwidth of a synthesized clock, and more importantly, decreases the peak amplitudes of its harmonics. This results in significantly lower system EMI compared to the typical narrow band signal produced by oscillators and most frequency generators. Lowering EMI by increasing a signal's bandwidth is called 'spread spectrum clock generation'.

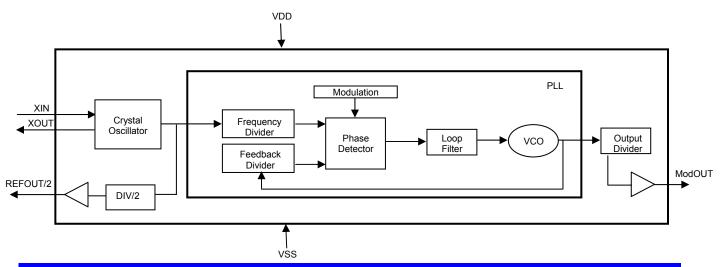
Applications

The ASM3P2969A is targeted towards all portable devices with very low power requirements like MP3 players, Notebooks and digital still cameras.

Key Specifications

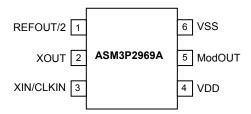
Description	Specification
Supply voltages	VDD = 3.3V /2.5V
Cycle-to-Cycle Jitter	200pS (Max)
Output Duty Cycle	45/55%
Modulation Rate Equation	F _{IN} /256
Frequency Deviation	±1% @ 10MHz

Block Diagram





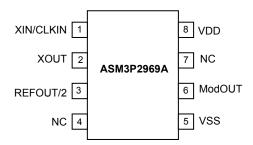
Pin Configuration (6-pin TSOT- 23 Package)



Pin Description

Pin#	Pin Name	Туре	Description		
1	REFOUT/2	0	Buffered and divided by 2 output of the input frequency.		
2	XOUT	0	Crystal connection. If using an external reference, this pin must be left unconnected.		
3	XIN/CLKIN	I	Crystal connection or external reference frequency input. This pin has dual functions. It can be connected either to an external crystal or an external reference clock.		
4	VDD	Р	Power supply for the entire chip		
5	ModOUT	0	Spread spectrum clock output.		
6	VSS	Р	Ground connection.		

Pin Configuration (8-pin SOIC and TSSOP Packages)

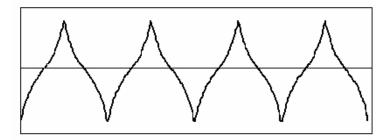


Pin Description

Pin#	Pin Name	Туре	Description			
1	XIN/CLKIN	I	Crystal connection or external reference frequency input. This pin has dual functions. It can be connected either to an external crystal or an external reference clock.			
2	XOUT	0	Crystal connection. If using an external reference, this pin must be left unconnected.			
3	REFOUT/2	0	Buffered and divided by 2 output of the input frequency.			
4	NC	1	No connect.			
5	VSS	Р	Ground connection.			
6	ModOUT	0	Spread spectrum clock output.			
7	NC	-	No connect.			
8	VDD	Р	Power supply for the entire chip			



Modulation Profile



Specifications

Description		Specification
Fraguency Pange	For 2.5V Supply	6MHz < CLKIN < 12MHz
Frequency Range	For 3.3V Supply	6MHz < CLKIN < 13MHz
Modulation Equation		F _{IN} /256
Frequency Deviation		±1% @ 10MHz

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit			
VDD, V _{IN}	Voltage on any pin with respect to Ground	-0.5 to +4.6	V			
T _{STG}	Storage temperature	-65 to +125	°C			
T _A	Operating temperature	-40 to +85	°C			
Ts	Max. Soldering Temperature (10 sec)	260	°C			
TJ	Junction Temperature	150	°C			
T_DV	Static Discharge Voltage (As per JEDEC STD22- A114-B)	2	KV			
	Note: These are stress ratings only and are not implied for functional use. Exposure to absolute maximum ratings for prolonged periods of time may affect device reliability.					



DC Electrical Characteristics for 2.5V Supply (Test condition: All parameters are measured at room temperature (+25°C) unless otherwise stated)

Symbol	Parameter	Min	Тур	Max	Unit		
V _{IL}	Input low voltage	VSS - 0.3	-	0.8	V		
V_{IH}	Input high voltage	2.0	-	VDD + 0.3	V		
I _{IL}	Input low current	-	-	-35	μA		
I _{IH}	Input high current	-	-	35	μA		
I _{XOL}	XOUT output low current (@0.5V, VDD=2.5V)	-	3	-	mA		
I _{XOH}	XOUT output high current (@1.8V, VDD=2.5V)	-	3	-	mA		
V _{OL}	Output low voltage (VDD = 2.5 V, I _{OL} = 8mA)	-	-	0.6	V		
V _{OH}	Output high voltage (VDD = 2.5 V, I _{OH} = 8mA)	1.8	-	-	V		
I _{DD}	Static supply current *	-	1.0	-	mA		
I _{CC}	Dynamic supply current (2.5V, 10MHz and no load)	-	2.5	-	mA		
VDD	Operating Voltage	2.375	2.5	2.625	V		
t _{ON}	Power-up time (first locked cycle after power-up)	-	-	5	mS		
Z _{OUT}	Output impedance	-	50	-	Ω		
* XIN / CLKIN	* XIN / CLKIN pin is pulled low						

AC Electrical Characteristics for 2.5V Supply

Symbol	Parameter			Тур	Max	Unit
CLKIN	Input frequency		6	-	12	MHz
ModOUT	Output frequency		6	-	12	MHz
f _d	Frequency Deviation	Input Frequency = 6MHz	-	±1.5	-	%
Id		Input Frequency = 12MHz	-	±0.8	-	70
t _{LH} *	Output rise time (measured from	om 0.7V to 1.7V)	0.7	1.4	1.7	nS
t _{HL} *	Output fall time (measured fro	m 1.7V to 0.7V)	0.4	0.9	1.1	nS
t _{JC}	Jitter (cycle to cycle)		-	-	200	pS
t _D	Output duty cycle		45	50	55	%
* t _{LH} and t _{HL} are me	* t _{LH} and t _{HL} are measured into a capacitive load of 15pF					



DC Electrical Characteristics for 3.3V Supply (Test condition: All parameters are measured at room temperature (+25°C) unless otherwise stated)

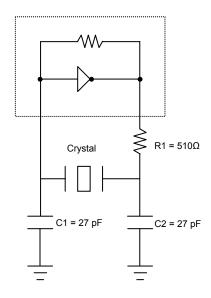
Symbol	Parameter	Min	Тур	Max	Unit		
V_{IL}	Input low voltage	VSS - 0.3	•	0.8	V		
V_{IH}	Input high voltage	2.0	-	VDD + 0.3	V		
I _{IL}	Input low current	-	-	-35	μA		
I _{IH}	Input high current	-	ı	35	μA		
I _{XOL}	XOUT output low current (@0.4V, VDD=3.3V)	-	3	-	mA		
I _{XOH}	XOUT output high current (@2.5V, VDD=3.3V)	-	3	-	mA		
V_{OL}	Output low voltage (VDD = 3.3 V, I _{OL} = 8mA)	-	-	0.4	V		
V _{OH}	Output high voltage (VDD = 3.3 V, I _{OH} = 8mA)	2.5	-	-	V		
I_{DD}	Static supply current *	-	1.1	-	mA		
I _{CC}	Dynamic supply current (3.3V, 10MHz and no load)	-	3.5	-	mA		
VDD	Operating Voltage	2.7	3.3	3.6	V		
t _{ON}	Power-up time (first locked cycle after power-up)	-	-	5	mS		
Z _{OUT}	Output impedance	-	45	-	Ω		
* XIN / CLKIN	* XIN / CLKIN pin is pulled low						

AC Electrical Characteristics for 3.3V Supply

Symbol	Parameter			Тур	Max	Unit
CLKIN	Input frequency		6	-	13	MHz
ModOUT	Output frequency		6	-	13	MHz
f _d	Frequency Deviation	Input Frequency = 6MHz	-	±1.5	-	%
Id	Frequency Deviation	Input Frequency = 13MHz	-	±0.75	-	70
t _{LH} *	Output rise time (measure	ed from 0.8 to 2.0V)	0.4	1.2	1.5	nS
t _{HL} *	Output fall time (measure	ed at 2.0V to 0.8V)	0.4	1.0	1.2	nS
t _{JC}	Jitter (cycle to cycle)	Jitter (cycle to cycle)		-	200	pS
t _D	Output duty cycle		45	50	55	%
*t _{LH} and t _{HL} are measured into a capacitive load of 15pF						



Typical Crystal Oscillator Circuit



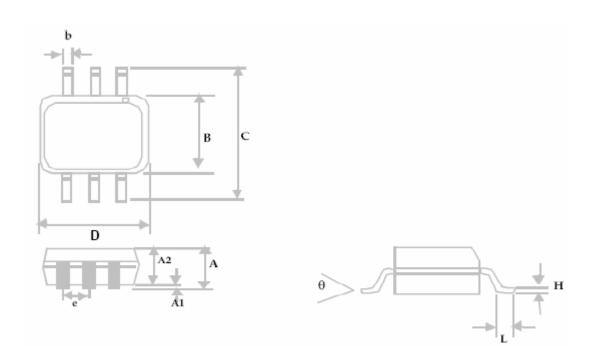
Typical Crystal Specifications

Fundamental AT cut parallel resonant crystal				
Nominal frequency	8.000MHz			
Frequency tolerance	± 50ppm or better at 25°C			
Operating temperature range	-25°C to +85°C			
Storage temperature	-40°C to +85°C			
Load capacitance	18pF			
Shunt capacitance	7pF maximum			
ESR	25 Ω			



rev 1.7
Package Information

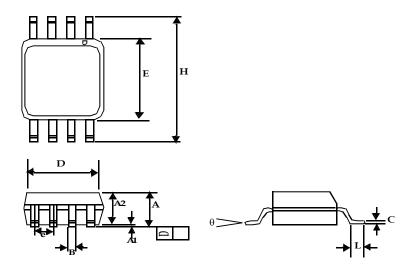
6-pin TSOT-23 Package



	Dimensions				
Symbol	Inc	hes	Millimeters		
	Min	Max	Min	Max	
Α		0.04		1.00	
A1	0.00	0.004	0.00	0.10	
A2	0.033	0.036	0.84	0.90	
b	0.012	0.02	0.30	0.50	
Н	0.005	0.005 BSC		BSC	
D	0.114	BSC	2.90	BSC	
В	0.06	BSC	1.60	BSC	
е	0.0374 BSC		0.950	BSC	
С	0.11 BSC		2.80 BSC		
L	0.0118	0.02	0.30 0.50		
θ	0°	4°	0°	4°	



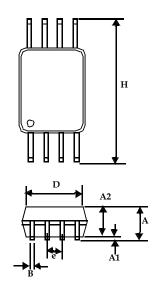
8-Pin SOIC Package

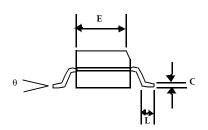


	Dimensions				
Symbol	Inc	hes	Millimeters		
	Min	Max	Min	Max	
A1	0.004	0.010	0.10	0.25	
Α	0.053	0.069	1.35	1.75	
A2	0.049	0.059	1.25	1.50	
В	0.012	0.020	0.31	0.51	
С	0.007	0.010	0.18	0.25	
D	0.193	BSC	4.90	BSC	
Е	0.154 BSC		3.91	BSC	
е	0.050 BSC		1.27	BSC	
Н	0.236	BSC	6.00 BSC		
L	0.016	0.050	0.41	1.27	
θ	0°	8°	0°	8°	



8-Pin TSSOP Package





	Dimensions				
Symbol	Inches		Millimeters		
	Min	Max	Min	Max	
Α		0.043		1.10	
A1	0.002	0.006	0.05	0.15	
A2	0.033	0.037	0.85	0.95	
В	0.008	0.012	0.19	0.30	
С	0.004	0.008	0.09	0.20	
D	0.114	0.122	2.90	3.10	
E	0.169	0.177	4.30	4.50	
е	0.026 BSC		0.65 BSC		
Н	0.252 BSC		6.40 BSC		
L	0.020	0.028	0.50	0.70	
θ	0°	8°	0°	8°	

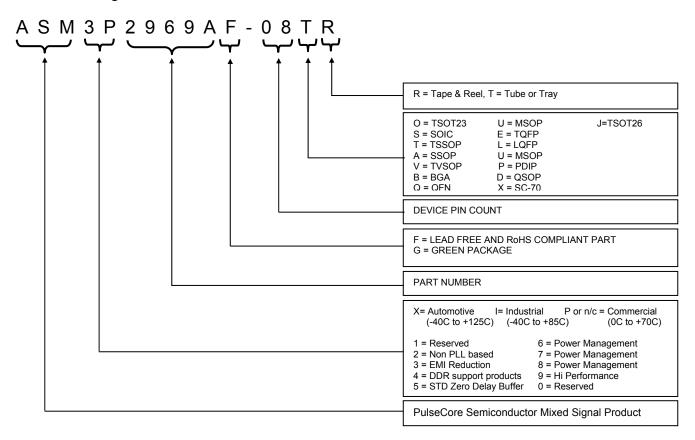


Ordering Information

Part Number	Marking	Package Type	Temperature
ASM3P2969AF-06OR	J4LL	6-Pin TSOT-23, TAPE & REEL, Pb Free	Commercial
ASM3P2969AF-08TT	3P2969AF	8-Pin TSSOP, TUBE, Pb Free	Commercial
ASM3P2969AF-08TR	3P2969AF	8-Pin TSSOP, TAPE & REEL, Pb Free	Commercial
ASM3P2969AF-08ST	3P2969AF	8-Pin SOIC, TUBE, Pb Free	Commercial
ASM3P2969AF-08SR	3P2969AF	8-Pin SOIC, TAPE & REEL, Pb Free	Commercial
ASM3P2969AG-06OR	J3LL	6-Pin TSOT-23, TAPE & REEL, Green	Commercial
ASM3P2969AG-08TT	3P2969AG	8-Pin TSSOP, TUBE, Green	Commercial
ASM3P2969AG-08TR	3P2969AG	8-Pin TSSOP, TAPE & REEL, Green	Commercial
ASM3P2969AG-08ST	3P2969AG	8-Pin SOIC, TUBE, Green	Commercial
ASM3P2969AG-08SR	3P2969AG	8-Pin SOIC, TAPE & REEL, Green	Commercial
ASM3I2969AF-06OR	J5LL	6-Pin TSOT-23, TAPE & REEL, Pb Free	Industrial
ASM3I2969AF-08TT	3I2969AF	8-Pin TSSOP, TUBE, Pb Free	Industrial
ASM3I2969AF-08TR	3I2969AF	8-Pin TSSOP, TAPE & REEL, Pb Free	Industrial
ASM3I2969AF-08ST	3I2969AF	8-Pin SOIC, TUBE, Pb Free	Industrial
ASM3I2969AF-08SR	3I2969AF	8-Pin SOIC, TAPE & REEL, Pb Free	Industrial
ASM3I2969AG-06OR	J6LL	6-Pin TSOT-23, TAPE & REEL, Green	Industrial
ASM3I2969AG-08TT	3I2969AG	8-Pin TSSOP, TUBE, Green	Industrial
ASM3I2969AG-08TR	3I2969AG	8-Pin TSSOP, TAPE & REEL, Green	Industrial
ASM3I2969AG-08ST	3I2969AG	8-Pin SOIC, TUBE, Green	Industrial
ASM3I2969AG-08SR	3I2969AG	8-Pin SOIC, TAPE & REEL, Green	Industrial



Device Ordering Information



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Note: This product utilizes US Patent #6,646,463 Impedance Emulator Patent issued to PulseCore Semiconductor, dated 11-11-2003

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