

May 2008 Giving you the edge PCS2P2309NZ

rev 0.4

#### 3.3V 1:9 Clock Buffer

#### **Features**

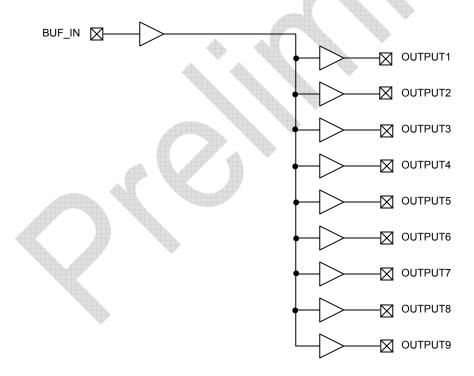
- One-Input to Nine-Output Buffer/Driver
- Buffers all frequencies from DC to 133.33MHz
- Low power consumption for mobile applications Less than 32mA at 66.6MHz with unloaded outputs
- Input-Output delay: 6nS(max)
- Output-output skew less than 250pS
- 16 pin SOIC Package
- Supply Voltage:3.3V±0.3V
- Commercial and Industrial temperature range

#### **Functional Description**

PCS2P2309NZ is a low-cost high-speed buffer designed to accept one clock input and distribute up to nine clocks in mobile PC systems and desktop PC systems. The device operates at 3.3V and outputs can run up to 133.33MHz.

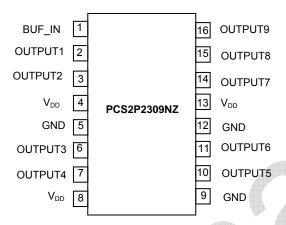
PCS2P2309NZ is designed for low EMI and power optimization and consumes less than 32mA at 66.6MHz, making it ideal for the low-power requirements of mobile systems. It is available in an 16 pin SOIC Package over Commercial and Industrial temperature range.

#### **Block Diagram**





# **Pin Configuration**



# **Pin Description**

Pin#	Pin Name	Description
4, 8, 13	$V_{DD}$	3.3V Digital Voltage Supply
5, 9, 12	GND	Ground
1	BUF_IN	Input Clock
2, 3, 6, 7, 10, 11, 14, 15, 16	OUTPUT [1:9]	Outputs

# **Absolute Maximum Ratings**

Parameter	Min	Max	Unit
Supply Voltage to Ground Potential	-0.5	+4.6	V
DC Input Voltage (Except REF)	-0.5	V <sub>DD</sub> + 0.5	V
DC Input Voltage (REF)	-0.5	7	V
Storage Temperature	-65	+150	°C
Max. Soldering Temperature (10 sec)		260	°C
Junction Temperature		150	°C
Static Discharge Voltage (As per JEDEC STD22- A114-B)		2000	V
Note: These are stress ratings only and functional usage is not implied. Exposure to absolu affect device reliability.	te maximum ratings for prolong	ed periods can	•

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## **Operating Conditions**

Parameter	Description	Min	Max	Unit
$V_{DD}$	Supply Voltage	3.0	3.6	V
T <sub>A</sub>	Commercial Temp.	0	70	°C
I A	Industrial Temp.	-40	85	°C
CL	Load Capacitance, Fout < 100MHz		30	pF
OL.	Load Capacitance,100MHz < Fout < 133.33MHz	4	15	pF
C <sub>IN</sub>	Input Capacitance		7	pF
BUF_IN, OUTPUT [1:9]	Operating Frequency	DC	133.33	MHz
t <sub>PU</sub>	Power-up time for all $V_{\text{DD}}$ 's to reach minimum specified voltage (power ramps must be monotonic)	0.05	50	mS

### **Electrical Characteristics for Commercial and Industrial Temperature Devices**

Symbol	Parameter		Test Conditions	Min	Max	Unit
V <sub>IL</sub>	Input LOW Voltage <sup>1</sup>				0.8	V
V <sub>IH</sub>	Input	HIGH Voltage <sup>1</sup>		2.2		V
I <sub>IL</sub>	Input LOW Current		V <sub>IN</sub> = 0V		50.0	μA
I <sub>IH</sub>	Input HIGH Current		$V_{IN} = V_{DD}$		100.0	μA
$V_{OL}$	Output LOW Voltage <sup>2</sup>		I <sub>OL</sub> = 12 mA		0.4	V
V <sub>OH</sub>	Output HIGH Voltage <sup>2</sup>		I <sub>OH</sub> = -12 mA	2.4		٧
I	Supply	Commercial temp.	Unloaded outputs at 66.66MHz		30	mA
I <sub>DD</sub>	Current	Industrial temp.	Officaced outputs at 60.000VITZ		32	IIIA

# Switching Characteristics for Commercial and Industrial Temperature Devices<sup>3</sup>

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
t <sub>3</sub>	Rise Time <sup>2</sup>	Measured between 0.8V and 2.0V		1.5	2	nS
t <sub>4</sub>	Fall Time <sup>2</sup>	Measured between 2.0V and 0.8V		1.5	2	nS
t <sub>D</sub>	Duty Cycle <sup>2</sup> = t <sub>2</sub> ÷t <sub>1</sub>	Measured at 1.4V (For an Input Clock Duty Cycle 50%)	45	50	55	%
t <sub>5</sub>	Output to Output Skew <sup>2</sup>	All outputs equally loaded			±250	pS
t <sub>6</sub>	Propagation Delay, BUF_IN Rising Edge to OUTPUT Rising Edge <sup>2</sup>	Measured at V <sub>DD</sub> /2		4	6	nS

- Note:

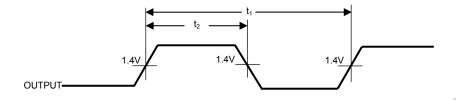
  1. BUF\_IN input has a threshold voltage of V<sub>DD</sub>/2.

  2. Parameter is guaranteed by design and characterization. It is not 100% tested in production.
- 3. All parameters specified with loaded outputs.

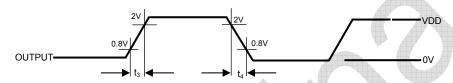


# **Switching Waveforms**

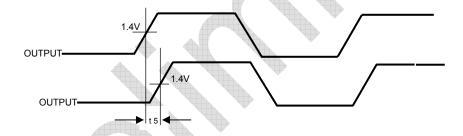
# **Duty Cycle Timing**



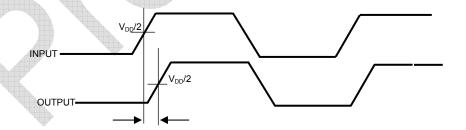
# All Outputs Rise/Fall Time



# **Output-Output Skew**

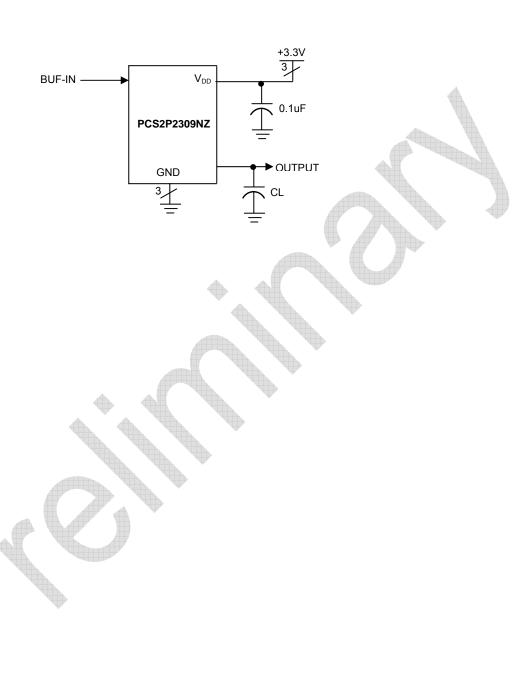


# **Input-Output Propagation Delay**





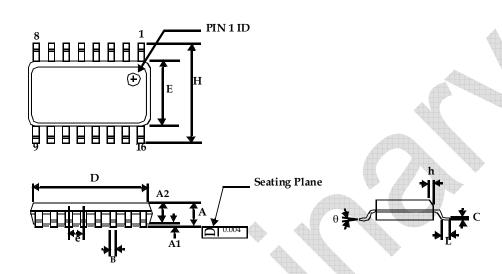
**Test Circuit** 





# **Package Information**

# 16-lead (150 Mil) Molded SOIC



	Dimensions				
Symbol	Inches		Millimeters		
	Min	Max	Min	Max	
Α	0.053	0.069	1.35	1.75	
A1	0.004	0.010	0.10	0.25	
A2	0.049	0.059	1.25	1.50	
В	0.013	0.022	0.33	0.53	
С	0.008	0.012	0.19	0.27	
D	0.386	0.394	9.80	10.01	
Е	0.150	0.157	3.80	4.00	
е	0.050 BSC 1.27 BSC		BSC		
Н	0.228	0.244	5.80	6.20	
h	0.010	0.016	0.25	0.41	
L	0.016	0.035	0.40	0.89	
θ	0°	8°	0°	8°	



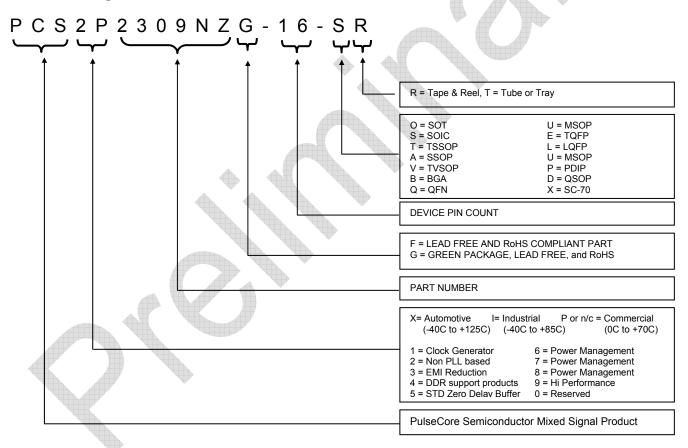
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### **Ordering Code**

Part Number	Marking	Package Type	Temperature
PCS2P2309NZF-16-ST	2P2309NZF	16-pin 150-mil SOIC, Pb Free	Commercial
PCS2P2309NZF-16-SR	2P2309NZF	16-pin 150-mil SOIC, Tape and Reel, Pb Free	Commercial
PCS2I2309NZF-16-ST	2I2309NZF	16-pin 150-mil SOIC, Pb Free	Industrial
PCS2I2309NZF-16-SR	2I2309NZF	16-pin 150-mil SOIC, Tape and Reel, Pb Free	Industrial
PCS2P2309NZG-16-ST	2P2309NZG	16-pin 150-mil SOIC, Green	Commercial
PCS2P2309NZG-16-SR	2P2309NZG	16-pin 150-mil SOIC, Tape and Reel, Green	Commercial
PCS2I2309NZG-16-ST	2I2309NZG	16-pin 150-mil SOIC, Green	Industrial
PCS2I2309NZG-16-SR	2I2309NZG	16-pin 150-mil SOIC ,Tape and Reel, Green	Industrial

### **Device Ordering Information**



Licensed under US patent #5,488,627, #6,646,463 and #5,631,920.



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Note: This product utilizes US Patent #6,646,463 Impedance Emulator Patent issued to PulseCore Semiconductor, dated 11-11-2003 Many PulseCore Semiconductor products are protected by issued patents or by applications for patent

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