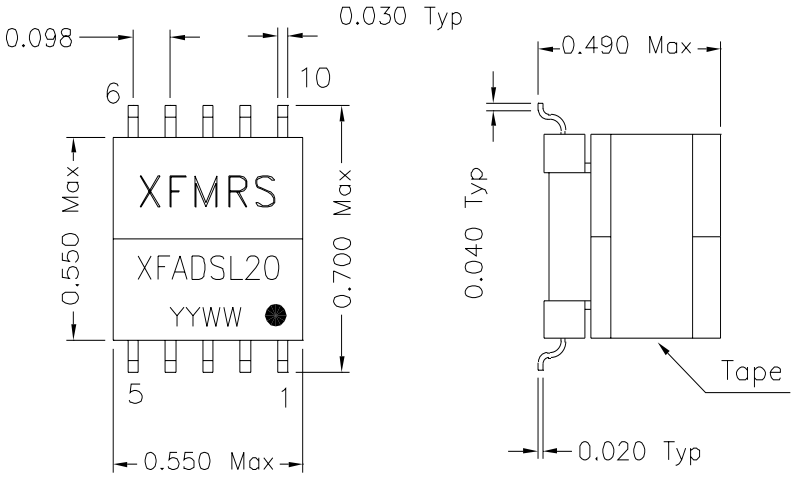
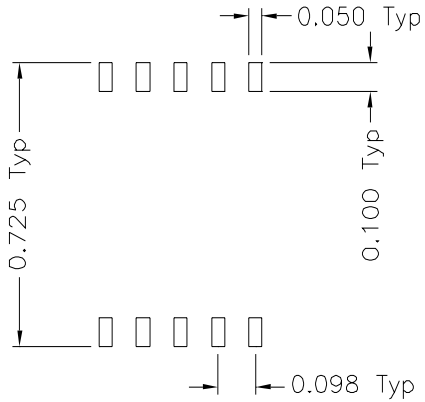
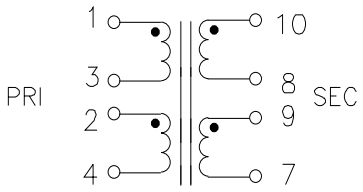
1. Dimensions:





Suggested PCB Layout

2.Schematic:



3.Electrical Specifications: @25°C

Turns Ratio: (P1-4):(P10-7) = 1:1 ±2% (TIE P3-2,P8-9)

PIN1-4 OCL: 100uH ±7% @10KHz 0.02V TIE P2-3 Ls

PIN1-4 LL: 4.0uH Max @100KHz 0.02V
(CONNECT PIN3-2, PIN8-9,PIN10-7) Ls

CW/W: PIN1-10 20pF Max. @100KHz 0.02V (TIE P3-2,P8-9) Cs

Cd: Pin1-4 20pF Max (tie 2-3 & 8-9)

DC Res.: P1-4 1.0 Ohms Max TIE P3-2
P10-7 1.0 Ohms Max TIE P8-9

Frequency Response: P1-4,P10-7 3.0 dB Max. 100KHz-1.1MHz
100 Ohms tie P3-2,P8-9

Insertion Loss: 0.5dB Max @300KHz

THD: -80dB Min. @100KHz 4.0V

Longitudinal Balance: 46dB Min.@1KHz-1.0MKHz

HIPOT: 2000VAC for 1 second between P10-1(Tie P8-9,P2-3)

Designed to meet UL1950 requirements for Supplementary Insulation with 250 working volts.

DOC. REV A/2

XFMRS Inc	Title: TRANSFORMER		
UNLESS OTHERWISE SPECIFIED TOLERANCES: .xxx ±0.010 Dimensions in INCH	P/N: XFADSL20		REV. A
	DWN.	廣玉坤	Feb-29-00
	CHK.	李清 IL	Feb-29-00
SHEET 1 OF 1	APP.	Joe Huff	Feb-29-00