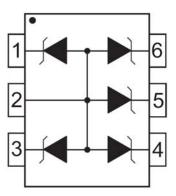
**ROHS** 



# Feature

- 100W peak pulse power per line ( $t_P = 8/20\mu S$ )
- SOT-563 package
- Protects four bidirectional lines and five Unidirectional lines
- Monolithic structure
- Working voltage: 5V
- Low clamping voltage
- ESD protection > 40 KV
- Low leakage current
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD)  $\pm$ 15KV(air),  $\pm$ 8KV(contact); IEC 61000-4-4 (EFT) 40A (5/50ns)



## **Applications**

- Communication systems & Cellular phones
- Printers
- Notebook and hand hold computers
- PDAs
- Video Equipment

# Electrical characteristics per line@25°C (unless otherwise specified) note1

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Reverse stand-off voltage	$V_{RWM}$				5	V
Reverse Breakdown voltage	$V_{BR}$	I <sub>t</sub> = 1mA	6			V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> = 5V T=25°C			5	μΑ
Clamping Voltage	V <sub>C</sub>	$I_{PP} = 1A$ $t_P = 8/20 \mu S$			8.8	V
Clamping Voltage	V <sub>C</sub>	I <sub>PP</sub> =10A t <sub>P</sub> = 8/20μ S			10.0	V
Junction Capacitance	C <sub>j</sub>	V <sub>R</sub> =0V f = 1MHz		40		pF

# Absolute maximum rating @25℃ note1

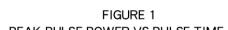
Rating	Symbol	Value	Units
Peak Pulse Power (t <sub>p</sub> =8/20μ S )	$P_{pp}$	100	W
Forward voltage@10mA	$V_{F}$	1.5	V
Operating Temperature	TJ	-55 to +150	$^{\circ}\! \mathbb{C}$
Storage Temperature	T <sub>STG</sub>	-55 to +150	$^{\circ}$ C

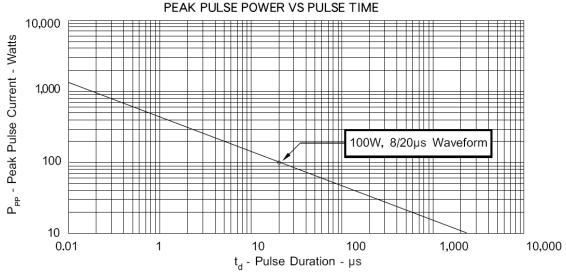
Note1: Pin 1, 3, 4, 5 or 6 to Pin 2

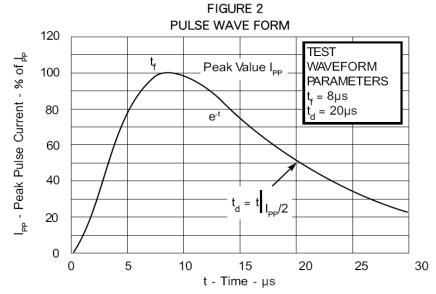


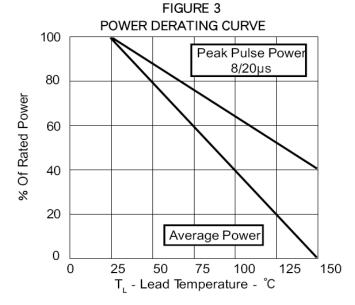


## **Typical Characteristics**





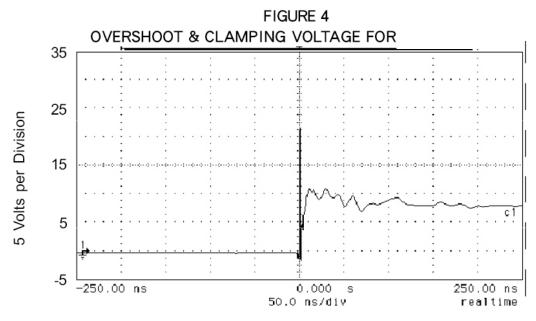


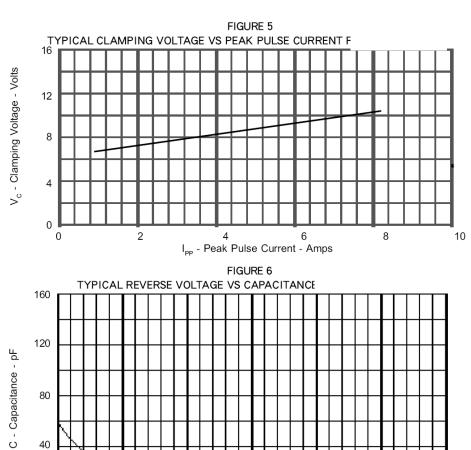






# **Typical Characteristics**





 $^{2}$   $^{3}$   $^{4}$  Neverse Voltage - Volts

5

6

0

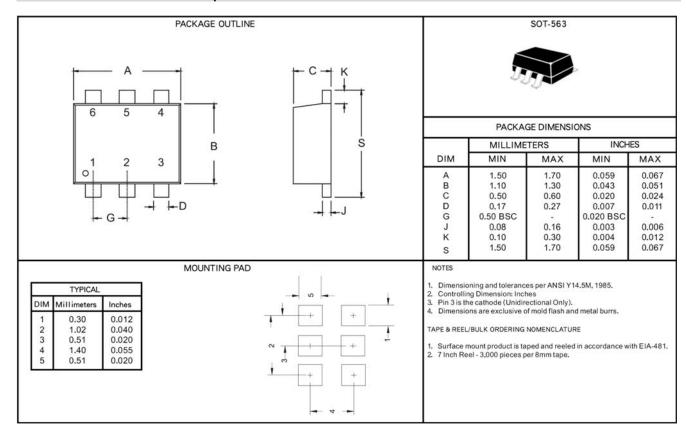
0

1



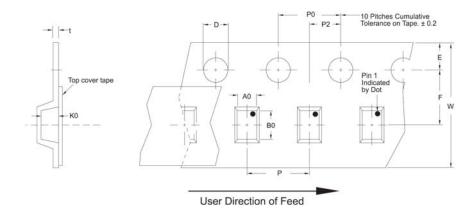


## Product dimension and pad size



#### Tape & Reel Specifications (Dimensions in millimeters)

Reel Dia.	Tape Width	A0	В0	K0	D	E	F	W	P0	P2	Р	tmax
178mm (7")	8mm	1.78 ± 0.05	1.78± 0.05	0.69± 0.05	1.50 ± 0.10	1.75±0.10	3.50 ± 0.05	8.00±0.30	4.00±0.10	2.00 ± 0.05	4.00±0.10	0.25





## Application note

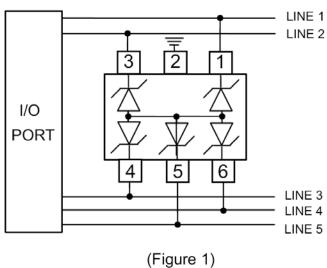
The sessvt563-6 Series is TVS arrays designed to protect I/O or data lines from the damaging effects of ESD or EFT. This product provides both unidirectional and bidirectional protection, with a surge capability of 100 Watts Ppp per line for an 8/20µs wave shape and ESD protection > 25 kilovolts.

### COMMON-MODE UNIDIRECTIONAL CONFIGURATION (Figure 1)

The SESSVT563-6 Series provides up to 5 lines of protection in a common-mode unidirectional configuration as depicted in Figure 1.

Circuit connectivity is as follows:

- Line 1 is connected to Pin 1.
- Line 2 is connected to Pin 3.
- Line 3 is connected to Pin 4.
- · Line 4 is connected to Pin 6.
- Line 5 is connected to Pin 5.
- Pin 2 is connected to ground.

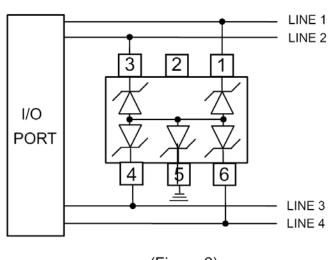


### COMMON-MODE BIDIRECTIONAL CONFIGURATION (Figure 2)

The SES5VT563-6 Series provides up to 4 lines of protection in a common-mode bidirectional configuration as depicted in Figure 2.

Circuit connectivity is as follows:

- Line 1 is connected to Pin 1.
- Line 2 is connected to Pin 3.
- Line 3 is connected to Pin 4.
- Line 4 is connected to Pin 6.
- · Pin 2 is not connected.
- Pin 5 is connected to ground.



(Figure 2)

SES Series

ESD Protector

SES5VT563-6

ROHS

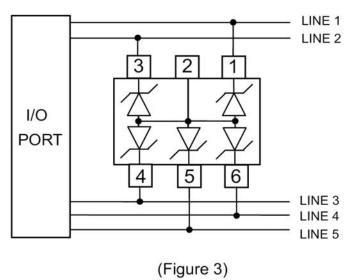
## Application note

DIFFERENTIAL-MODE BIDIRECTIONAL CONFIGURATION (Figure 3)

The **(SES5VT563-6** Series provides up to 5 lines of protection in a differential-mode bidirectional configuration as depicted in Figure 3.

Circuit connectivity is as follows:

- · Line 1 is connected to Pin 1.
- Line 2 is connected to Pin 3.
- · Line 3 is connected to Pin 4.
- Line 4 is connected to Pin 6.
- Line 5 is connected to Pin 5.
- · Pin 2 is not connected.



### Circuit board layout and protection device placement:

Circuit board layout is critical for the suppression of ESD transients.

The following guidelines are recommended:

- 1. Place the protection device as close to the input terminal or connector as possible.
- 2. The path length between the protection device and the protected line should be minimized.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protection conductors in parallel with unprotected conductor.
- 5. Minimize all printed-circuit board conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- Ground planes should be used whenever possible. For multilayer printed-circuit boards, use ground vias.



GOOD-ARK

ESD Protector

SES Series

ROHS

# **Revision History**

Revision	Date	Changes
1.0	2008-7-3	-