# MADS-001317-1320AG



## GaAs Solder Bump Flip Chip Schottky Diode

M/A-COM Products Rev. V2

#### **Features**

- Low Series Resistance, 4 Ω
- Low Capacitance, 45 fF
- High Cutoff Frequency
- Silicon Nitride Passivation
- Polyimide Scratch Protection
- Solderable Bump Die Attach

### Description

M/A-COM's MADS-001317-1320AG is a Gallium Arsenide Flip-Chip Schottky diode with solder bumps. These devices are fabricated on OMCVD epitaxial wafers using a process designed for high device uniformity and extremely low parasitics. This device can be used up to 80 GHz. This diode is fully passivated with silicon nitride and has an additional layer of a polymer for scratch protection. The protective coatings prevent damage to the junction during handling and circuit attachment.

## **Applications**

The high cutoff frequency of this device allows use through millimeter wave frequencies. Typical Applications include single and double balanced mixers in PCN transceivers, radios, police radar detectors and automotive radar detectors.



**Mounting Side with Solder Bumps** 

# Electrical Specifications T<sub>A</sub> = 25°C

Parameters and Test Conditions	Symbol	Units	Min.	Тур.	Max.
Junction Capacitance at 0V at 1 MHz	Cj	pF	-	0.020	_
Total Capacitance at 0V at 1 MHz <sup>1</sup>	Ct	pF	.030	0.045	0.060
Forward Voltage at 1mA	Vf	Volts	.60	.70	.80
Dynamic Resistance at 9.5 - 10.5 mA	Rd	Ohms	_	4	7
Reverse Breakdown Voltage at 10 μA	Vb	Volts	4.5	7	_

<sup>1.</sup> Total Capacitance is equivalent to the sum of junction capacitance (Cj) and parasitic capacitance (Cp).

<sup>•</sup> Europe Tel: 44.1908.574.200 / Fax: 44.1908.574.300

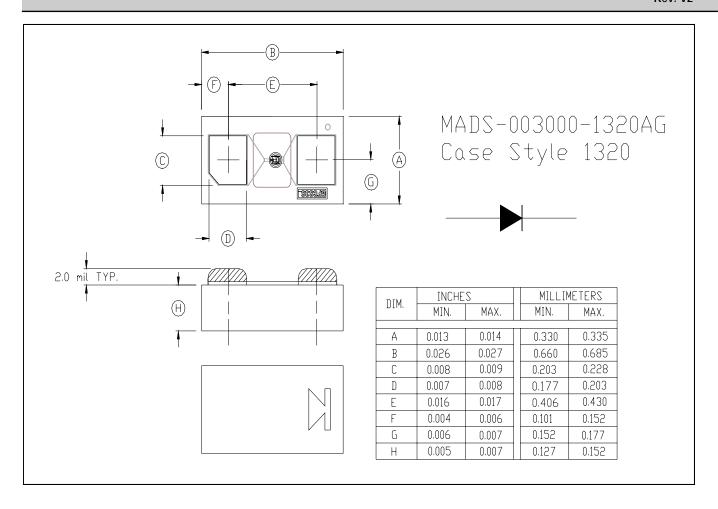
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 Visit www.macom.com for additional data sheets and product information.

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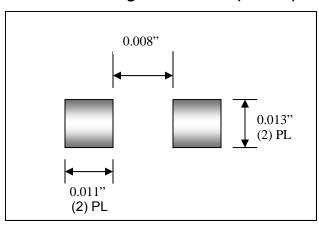


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## **Circuit Mounting Dimensions (Inches)**



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  PRELIMINARY: Data Sheets contain information regarding a product M/A-COM has under development. Performance is based on engineering tests. Specifications are typical. Mechanical outline has been fixed. Engineering samples and/or test data may be available. Commitment to produce in volume is not guaranteed.
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#### **Device Installation Procedures**

The following guidelines should be observed to avoid damaging GaAs Flip-Chips.

#### Cleanliness

These devices should be handled in a clean environment. Do not attempt to clean die after installation.

### **Static Sensitivity**

Gallium Arsenide Schottky diodes are ESD sensitive and can be damaged by static electricity. Since Schottky diodes are rated as Class 0, proper ESD techniques should be used when handling these devices.

### **General Handling**

These devices have a polymer layer which provides scratch protection for the junction area and the anode air bridge. Die can be handled with plastic tweezers or picked and placed automatically with a #27 tip vacuum pencil.

Assembly Requirements using Tin Lead Solder

This Flip Chip Diode employs a 6  $\mu$ m thick, Sn 63/Pb 37 Solderable interface as part of the 50  $\mu$ m high solder bump. These chips are designed to be soldered onto hard or soft substrates with the junction side down. They should be mounted onto silkscreened circuits using 60/40 Sn/Pb solder paste. A typical profile for a Sn 63/Pb 37soldering process is provided in <u>Application Note</u>, <u>M538 Surface Mounting Instructions</u> on the M/A-COM web-site <u>www.macom.com</u>

### **Typical Spice Parameters**

Is (A)	Rs (Ω)	N	Ct0 (pF)	М	lk (A)	Vj (V)	FC	BV (V)	IBV (A)
1.7 E-14	4.6	1.08	.047	.38	.016	.86	.99	7	1.0 E-5

# Absolute Maximum Ratings @ 25°C 2

Parameter	Maximum Ratings
Operating Temperature	-65°C to +125°C
Storage Temperature	-65°C to +150°C
Incident LO Power	+20 dBm
Incident RF Power	+20 dBm
Mounting Temperature	+300°C for 10 seconds.

<sup>2.</sup> Exceeding these limits may cause permanent damage.

### **Ordering Information**

Part Number	Packaging
MADS-001317-1320AG	Gel Pack

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